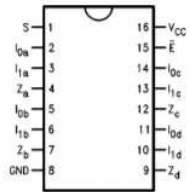
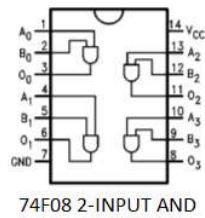


Connection Diagram

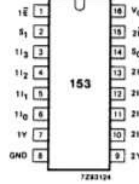


74F157A 2-input multiplexer

Connection Diagram



74F08 2-INPUT AND



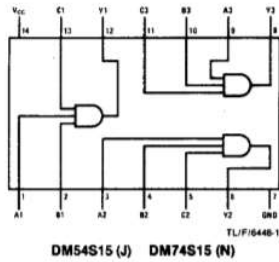
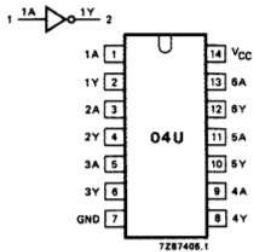
74HCT153 4-Input multiplexer

Fig.1 Pin configuration.



Fig.2 Logic symbol.

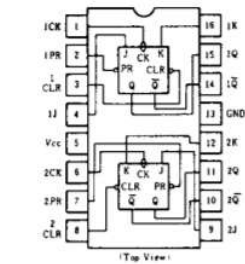
74HCU04N Hex Inverter



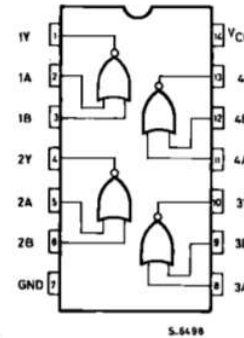
DM54S15 (J) DM74S15 (N)

HD74LS76A J-K Flip-Flop

PIN ARRANGEMENT



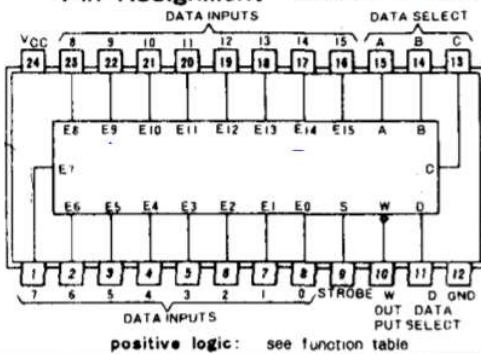
M74HC0281 NOR GATE



S-8498

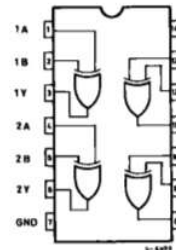
Pin Assignment

HD74150P 16-1 MUX

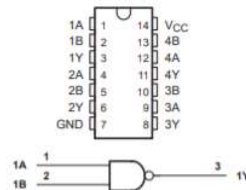


positive logic: see function table

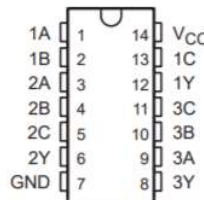
M74HC8681 Exclusive OR



SN74F00 ... D OR N PACKAGE (TOP VIEW)



SN54F27 ... J PACKAGE SN74F27 ... D OR N PACKAGE (TOP VIEW)



SN54ALS32, SN54AS32 ... J PACKAGE SN74ALS32, SN74AS32 ... D OR N PACKAGE (TOP VIEW)

