CACHE COHERENCE PROTOCOLS ANALYZER

A tool for analyzing how different Snooping based Cache Coherence Protocols perform under varying workloads

15-618 SPRING 2017 FINAL PROJECT

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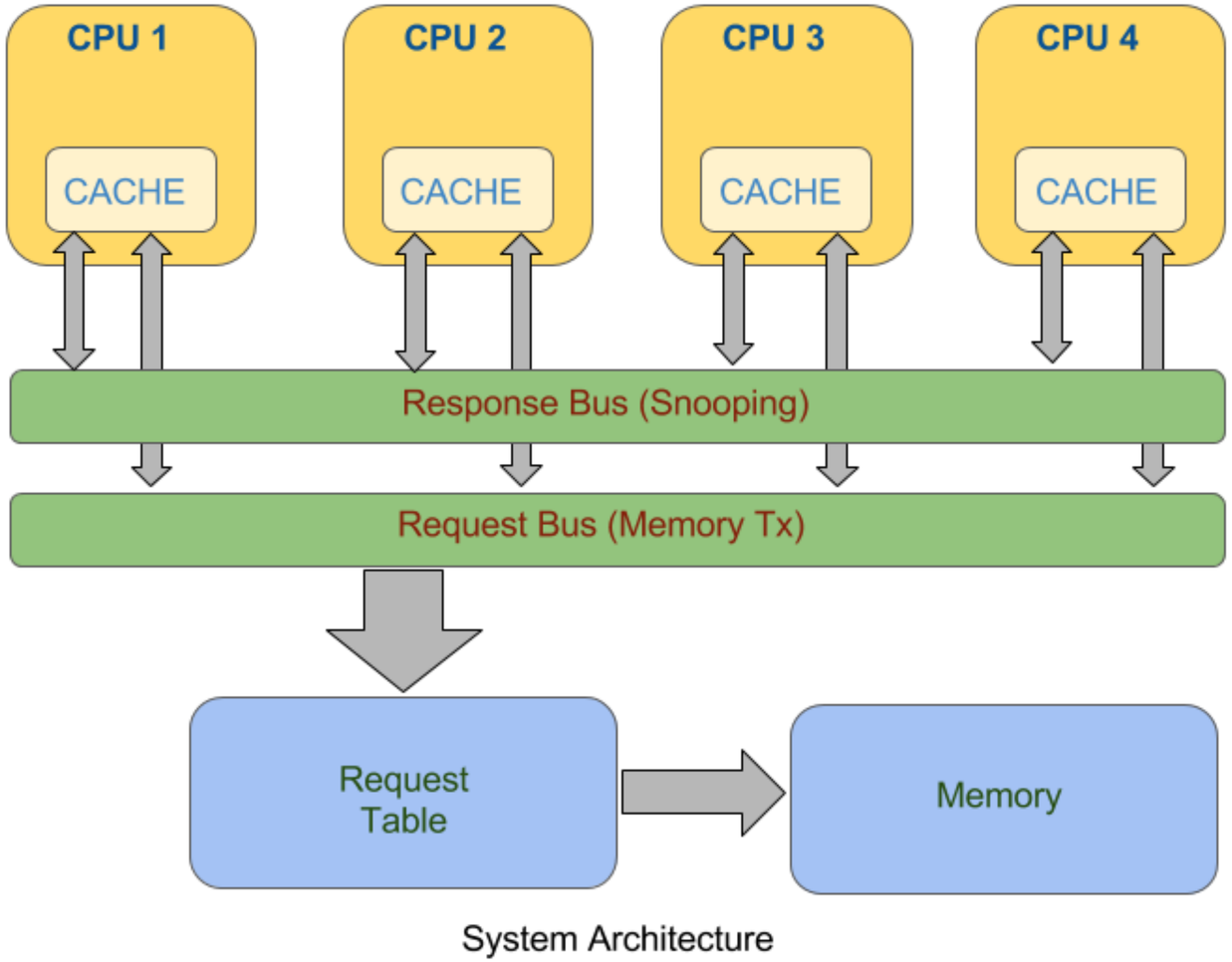
WHAT DID WE MAKE?

- Cache Simulator
  - Generate memory traces
  - Analyze the memory traces
    - Multiple snooping based cache protocol
    - Mimic real world system behavior

WHAT ARE WE TRYING TO SOLVE?

- Have a problem to solve
  - Need to design a system to solve it
    - What kind of cache coherence protocol can we use?
    - Our tool will help you decide!

SYSTEM DESIGN



System Architecture

PROTOCOLS

- Write Invalidate Protocols
  - MSI
  - MESI
  - MOSI
  - MOESI
- Write-Update Protocol
  - Dragon
- Hybrid Protocol
  - Competitive Snooping (!)

HOW DO WE COMPARE THE PROTOCOLS?

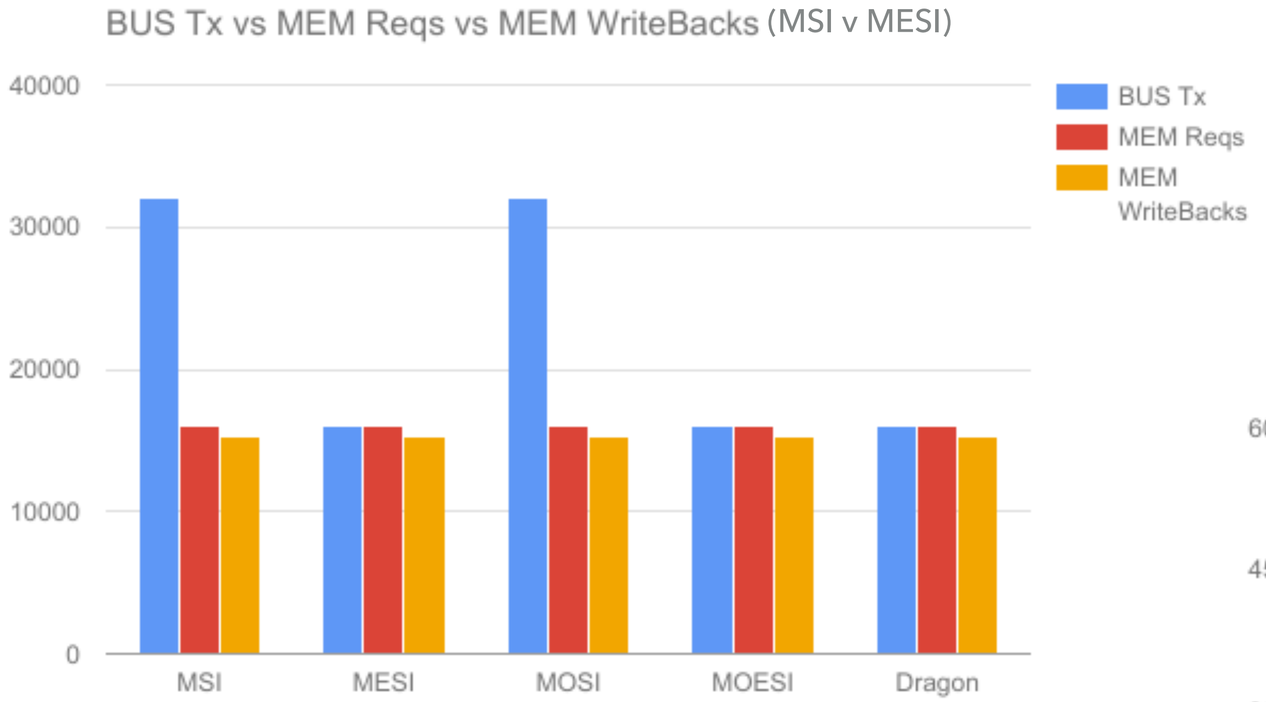
- Number of Bus Transactions
- Number of Memory Requests
- Number of Memory Write-Backs
- Number of Cache to Cache Transfers

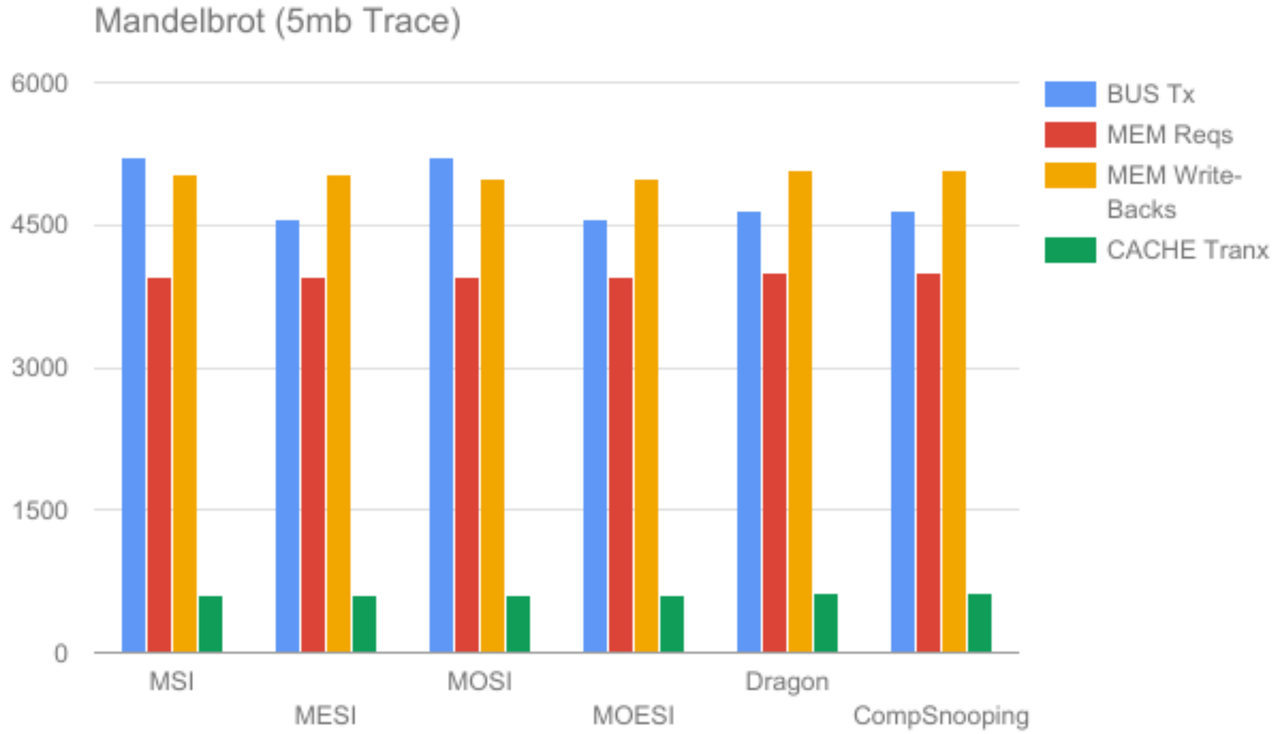
HOW DO WE GENERATE THE MEMORY TRACE?

- Intel Pin-tool
  - Memory trace of a program
    - Problem?
      - Relevance of functions
      - HUGE!
    - Solution
      - Dummy functions

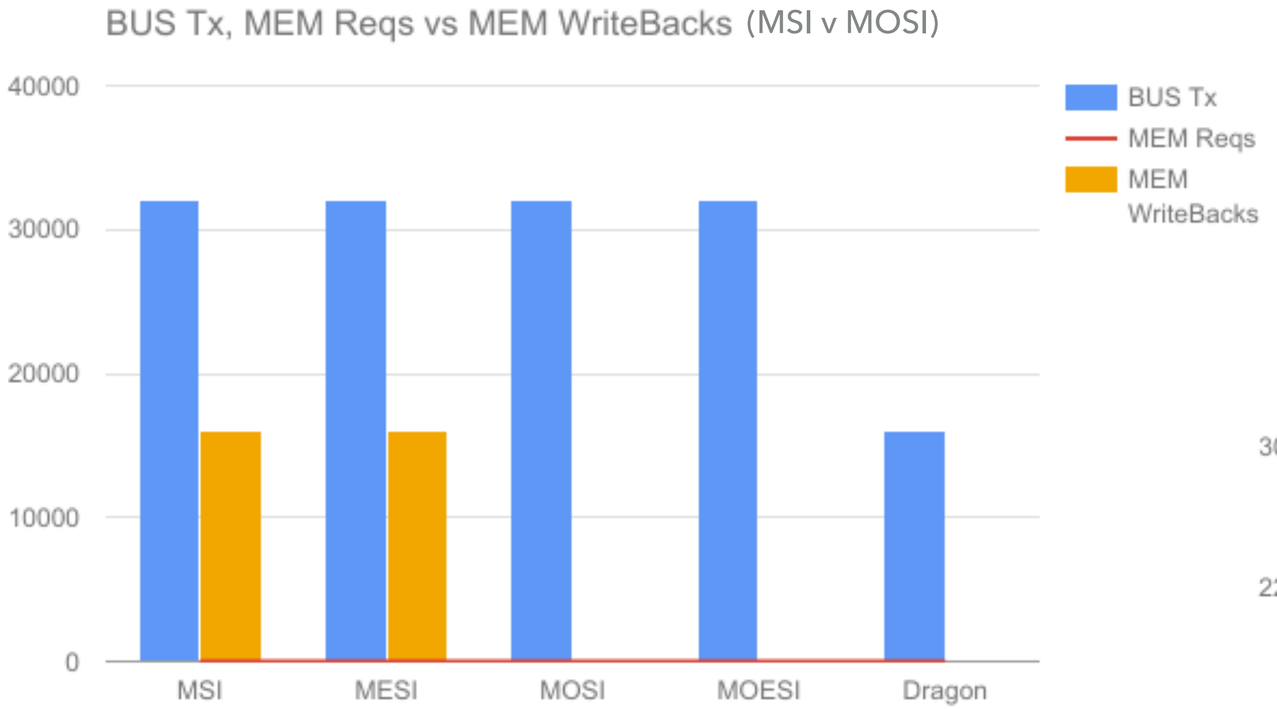
WHAT DID THE TOOL HELP US ANALYZE?

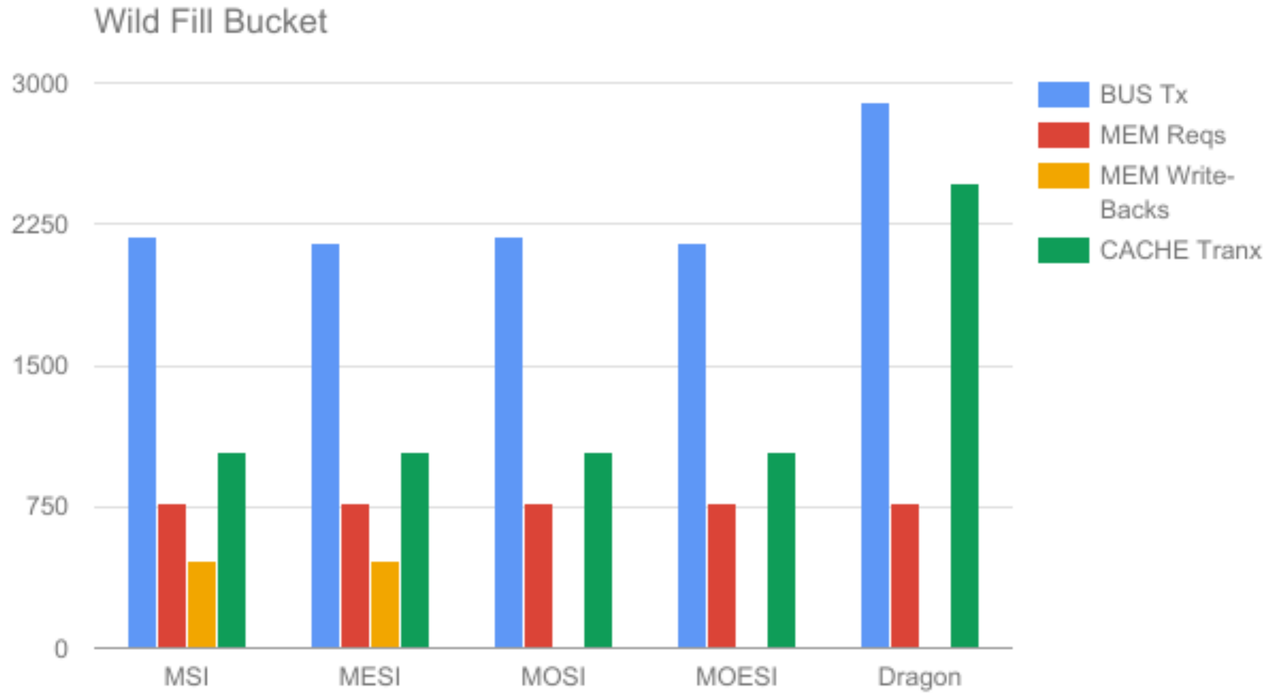
BENEFIT OF ‘E’ STATE



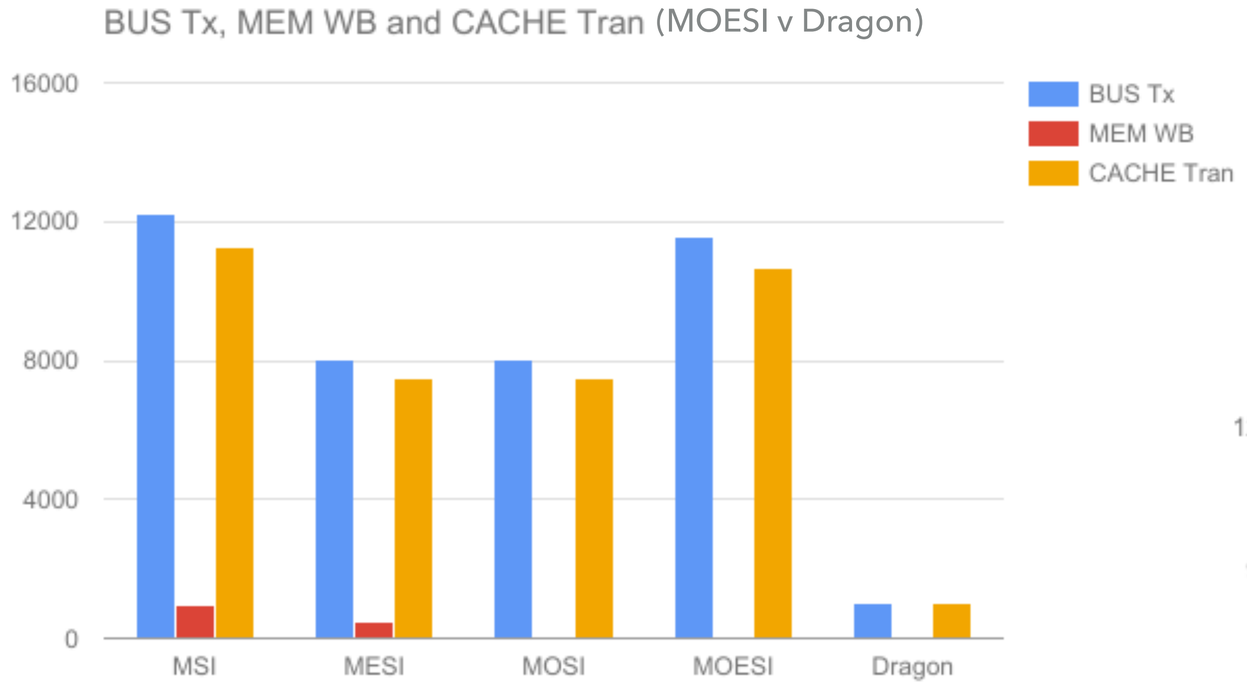


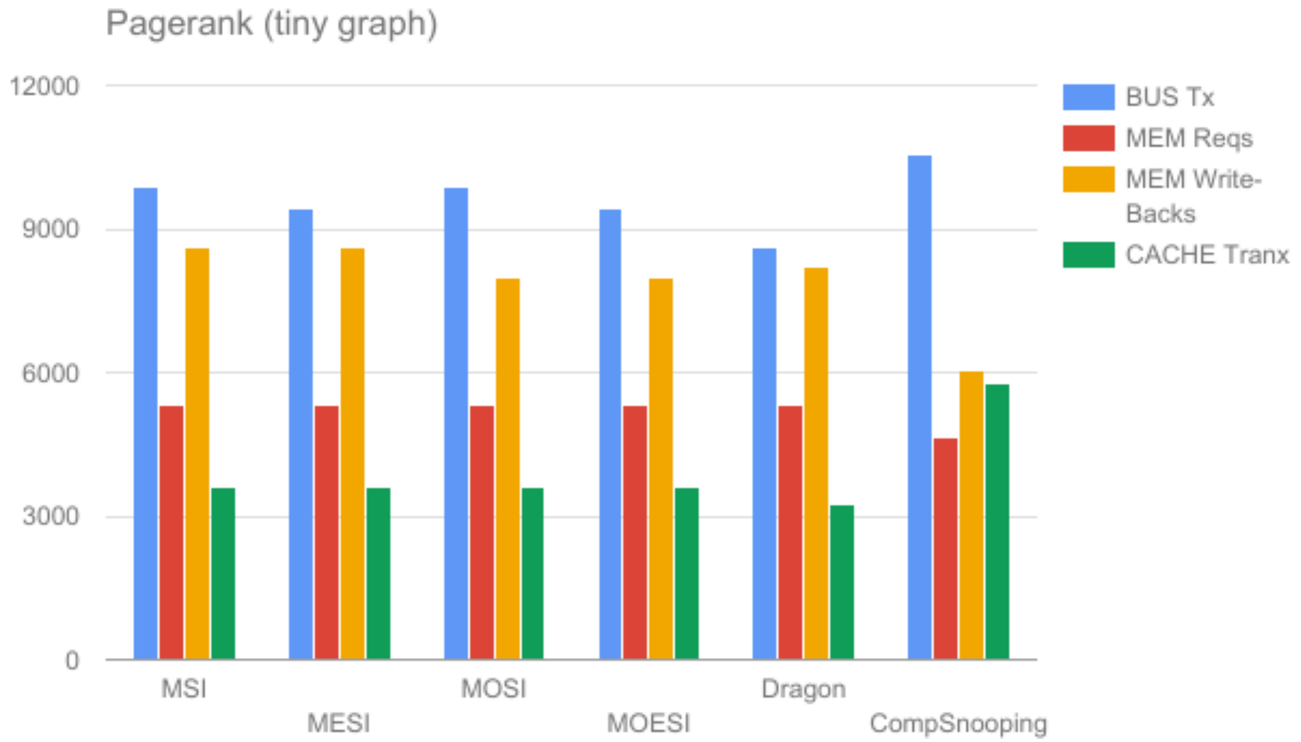
BENEFIT OF ‘O’ STATE



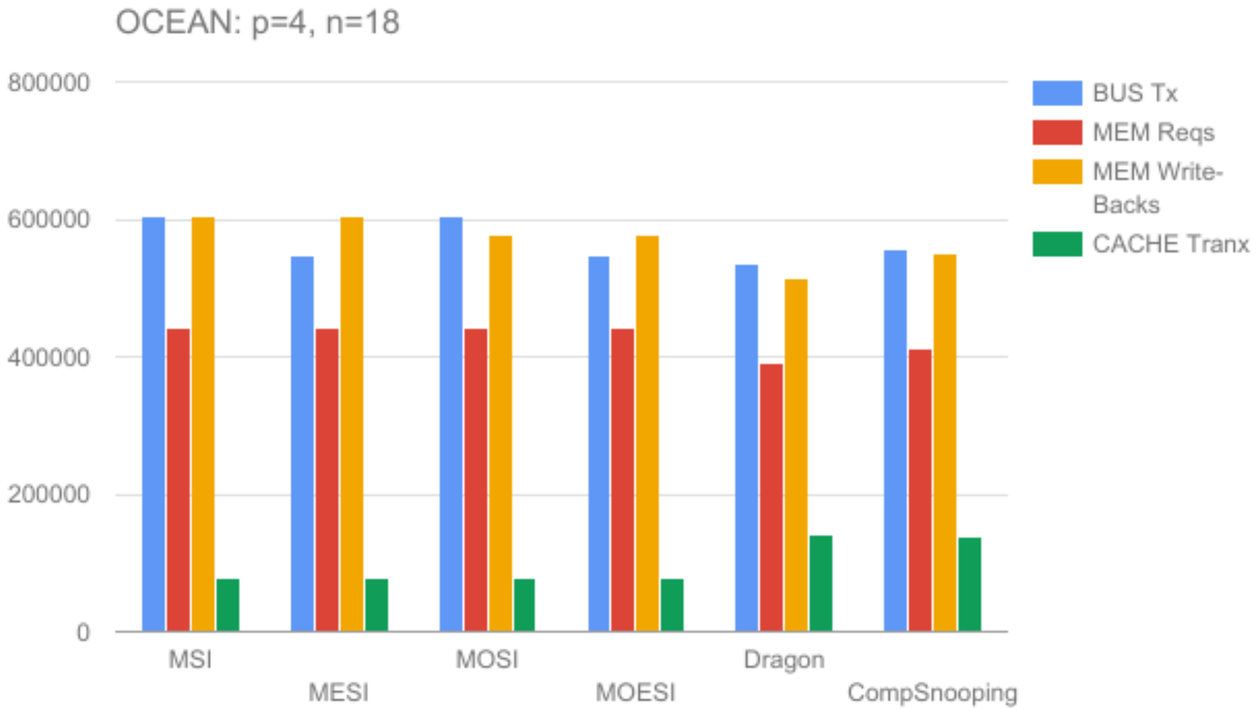


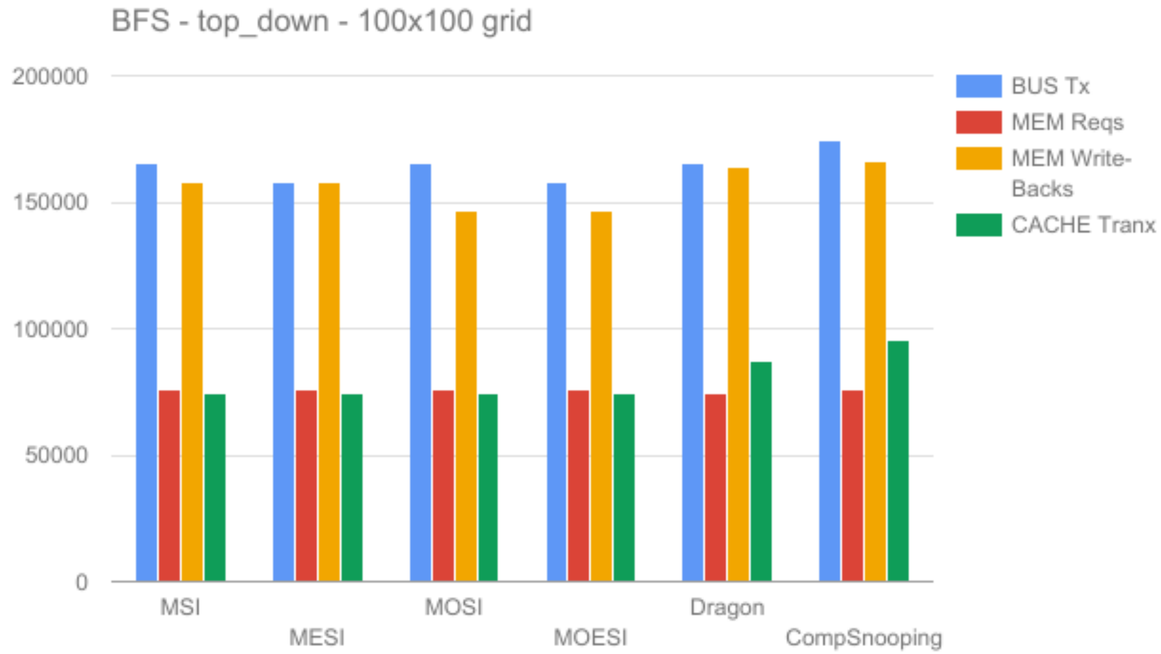
WRITE INVALIDATE vs WRITE UPDATE





COMPETITIVE SNOOPING (!)

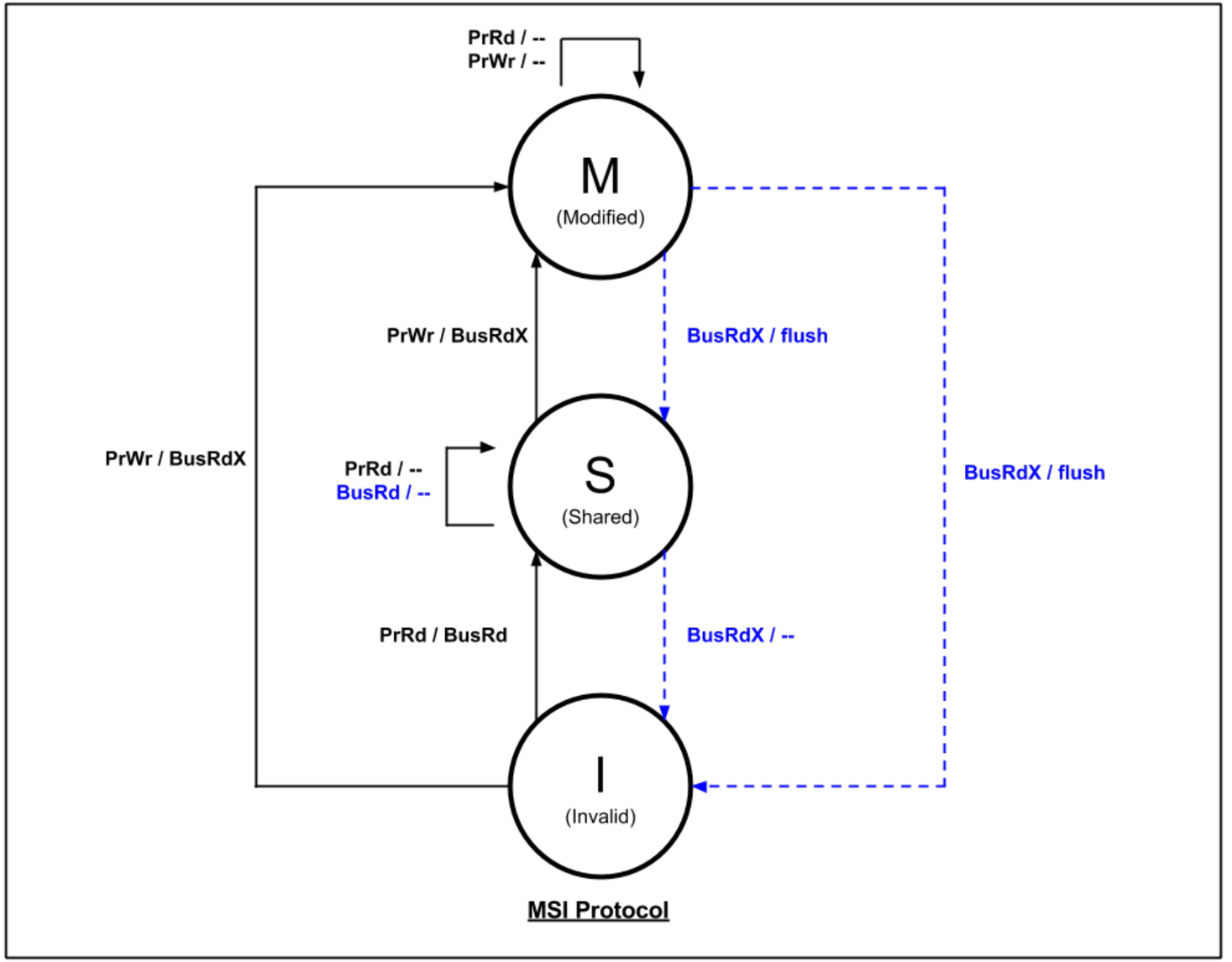


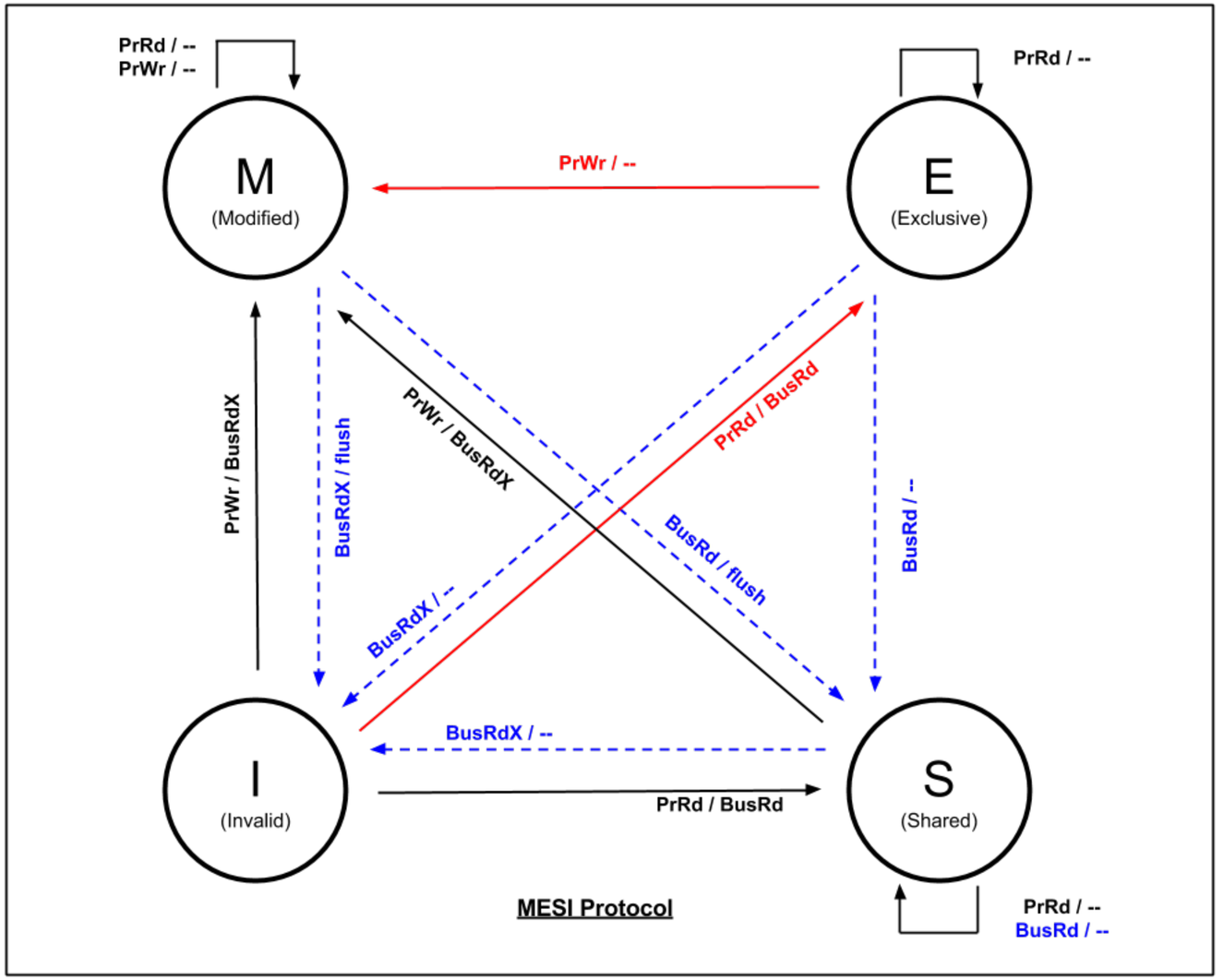


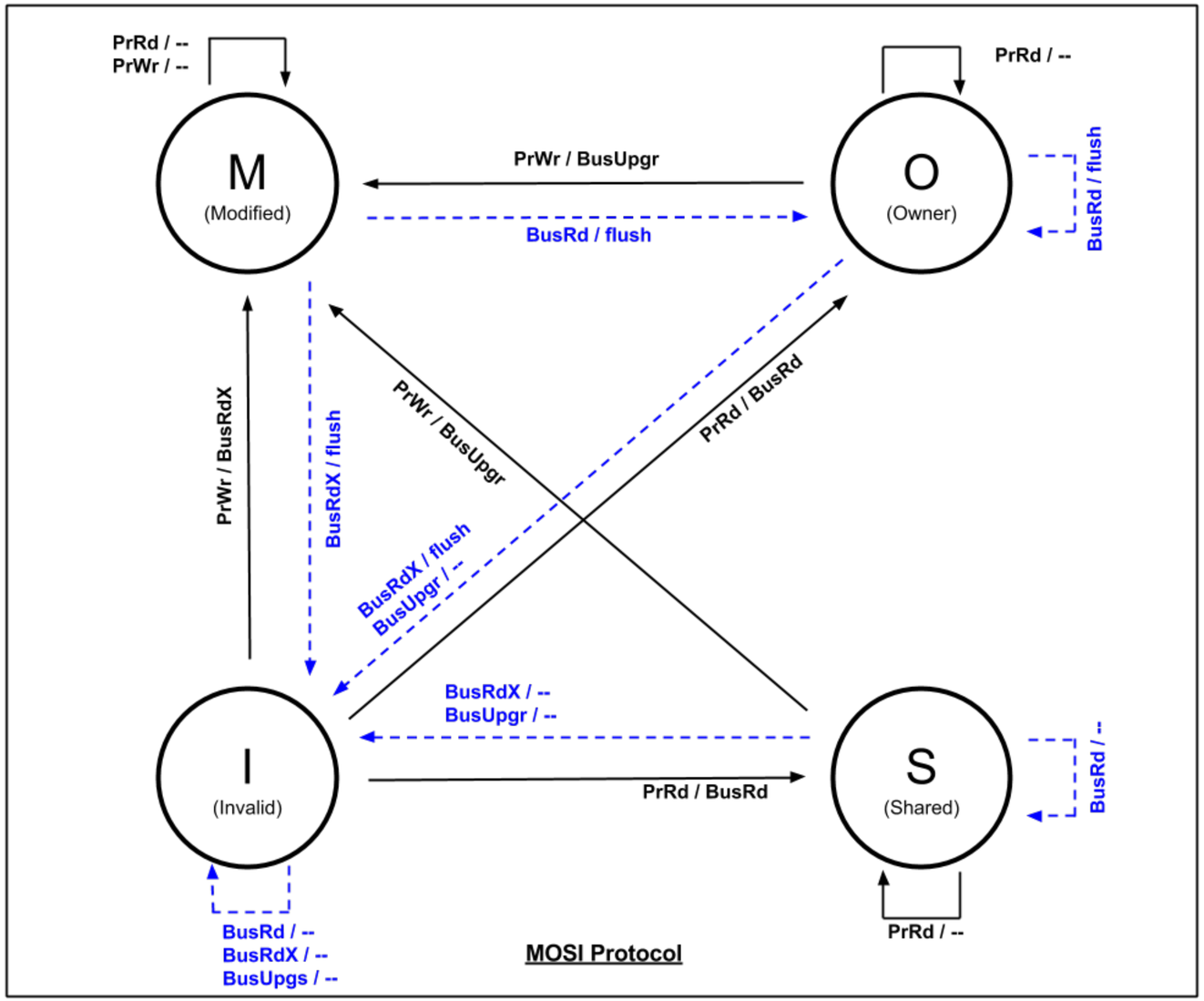
WHICH ONE WOULD YOU CHOOSE?

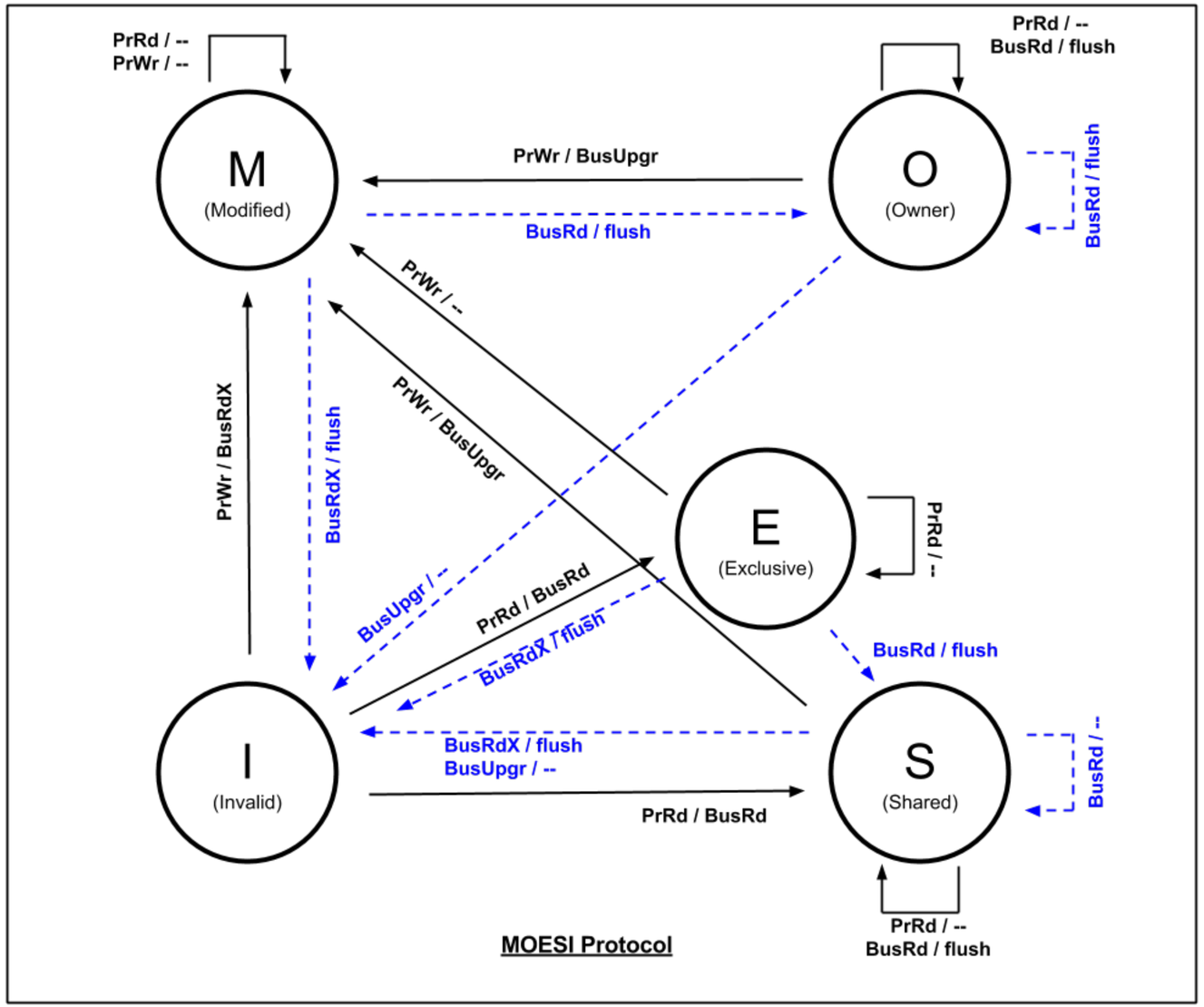
THANK YOU!

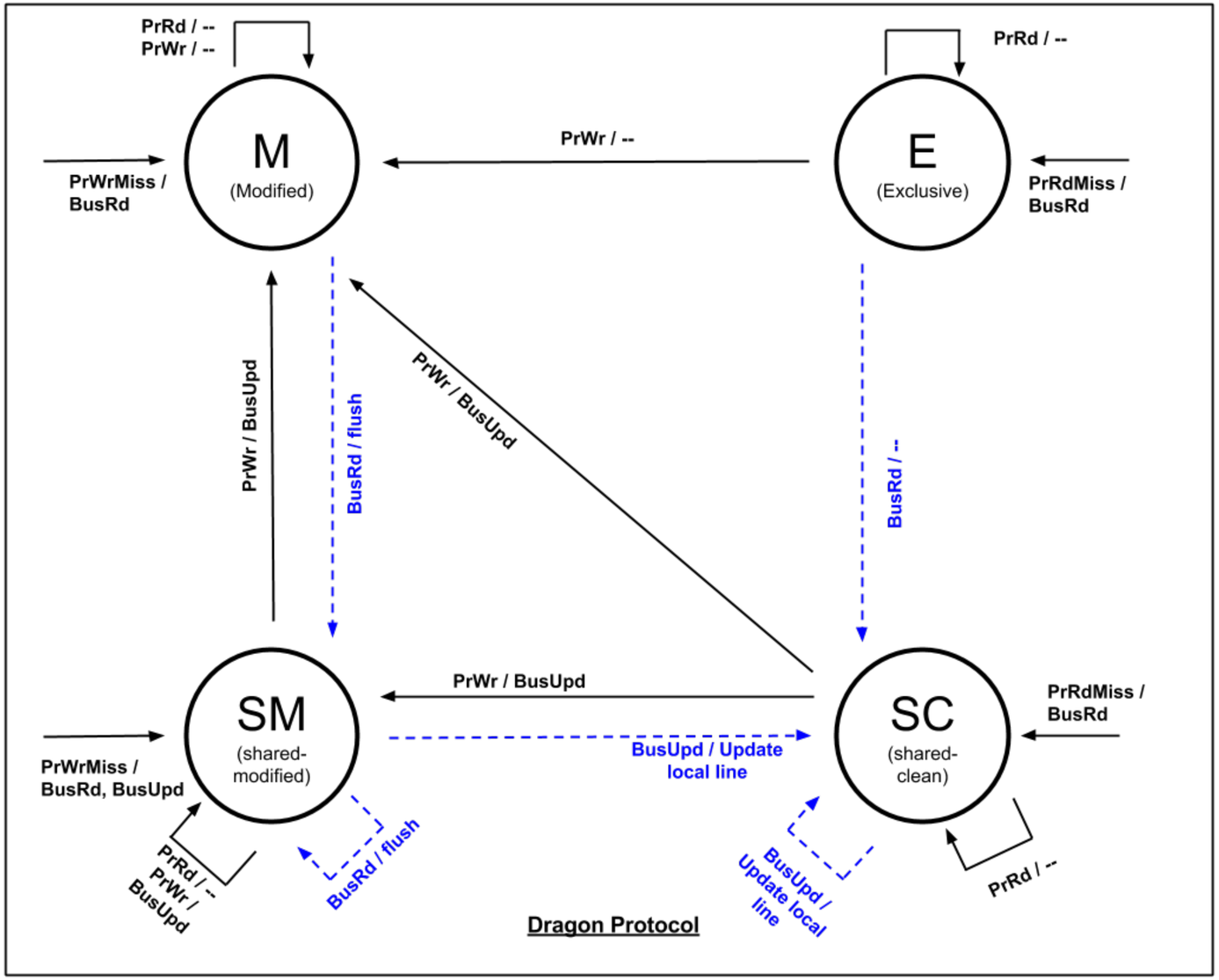
Kshitiz Dange and Yash Tibrewal


PrRd / --
PrWr / --
M
(Modified)
PrWr / BusRdX
BusRdX / flush
PrWr / BusRdX
PrRd / --
BusRd / --
S
(Shared)
BusRdX / flush
PrRd / BusRd
BusRdX / --
I
(Invalid)
MSI Protocol


PrRd / --
PrWr / --
M
(Modified)
PrRd / --
E
(Exclusive)
PrWr / --
PrWr / BusRdX
BusRdX / flush
PrWr / BusRdX
PrRd / BusRd
BusRd / flush
BusRd / --
BusRdX / --
BusRdX / --
I
(Invalid)
PrRd / BusRd
S
(Shared)
PrRd / --
BusRd / --
MESI Protocol


PrRd / --
PrWr / --
M
(Modified)
PrWr / BusUpgr
BusRd / flush
O
(Owner)
PrRd / --
BusRd / flush
PrWr / BusRdX
BusRdX / flush
PrWr / BusUpgr
PrRd / BusRd
BusRdX / flush
BusUpgr / --
BusRdX / --
BusUpgr / --
I
(Invalid)
PrRd / BusRd
S
(Shared)
BusRd / --
PrRd / --
BusRd / --
BusRdX / --
BusUpgs / --
MOSI Protocol


PrRd / --
PrWr / --
M
(Modified)
PrRd / --
BusRd / flush
O
(Owner)
PrWr / BusUpgr
BusRd / flush
BusRd / flush
PrWr / --
PrWr / BusRdX
BusRdX / flush
PrWr / BusUpgr
E
(Exclusive)
PrRd / --
BusUpgr / --
PrRd / BusRd
BusRdX / flush
BusRd / flush
I
(Invalid)
BusRdX / flush
BusUpgr / --
S
(Shared)
BusRd / --
PrRd / BusRd
PrRd / --
BusRd / flush
MOESI Protocol


PrRd / --
PrWr / --
M
(Modified)
PrWrMiss /
BusRd
PrWr / --
E
(Exclusive)
PrRd / --
PrRdMiss /
BusRd
PrWr / BusUpd
BusRd / flush
PrWr / BusUpd
BusRd / --
SM
(shared-
modified)
PrWr / BusUpd
SC
(shared-
clean)
PrRdMiss /
BusRd
PrWrMiss /
BusRd, BusUpd
BusUpd / Update
local line
PrRd / --
PrWr /
BusUpd
BusRd / flush
BusUpd /
Update local
line
PrRd / --
Dragon Protocol

HOW CAN YOU CONFIGURE OUR TOOL?

- You can:
  - Specify the Number of Cores
  - Specify the cache size
  - Specify the associativity of cache
  - Specify the program trace to analyze
  - Specify the snooping protocol to use