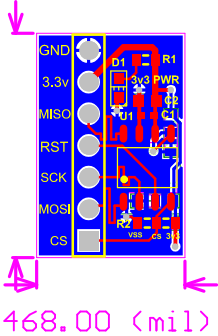


708.00 (mil)



468.00 (mil)

A

B

C

D

A

B

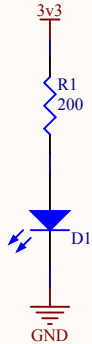
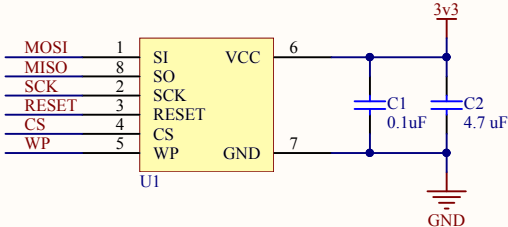
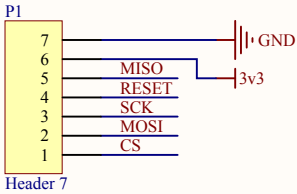
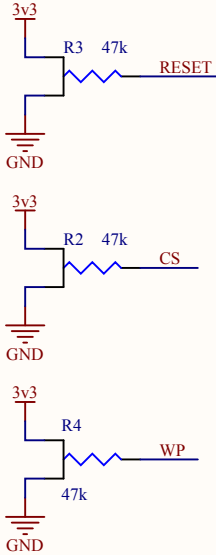
C

D

Henrik
CS High 470k
RESET -> RST pin (NC)
WP high 0 Ohm

Reference
CS pulled High 470k
HOLD high 0 Ohm
WP high 0 Ohm

Will do
CS pulled High 470k
Reset DNP, connect to header
WP high short



Title		
Size	Number	Revision
A		
Date: 2/24/2015		Sheet of
File: C:\Users\...\SPI Breakout.SchDoc		Drawn By:

Comment	Description	Designator	Footprint	LibRef	Quantity
CL05B104KQ5NNNC	Cap Ceramic 0.1uF 6.3V X7R 10% SMD 0402	C1	0402	CL05B104KQ5NNNC	1
CL10B475KQ8NQNC	CAP CER 4.7UF 6.3V 10% X7R 0603	C2	1608[0603]	CL10B475KQ8NQNC	1
LSQ976-Z	OSRAM - LED, SMD, RED, 180MCD, 633NM	D1	LSQ976-Z	LSQ976-Z	1
Header 7	Header, 7-Pin	P1	HDR1X7	Header 7	1
MCR03ERTJ201	RES 200 OHM 1/10W 5% 0603 SMD	R1	J1-0603	MCR03ERTJ201	1
MCR03ERTF4702_SPDT	RES SMD 47K OHM 1% 1/10W 0603 selectable SPDT	R2, R3, R4	0603 Resistor Selectable	MCR03ERTF4702_SPDT	3
AT45DB321E-SHF-B	DATAFLASH 32MBIT 85M	U1	SOIC127P600X175-8N	AT45DB321E-SHF-B	1