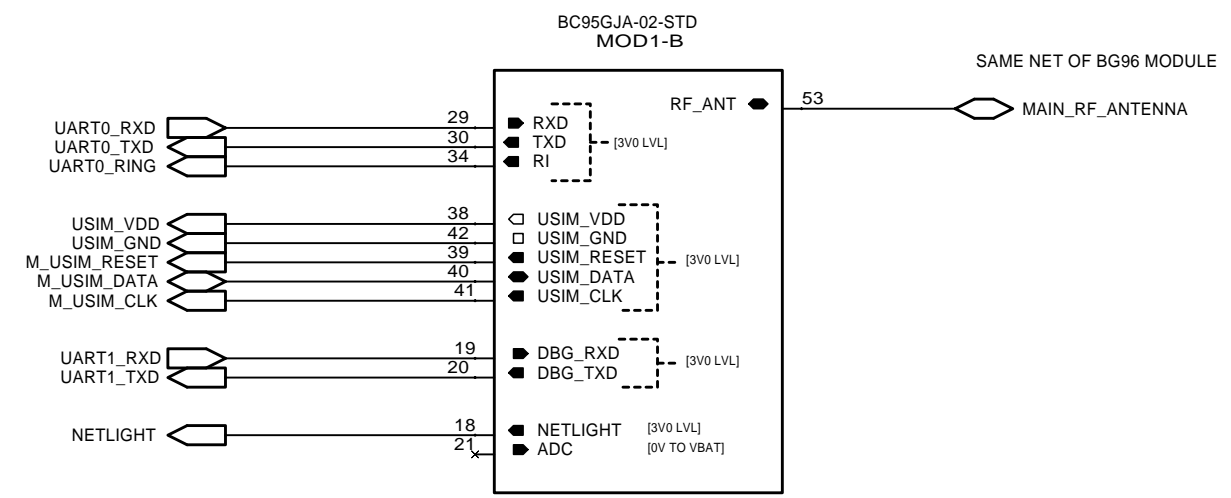
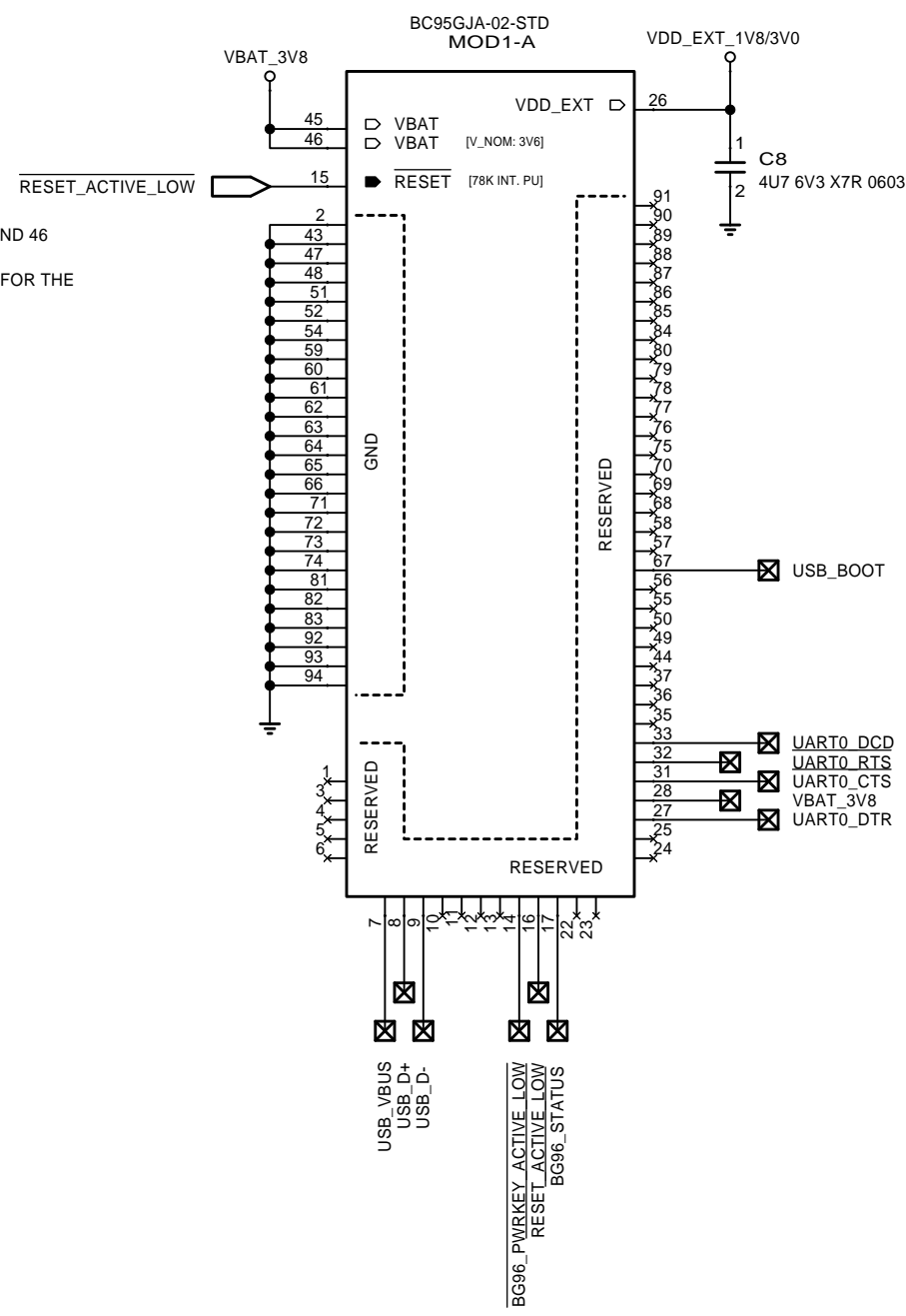
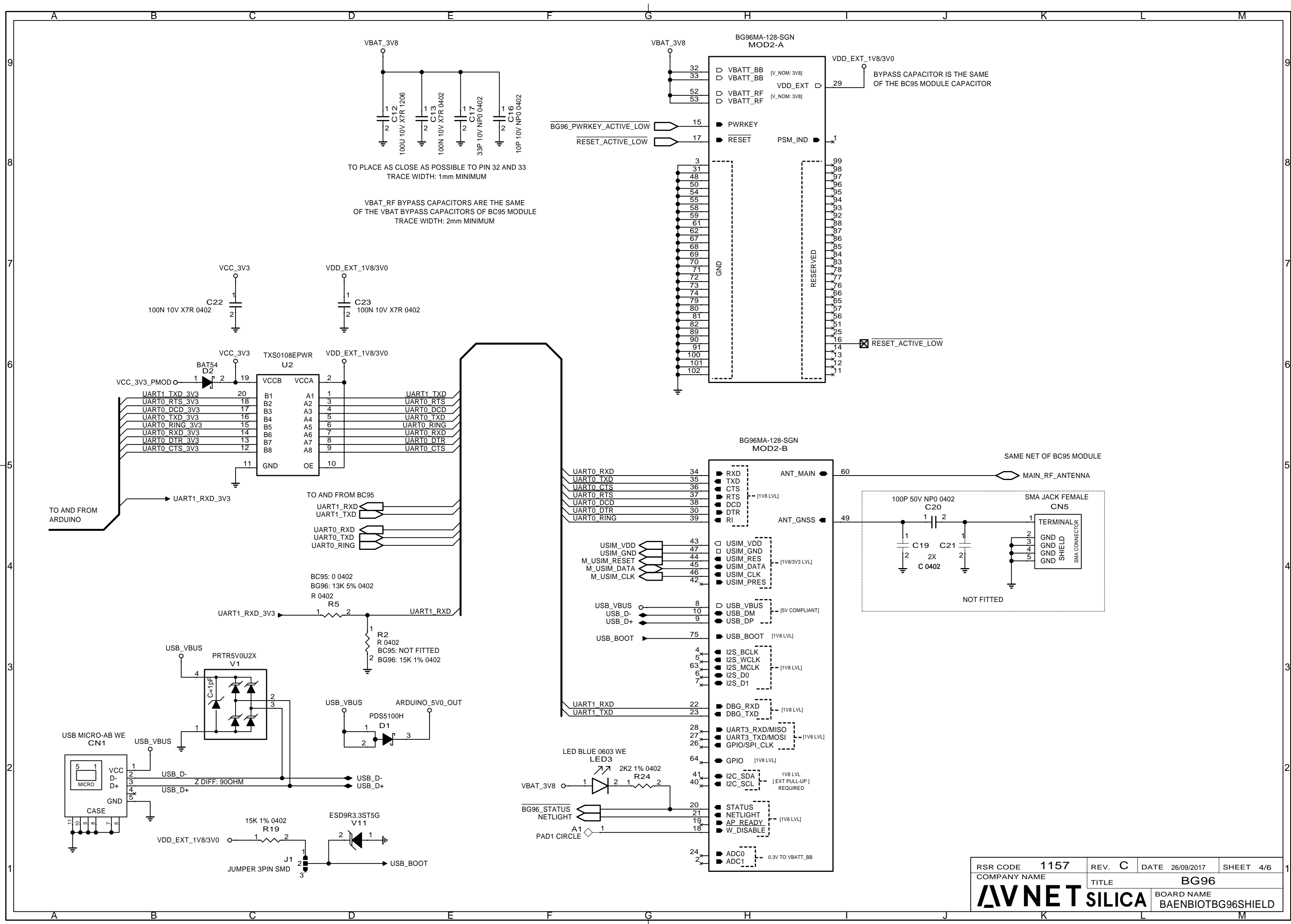


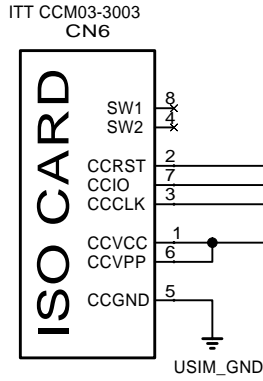
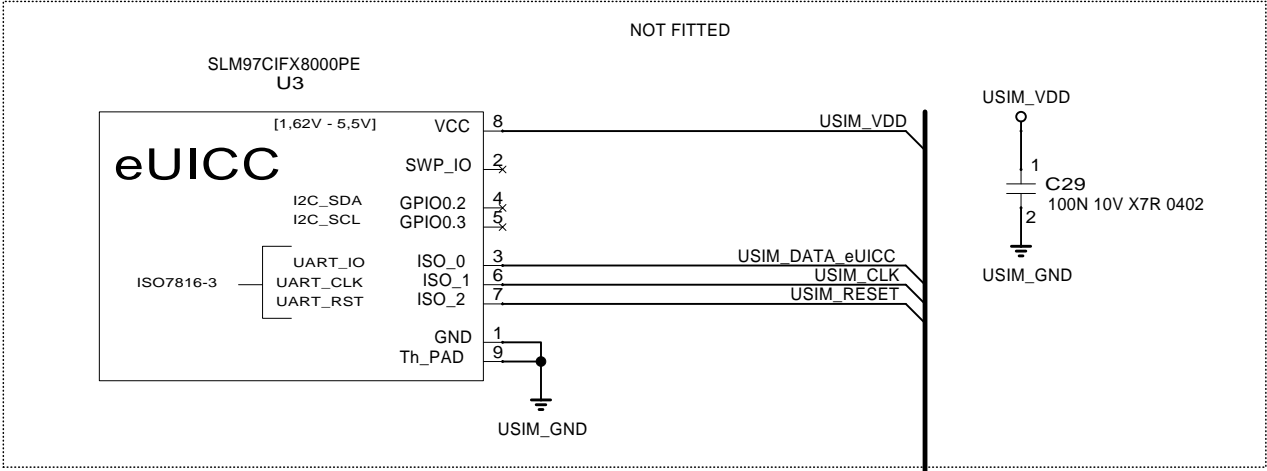
TO PLACE AS CLOSE AS POSSIBLE TO PIN 45 AND 46

THESE BYPASS CAPACITORS ARE USED ALSO FOR THE ALTERNATIVE BG96 MODULE OPTION

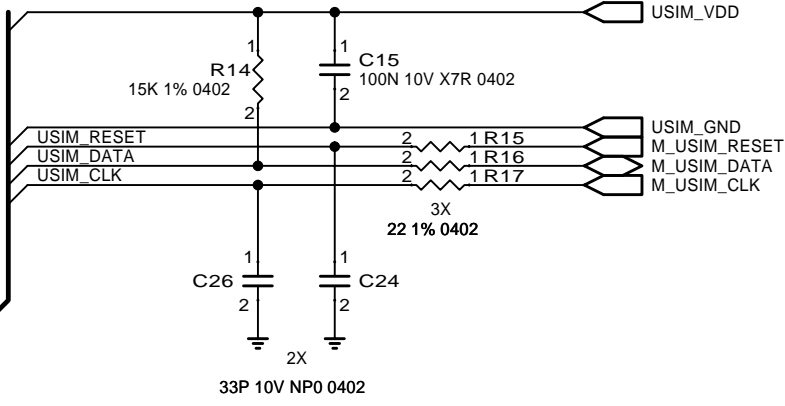
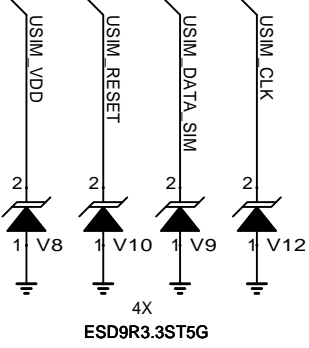
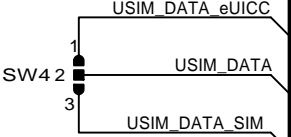
TRACE WIDTH: 1mm MINIMUM



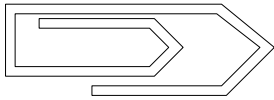




SWITCH P2.54 WE  
TO SELECT  
SIM CARD OR eUICC



RSR CODE	1157	REV.	C	DATE	26/09/2017	SHEET	5/6
COMPANY NAME		TITLE eUICC / SIM CARD					
<b>AVNET</b>		SILICA		BOARD NAME BAENBIOTBG96SHIELD			



HISTORY OF HARDWARE VERSIONS AND CHANGES

REVISION	DATE	DESCRIPTION / CHANGES	SIGNATURE
-	31/07/2017	PROTOTYPE REVISION	ANDREA S.
B	26/09/2017	UART CONNECTION MODIFIED TO WORK WITH ST NUCLEO BOARDS USING DEBUG FEATURES. ADDED JUMPERS TO SELECT STANDARD ARDUINO UART OR ST NUCLEO UART.  W_DISABLE SIGNAL MOVED ON A TEST POINT	ANDREA S.
C	24/10/2017	- R19 VALUE CHANGED IN 15K 1% 0402 - R21 VALUE CHANGED IN 4K7 1% 0402 - 100K RESISTOR ADDED BETWEEN VCC_3V3_PMOD AND GND TO DISCHARGE POWER SUPPLY WHEN PMOD IS NOT CONNECTED - 15K RESISTOR PULL-UP ADDED ON SIGNAL UART0_RXD_3V3 - VOLTAGE LEVEL TRASLATOR POWER SUPPLY CHAIN MODIFIED ADDING VCC_3V3_PMOD VIA A DIODE - LED TO INDICATE POWER PRESENCE ADDED - STATUS LED ADDED - PWR_KEY BUTTON ADDED - SILKSCREEN ADDED (INCLUDING AVNET SILICA BOARD CODE)	ANDREA S.