EECE 7205: Introduction of Computer Engineering

Assignment 1

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1.

Code source:

Cpu频率：cat /proc/cpuinfo | grep MHz

<https://www.cnblogs.com/ggjucheng/archive/2013/01/14/2859613.html>

htop – cpu

https://github.com/nfinit/ansibench

https://github.com/jamesETsmith/SpMV\_benchmark

Salloc -N1 –-exclusive -p express

Node: c0241

Linux-based system: Discovery systems

Architecture: x86\_64

Model: Intel(R) Xeon(R) CPU E5-2690 v3 @ 2.60GHz

Frequency of the CPU: 2.60GHz

Number of cores: 48

Threads per core: 2

Cores per socket: 12

Memory size: 131141092 kB (in total)

Operating system version: 3.10.0-1160.25.1.el7.x86\_64

Source code: https://github.com/nfinit/ansibench

Benchmark: LINPACK - calculate FLOPS

A screenshot of a computer

Description automatically generated with low confidenceA screenshot of a computer

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All the execution time over the 5 runs of the program are the results of evaluating single-precision performance and about 19 second, but the execution time of the first program is a little faster than the other four programs. And the first program has the highest KFLOPS. The difference of execution time and KFLOPS can attribute to memory sharing, cache occupation and core occupation, which causes the difference of percentage about each process like DGEFA, DGESL and OVERHEAD.

Source code: <https://github.com/nfinit/ansibench>

Benchmark: STREAM - measuring memory performance

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The Stream benchmark is used to measure the memory performance (bandwidth) with the respect of four kernels, which are copy, scale, add and triad. The triad function is a combination of the others, so I use this to observe the program execution time. Through the screenshot, all the execution time of average time of the triad function over the 5 runs of the program are the about 0.03, but the average time of the fourth program is a little faster than the other four programs. The difference of execution time can attribute to the occupation of cache 1, 2 and main memory and the simultaneous use of the same core.

Source code: <https://github.com/nfinit/ansibench>

Benchmark: Whetstone – measures a computer's floating-point performance

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The Whetstone benchmark measures a computer or server’s floating-point performance. The overall performance is calculated by Whetstone Instructions Per Second (WIPS). Through the screenshot, all the duration over the 5 runs of the program are about 38sec, but the second, fourth and fifth program is a little faster than the other four programs. And the Whetstone speed ratings are 2631.6 MWIPS. The difference of execution time and KFLOPS can attribute to memory sharing, cache occupation and core occupation.

b.

Benchmark: LINPACK

O0((do no optimization, the default if no optimization level is specified)),

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Description automatically generated with low confidence

O1((optimize minimally)),

A screenshot of a computer

Description automatically generated with medium confidence

O2((optimize more)),

A screenshot of a computer

Description automatically generated with low confidence

O3((optimize even more)),

A screenshot of a computer

Description automatically generated with low confidence

Ofast((optimize very aggressively to the point of breaking standard compliance))

A screenshot of a computer

Description automatically generated with medium confidence

Benchmark: Whetstone

O0((do no optimization, the default if no optimization level is specified)),

Text

Description automatically generated

O1((optimize minimally)),

Text

Description automatically generated

O2((optimize more)),

Text

Description automatically generated

O3((optimize even more)),

Text

Description automatically generated

Ofast((optimize very aggressively to the point of breaking standard compliance))

Text

Description automatically generated with low confidence

The above screenshots show the results of turning on optimization flags of gcc to reduce the cost of compilation and produce an expected result. The optimization strategy I use is changing the -O variable, which can make code compilation take more time and take up much more memory as the increasing of optimization level. The levels I applied of the optimization flag are O0, O1, O2, O3 and Ofast.

O0 is the default setting and turns off optimization totaly. O1 reduces the execution time by turning on a list of optimization flags, such as -fdce, -fdse and -fif-conversion. It is the basic optimization level and produce faster execution results for LINPACK and Whetstone. O2 flag optimizes even more than O1. Except those flags O1 turning on, O2 also turns on another lists of optimization flags based on O1, such as -falign-functions and -falign-jumps. Without taking too much compilation time and compromising on memory, it increases benchmark performance more than O1 and the execution time results are shown above. Similarly, O3 turns on addition optimization flags depending on O2. It produces a better execution time but not recommend because it may slow down a system due to large binaries and memory usage. Ofast not only turns on all O3 optimizations, but also enables some optimizations that are not valid for all standard-compliant programs, like -ffast-math, -fallow-store-data-races and the Fortran-specific -fstack-arrays. It is not a recommended optimization flag either because it breaks strict standards compliance. The Whetstone is executed much better but linpack, which can attribute to the breaking of standards compliance.

c.

Pthreads are kinds of C language programming types by using pthread.h header to take advantage of those additional threads provided by server or PC. Pthread can efficiently increase testing speed and decrease the cost of data exchange.

For benchmark LINPACK, I would use Pthreads to create three threads to execute DGEFA, EGESL and OVERHEAD separately, and summary the results to obtain additional speedup.

For benchmark Stream, it run each kernel “NTIMES” to evaluate and report memory performance and the source code set “NTIMES” as ten. I would take advantage of Pthreads to create 10 threads and each thread execute the program for each kernel for one time at the same time. To conclude, I sum each result, terminate threads and report results in total.

For benchmark Whetstone, it evaluates program running time by execute instruction for “n” times loop. I would take advantage of Pthreads to create like 10 threads and each thread execute the program for “n/10” time. In conclusion, I sum result of each thread, terminate threads and write down totally duration and MWIPS.

2.



Graphical user interface, text, application

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The screenshot shows that the merge sort program was run with 1, 2, 4 and 8 threads separately on my mac OS system and relevant code is cited from website <https://malithjayaweera.com/2019/02/parallel-merge-sort/>. Pthreads were used by divided the whole random integers into n threads and evaluate results. Ideally, results display that the execution will take less time as program run with more threads.

1. Challenges:
2. Pick up a sorting method. Not all sorting methods are suitable for using multiple threads.
3. Find a suitable place in merge sort program to use Pthreads. In the process of merge sorting, there are some places can be changed to use Pthreads, but not all places are reasonable.
4. How to assign work to each thread. Pthreads are new things for me, understanding the use and code of Pthreads need take a little more time.
5. Strong scaling means that the upper limit of speedup is determined by the serial fraction of the code. Weak scaling means that there is no upper limit for the scaled speedup and the execution time is calculated based on the amount of work done for a scaled problem size. Through the performance report, the execution time is markedly improved as threads increase, but the improved results are not double. It indicates that the weak scaling properties of my sorting implementation are stronger than the strong scaling. The serial fraction of my sorting method affect result and not determine the upper limit of speedup.

3.

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1. The CPU Model: Intel(R) Xeon(R) CPU E5-2690 v3 @ 2.60GHz

L1d cache size: 32K

L1i cache size: 32K

L2 cache size: 256K

L3 cache size: 30720K

L1d cache assoc: 8

L1i cache assoc: 8

L2 cache assoc: 8

Table

Description automatically generatedL3 cache assoc: 20

High speed data transfer: over 10 Gbps Ethernet (GbE)

Bandwidth: 10 GbE or a high-performance HDR200 InfiniBand (IB) interconnect running at 200 Gbps

Latency: about 0.1ms of the network interconnect for this node



Linux c0186 3.10.0-1160.25.1.el7.x86\_64 #1 SMP Wed Apr 28 21:49:45 UTC 2021 x86\_64 x86\_64 x86\_64 GNU/Linux

4.

Summary of trends:

A supercomputer is composed of processors, memory, I/O system, and an interconnect. Four main factors determine the Top500 list, which are number of cores, Rmax, Rpeak and cost of power. Rmax is the maximum value that the system can perform of the Linpack benchmark while Rpeak represents theoretical maximum number that system can run the Linpack benchmark. Rmax is the dominating factor to rank the Top500 supercomputer. In the top 10 of the TOP500, there are over 20 thousand TFlop/s Rmax and nearly over half million cores number. IBM, AMD and Intel and the three main company providing processors and GPU of NVIDIA was widely used by those top10 supercomputer. Apart from achieving high computing performance, power consumption is an indispensable factor that supercomputer designers must consider. Based on the contemporary data, top10 supercomputer consume exceed 5 megawatts. In the future supercomputer design, high-performance, high-density packaging and low power consumption and heat generation would be a better choice and a new goal to pursue.

My architecture: Supercomputer Jiayun, IBM POWER9 22C 2.8GHZ, NVIDIA Volta GV100, Dual-rail Mellanox EDR Infiniband

Diagram

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Figure 1

*Picture source: https://www.nextplatform.com/2017/12/05/power9-to-the-people/*

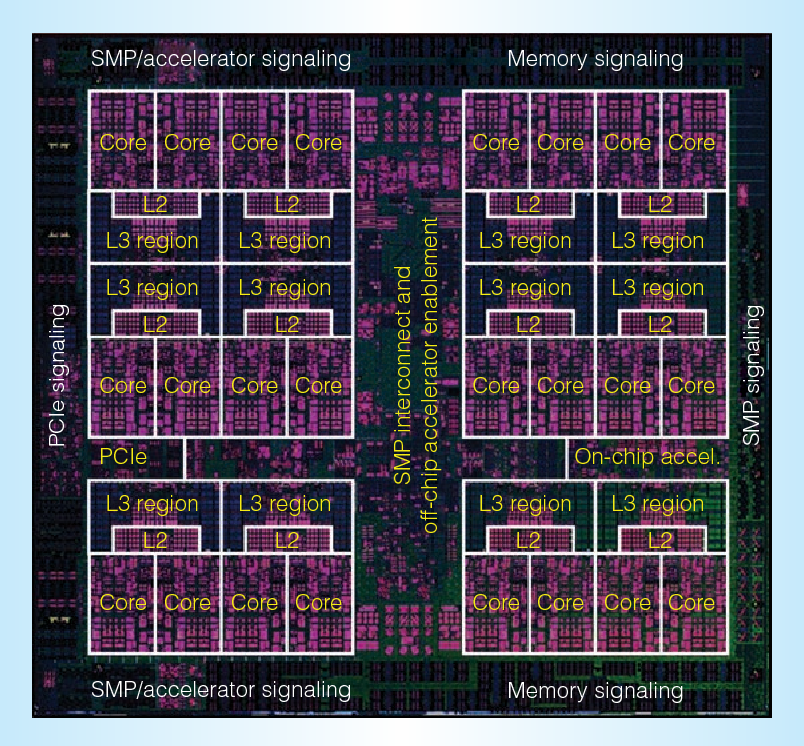


Figure 2

*Picture source:* [*https://en.wikichip.org/wiki/ibm/microarchitectures/power9#Memory\_Hierarchy*](https://en.wikichip.org/wiki/ibm/microarchitectures/power9#Memory_Hierarchy)

Diagram

Description automatically generated

Figure

*Picture source: https://en.wikichip.org/wiki/ibm/microarchitectures/power9#Memory\_Hierarchy*

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CPU Clock Speed (GHz): 2.8

Nodes: Login Nodes: 5

GPU nodes: 9000

Total nodes: 10000

CPUs: CPU Architecture: IBM Power 9

Cores/Node: 24

Total Cores: 250,000

Min. feature size: 14nm

GPUs: GPU architecture: NVIDIA V100 (Volta)

Total GPUs: 18,000

GPUs per compute node: 4

Memory: Memory total (GB): 1,500,000

CPU memory/Node (GB): 256

GPU Memory/Node (GB): 64

L1 cache: 32+32 KiB per core

L2 cache: 512 KiB per core

L3 cache: 120 MiB per chip

L4 cache: via Centaur

The first picture shows a system board block diagram and the second one displays the CPU architecture I would like to use. The supercomputer I design will have 10,000 nodes, each hold 2 Power 9 CPUs with 24 cores. Overall, about 250,000 cores are used to increase Rmax value. Minimum feature size I choose 14nm to avoid high heat generation caused by smaller size chip and more intensive arrangement. The GPU I choose NVIDIA V100. All nodes are linked together by using Mellanox dual-rail EDR InfiniBand network due to its high bandwidth and low latency.

5.

Differences:

1. Ordering method: The supercomputers in Top500 are ranked by Ramx (TFlop/s) while the Green 500 ranks computer based on power efficiency (GFlops/watts). The power efficiency is calculated by making Rmax dividing power consumption.
2. Number of cores: top 10 systems in Green500 have much less number of cores than the top 10 systems in Top500.
3. Power consumption: top 10 systems in Green500 consumes much less power than the top 10 systems in Top500.
4. CPU: 8 of 10 systems in Green500 list apply AMD EPYC processor while those systems in Top500 use like IBM Power9, Intel Xeon and AMD EPYC.