Working with Snickerdoodle

An introduction to the world of FPGA's

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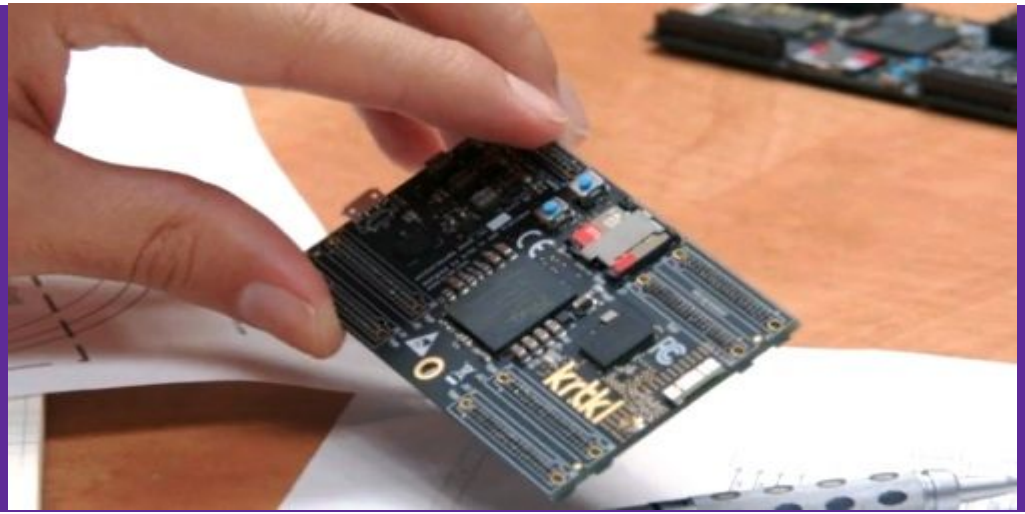


Goal of Project

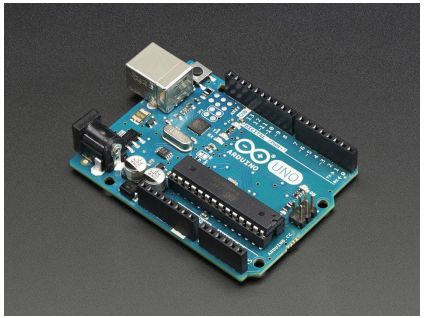
To develop a video project tutorial for hobbyists new to FPGA’s

What is an FPGA?

-Stands for: Field Programmable Gate Array

-Difference between Raspberry Pi (CPU) and Arduino (Microcontrollers)

-Why is it better in some cases?



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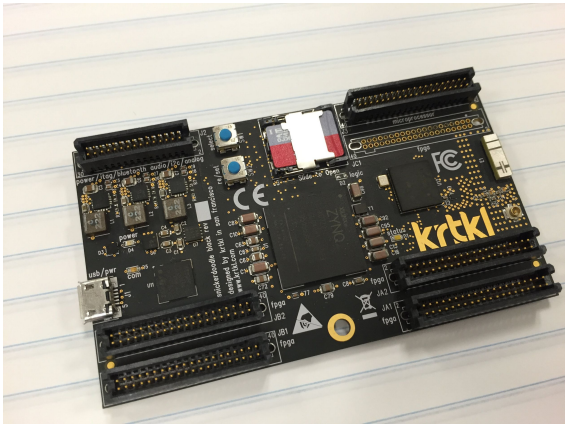


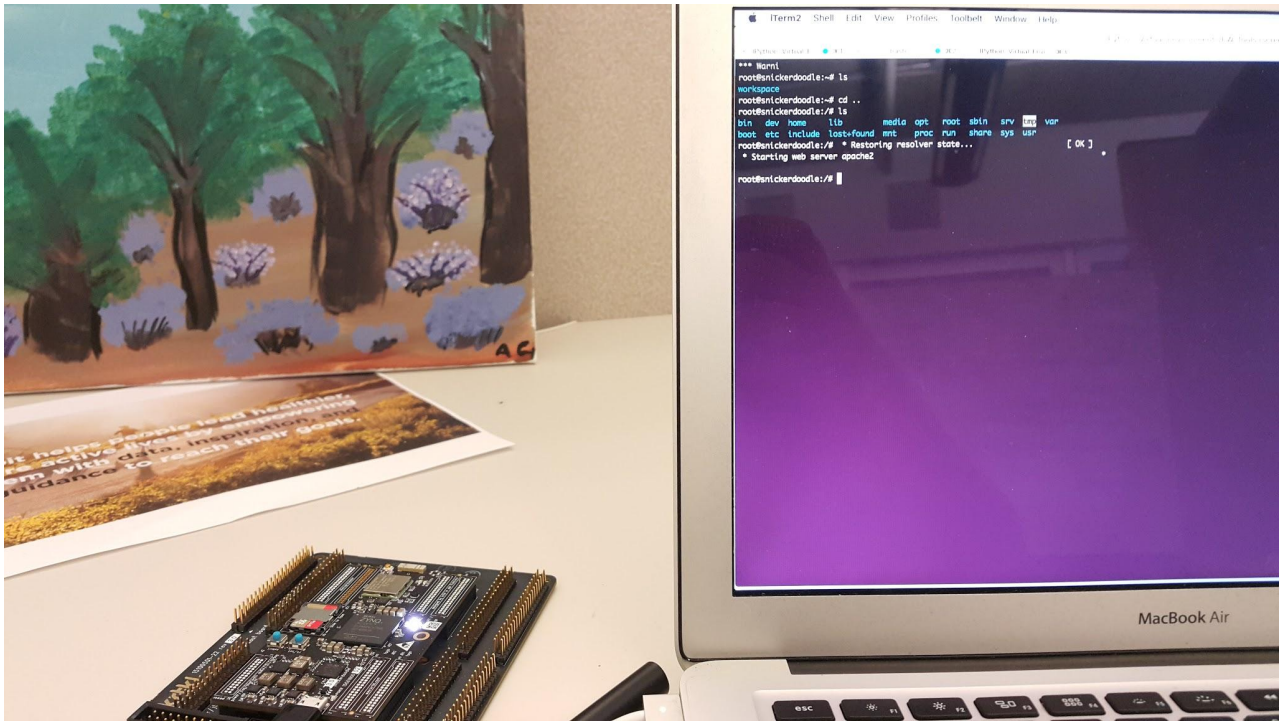
Deliverables: Phase 1 Description

Bring Up Snickerdoodle

- Linux environment
- Annotations of krtkl’s documentation

Deliverables: Phase 1 Status

- Successfully set up Linux on Snickerdoodle
- Annotated Documentation



Deliverables: Phase 2 Description

Port over Zybo VGA video project to Snickerdoodle:

- Hardware Block Diagram
- VHDL Code
- Demo of camera successfully running

Deliverables: Phase 2 Status

Hardware Block Diagram	Complete
VHDL Code	Complete
Camera Successfully running on Snickerdoodle	Incomplete

Phase 2: What is left

- Successfully generating a bitstream for Snickerdoodle
- Uploading bitstream to Snickerdoodle
- Completed Guide for new users

Retrospective on Project

1. Unbounded tasks: Time management and Estimation
2. Monitoring new task duration as future reference
3. Frequent communication with manager