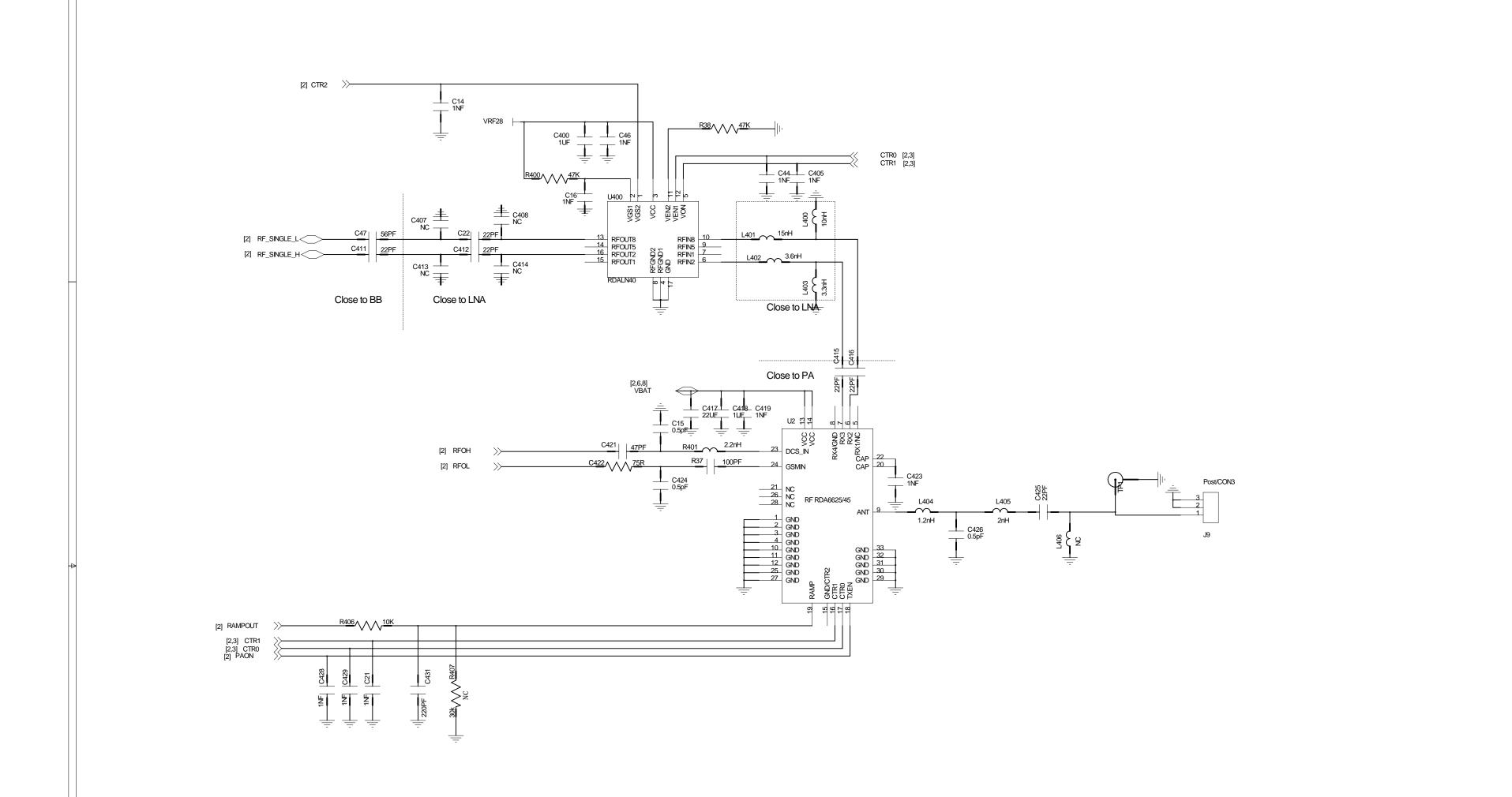
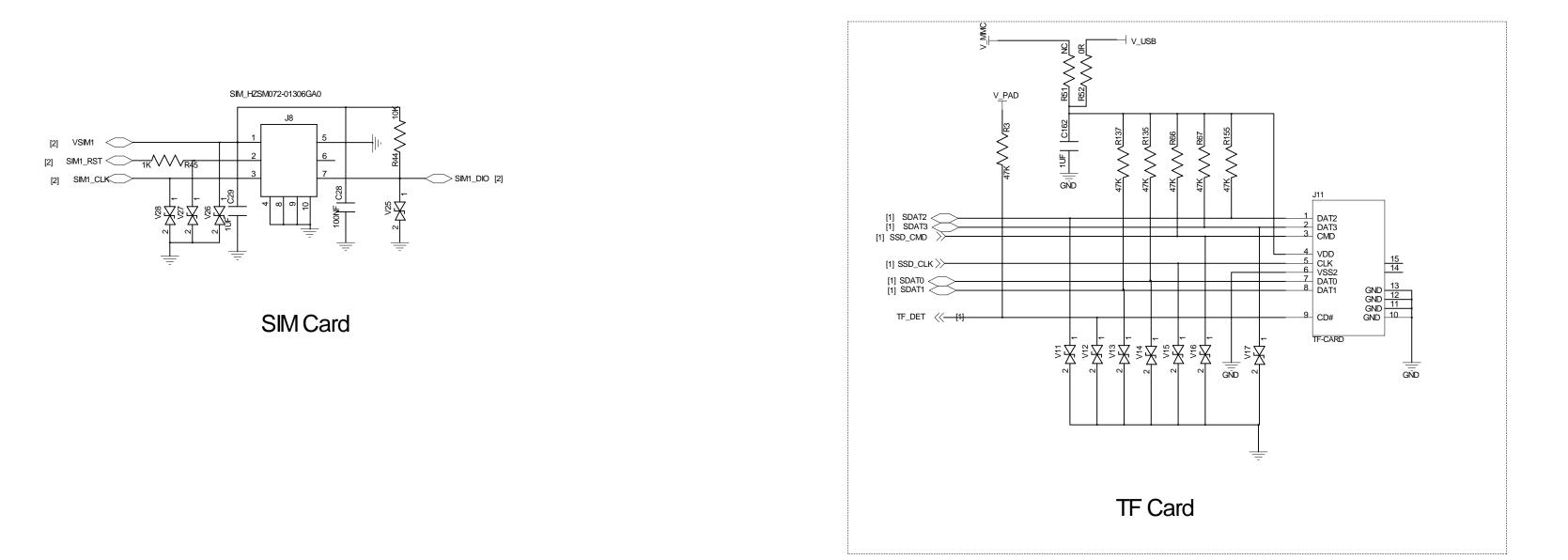


Power name	output	output	Default	Sleep	Field	Note
V_PAD	voltage(V) 1.8/2.8	current(mA) 60/120	ON	ON	GPIO/SPI/UART/HOST/I2C	
_						
V_ANA	2.5	120	ON	OFF		
VSPI_MEM	1.8/2.8	170	ON	ON	SPI_MEM	
V_MEM	1.8	120	ON	ON		Internal memory
V_CAM	1.8/2.8	100/120	ON	OFF	CAM	
V_MMC	2.7/3.0	100	ON	OFF	MMC	SD card
V_LCD	1.8/2.8	140/140	ON	OFF	LCM	
V_MIC	1.8	/	OFF	OFF		
V_VIB	3.2	150	OFF	OFF		Viberator
V_ASW	1.8/2.8	170/190	ON	OFF		
V_USB	3.3	240	ON	ON		USB
VRF28	1.8/2.8	130/130	ON	ON		RF
VTT_DDR	0.6	/	OFF	OFF		
BL_LED_OUT	3.2	190	OFF	OFF		DDR ref
VSIM1	1.8/2.8	100	OFF		SIM1	LED
VSIM2	1.8/2.8	100	OFF		SIM2	
VSIM3	1.8/2.8	100	OFF		SIM3	
V_RTC	2.8	/	ON	ON		

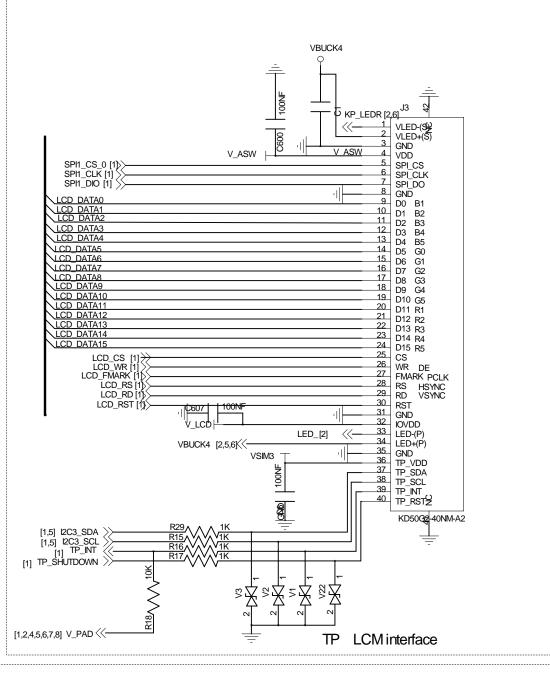
Xunlong Software						
Design Nar	ne 2G-IOT -	-V1.4				
Size C	Page Name	RDA8	810NL	_&P&PL	_AB	REV 1.4
Date:	Monday£-April 10	D£-2017	Sheet	2 of 8		

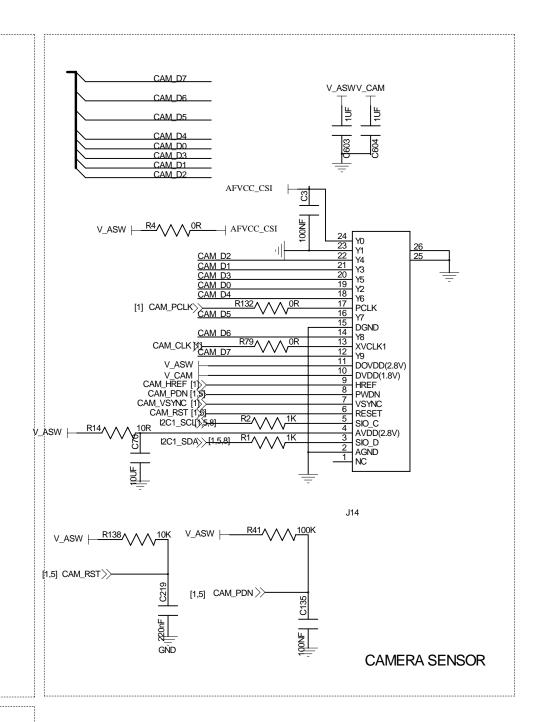


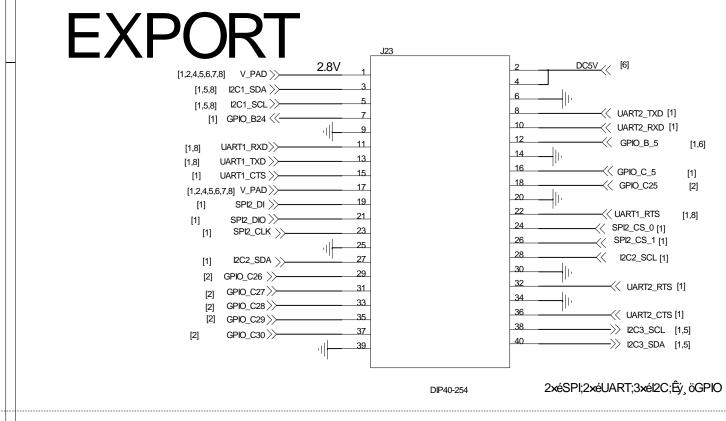
Xunlong Software					
Design Nan	^{ne} 2G-IOT-V1.4				
Size C	Page Name RF_R	DA6625	REV 1.4		
Date: N	Monday£-April 10£-2017	Sheet 3 of 8	•		



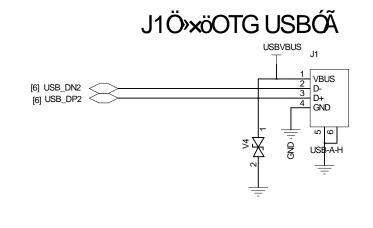
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Design Nar	^{ne} 2G-IOT-V1.4		
Size C	Page Name SIM8	T-FLASH_CARD	REV 1.4
Date:	Monday£-April 10£-2017	Sheet 4 of 8	•

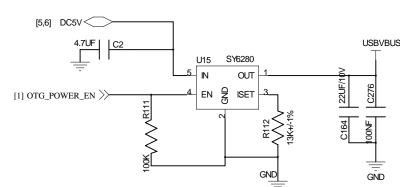






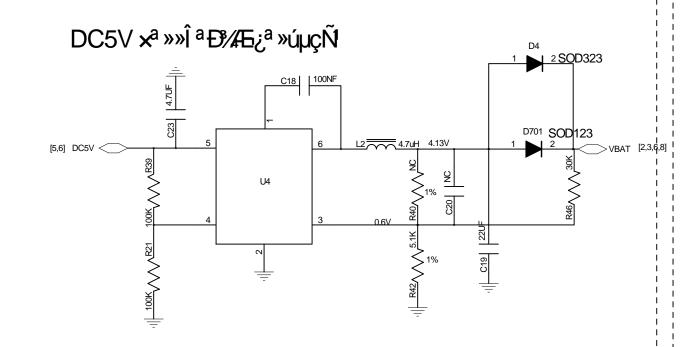
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Design Nar	ne 2G-IOT -	·V1.4		
Size C	Page Name	LCMC	CAMERA/EXPORT	REV 1.4
Date:	Monday£-April 10	£-2017	Sheet 5 of 8	•

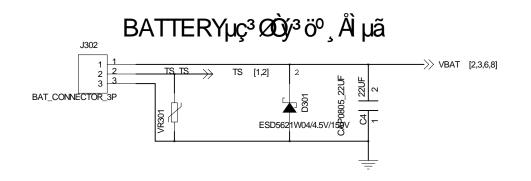


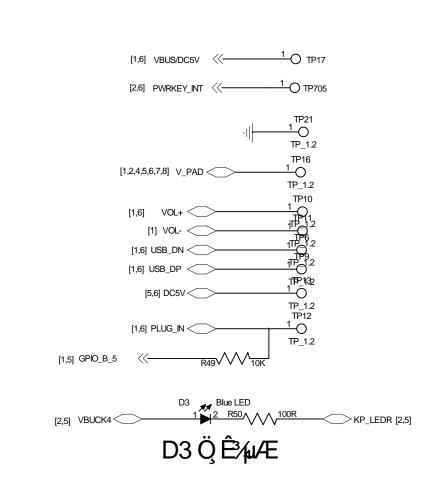


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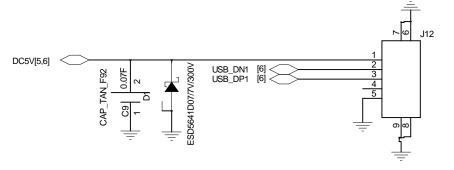
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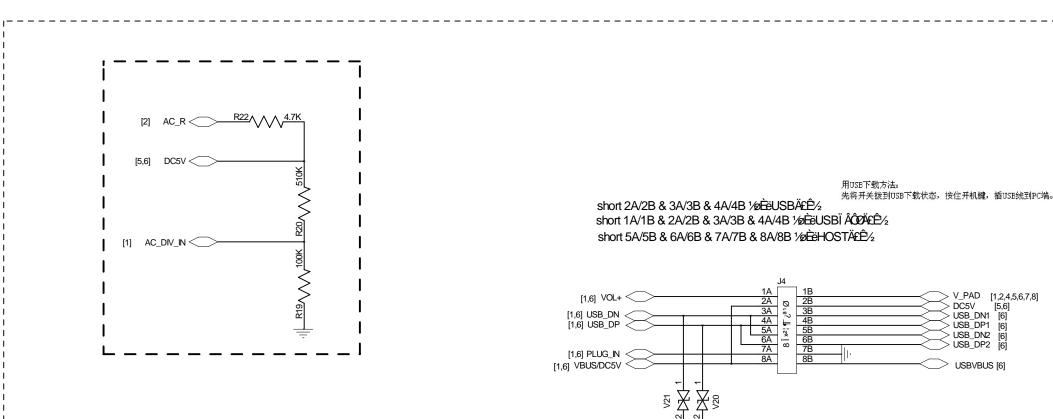


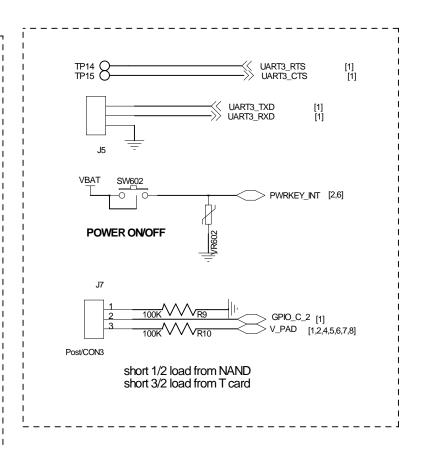




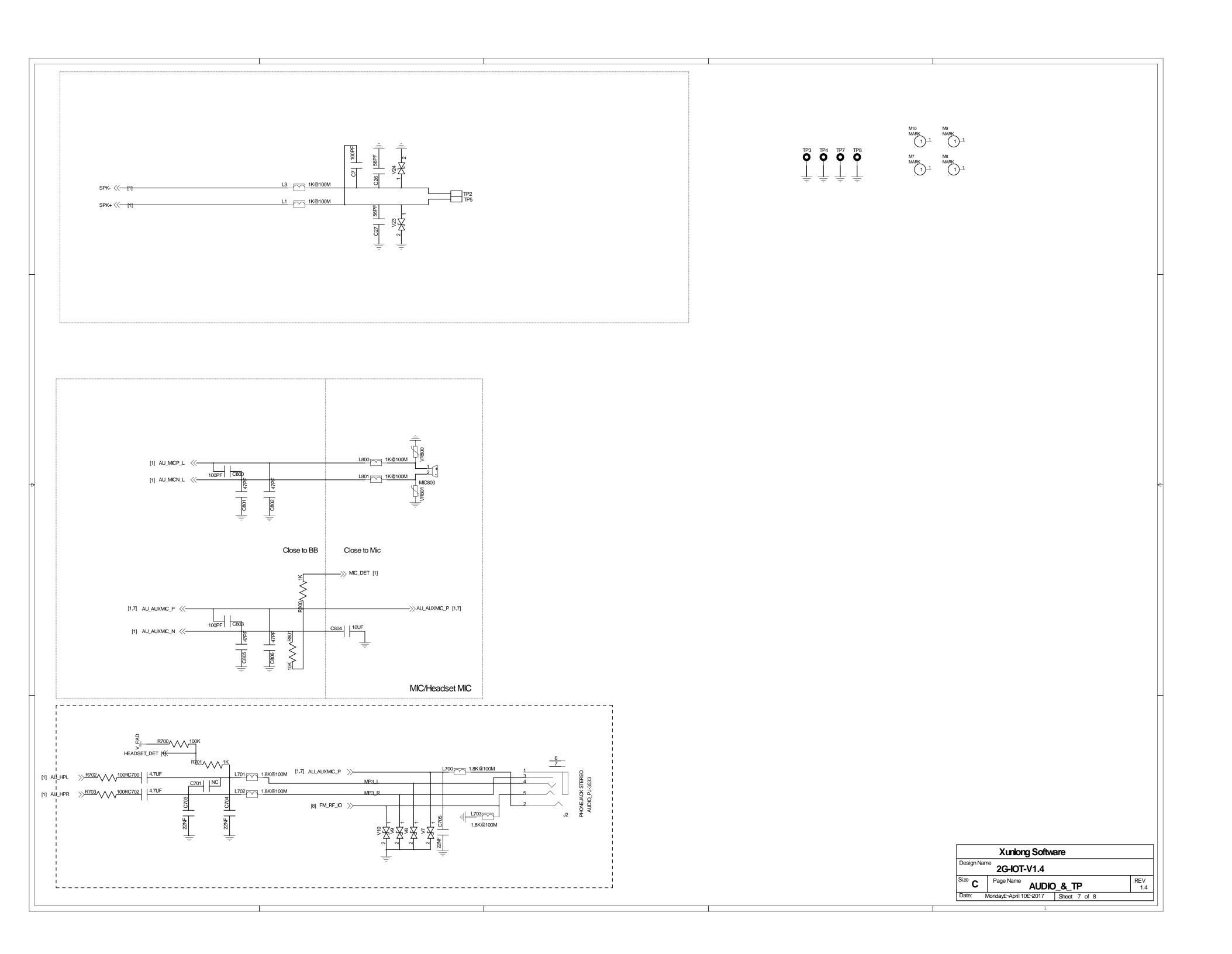
USB DC JACK(5VÊäÈë)

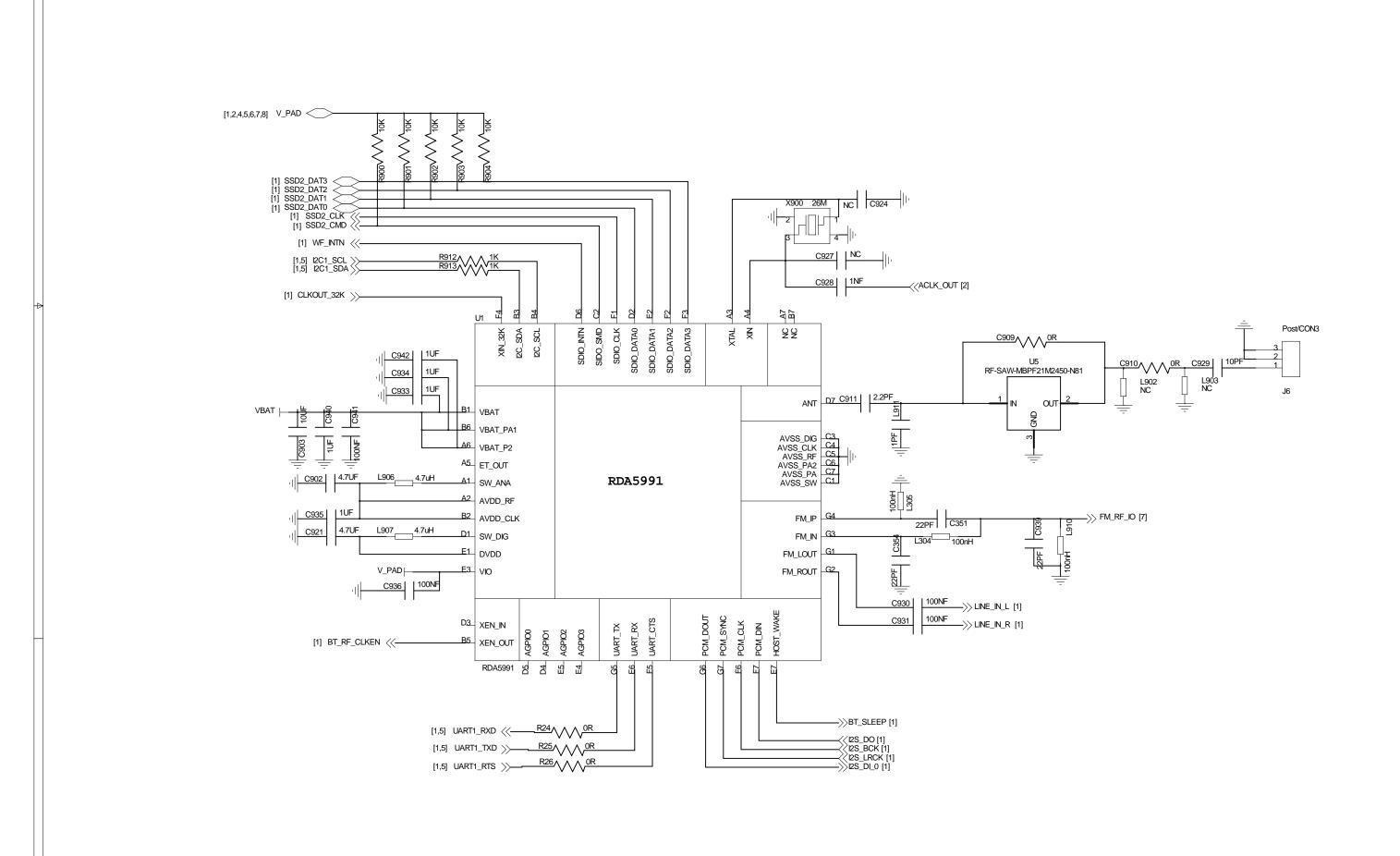






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Monday£-April 10£	-2017	Sheet 8 of 8		
	Page Name	2G-IOT-V1.4	2G-IOT-V1.4 Page Name WIF/BT/FM_RDA599	Page Name WIF/BT/FM_RDA5991