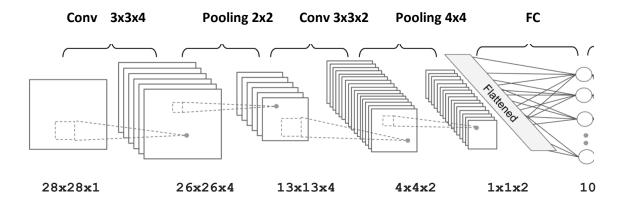
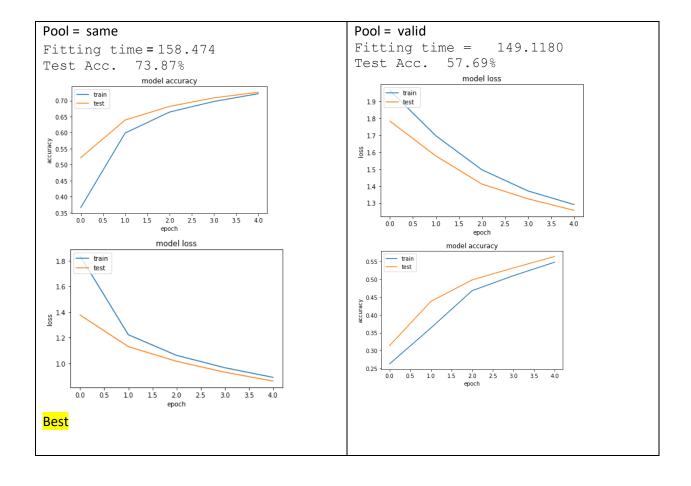
HW8



The shape out the output tensor of each layer given a batch size of 100 using the previously defined architecture:

```
Conv_1
TensorShape([16, 26, 26, 4])
pool_1
TensorShape([16, 13, 13, 4])
Conv_2
TensorShape([16, 4, 4, 2])
pool_2
TensorShape([16, 1, 1, 2])
Flatten
TensorShape([16, 2])
fc_1
TensorShape([16, 10])
```

Same is better



As kernel size increase, accuracy and training time increase and loss decreases

