

TEST CASE 1

MESSAGE: 10011101

GENERATOR: 1001

GENERATOR <FILE | VERIFIER

```
Please select which sequance you want:

1 - generator --> verifier

2 - generator --> verifier --> alter --> vierifier

1
transmitted message: 10011101100
message is correct
```

GENERATOR <FILE | ALTER 3 | VERIFIER

```
Please select which sequance you want:

1 - generator --> verifier

2 - generator --> verifier --> alter --> vierifier

2
Enter the index of the bit want to change

3
transmitted message : 10011101100
message is correct
altered message : 10001101100
message is not correct
```

TEST CASE 2

MESSAGE: 1101011111

GENERATOR: 10011

GENERATOR <FILE | VERIFIER

```
Please select which sequance you want:

1 - generator --> verifier

2 - generator --> verifier --> alter --> verifier

1

transmitted message : 11010111110010

message is correct
```

GENERATOR < FILE | ALTER 5 | VERIFIER

```
Please select which sequance you want:

1 - generator --> verifier

2 - generator --> verifier --> alter --> verifier

2
Enter the index of the bit want to change

5
transmitted message: 11010111110010
message is correct
altered message: 11010011110010
message is not correct
```