



Faculty of Engineering and Technology Department of Electrical and Computer Engineering

Circuit Analysis – ENEE2304

Name: Aya Abed AL-Rahman Fares Shejaeya

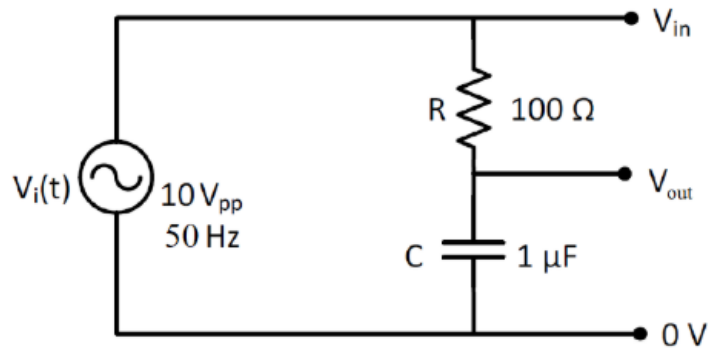
Std.no: 1222654

Section:4

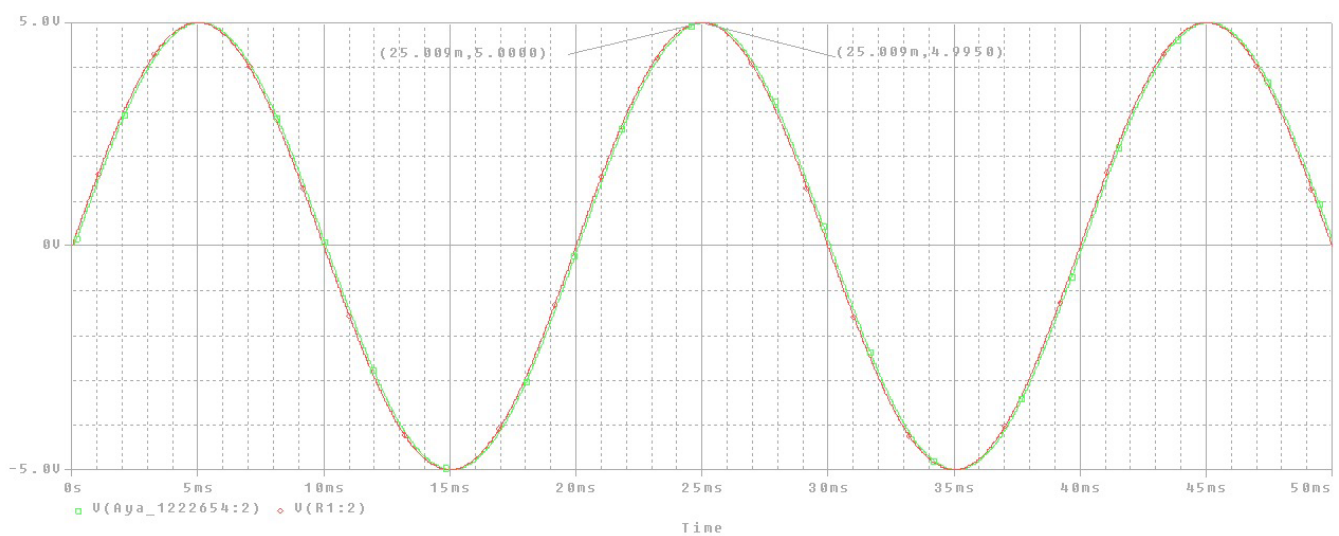
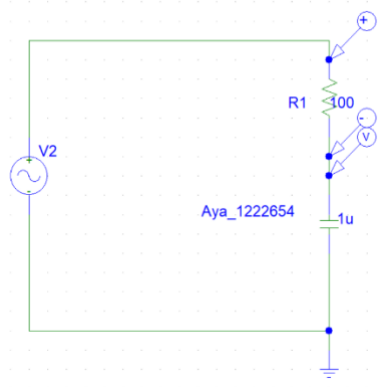
Date:25/Jun/2024

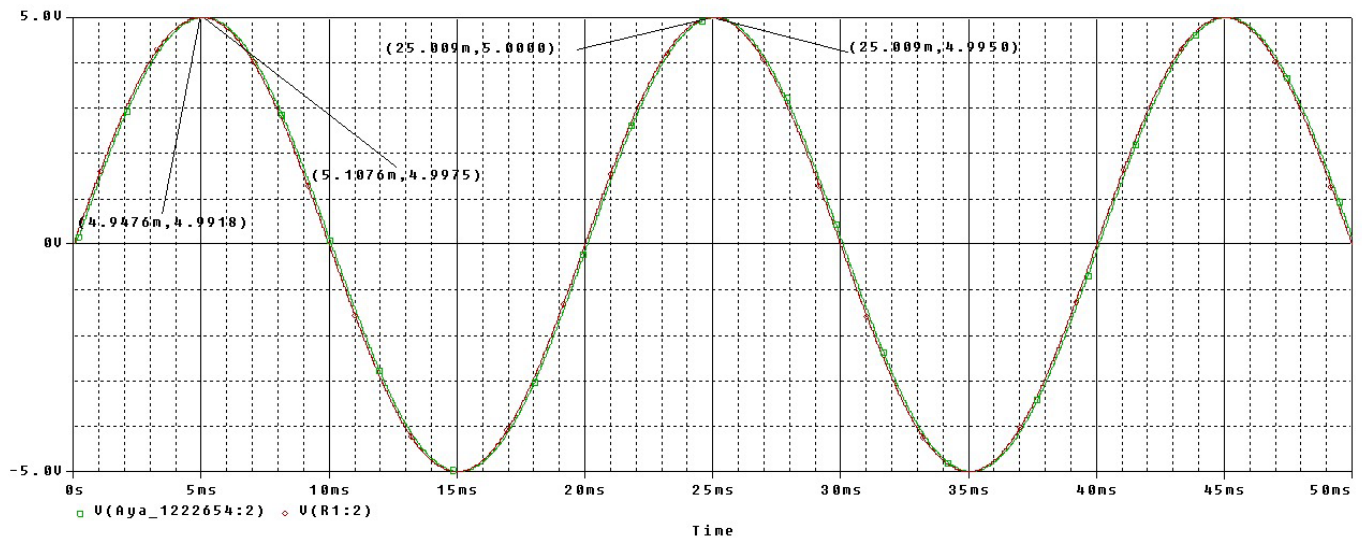
Question #1:

: Sinusoidal Steady State Analysis



1. Use PSpice to do a transient analysis of the circuit, and show $V_{in}(t)$ and $V_{out}(t)$ on one plot (you may need to use different Y-axes).
2. Use cursors to measure the time difference between the peaks of the two signals, then use the following relationship to calculate the phase shift using the measured time $\{\Delta\theta = 360^\circ \times f \times \Delta t\}$.
3. Discuss the results obtained.





$$\Delta\theta = 360^\circ \times f \times \Delta t = 360^\circ \times 50\text{Hz} \times (5.1076 - 4.9476)\text{ms} = 1.8^\circ$$

In the analysis of the circuit using PSPICE, several key aspects were explored to understand the behavior of the system under different conditions. Let's delve into a detailed discussion of the results obtained:

1. Transient Analysis and Waveform Plotting:

- The transient analysis provided insights into how the circuit responds to changes over time. By plotting $V_{int}(t)$ and $V_{out}(t)$ on the same graph with distinct Y-axes, we could visualize the input and output waveforms simultaneously.
- The plot allowed us to observe any transient effects, steady-state behavior, and the relationship between the input and output signals.

2. Phase Shift Calculation:

- By measuring the time difference between the peaks of $V_{int}(t)$ and $V_{out}(t)$ using cursors, we calculated the phase shift between the two signals.
- The phase shift value obtained signifies the delay or advancement of the output waveform relative to the input waveform. This information is crucial in understanding the circuit's response to the input signal.

3. Interpretation of Phase Shift:

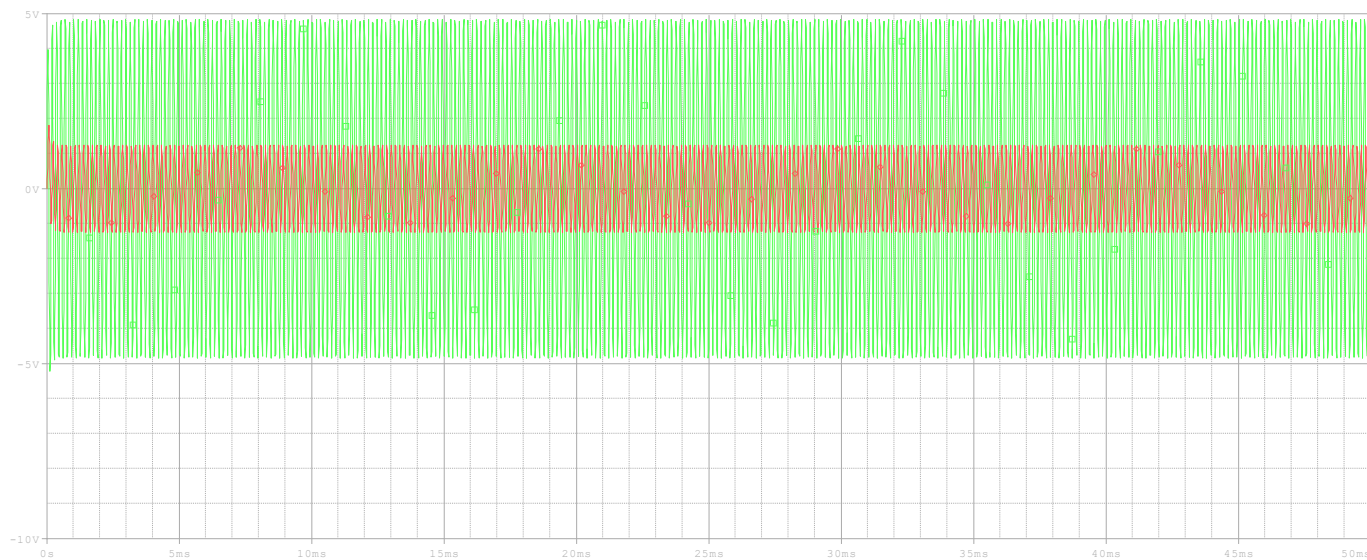
- A phase shift of 1.8° (or any specific value calculated) indicates the temporal displacement between $V_{int}(t)$ and $V_{out}(t)$. This phase shift can result from various factors such as circuit components, frequency response, and signal processing.
- Understanding the phase relationship between input and output signals is essential for analyzing the circuit's behavior, especially in applications where phase alignment is critical.

4. We want to change the input frequency three times, and make it equal to: $f_2 = (\text{your ID number} / 500) \text{ Hz}$, for example, $1219999/500 = 2439.998 \text{ Hz}$ $f_3 = (\text{your ID number} / 200) \text{ Hz}$ $f_4 = (\text{your ID number}) \text{ Hz}$ For each time, draw $V_{out}(t)$, compare and discuss the results obtained.

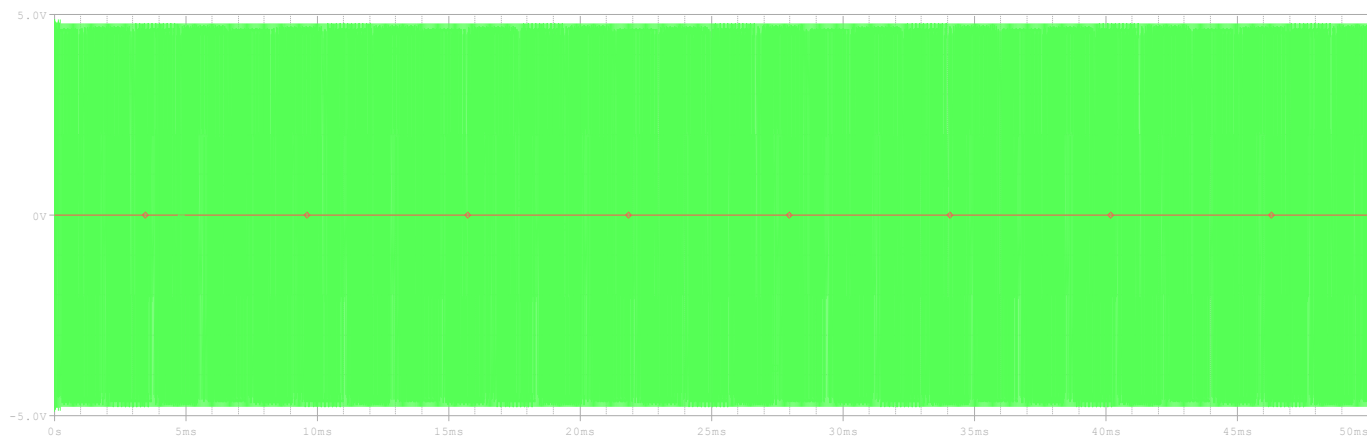
When $f_2 = 1222654/500 = 2445.308$



When $f_3 = 1222654/200 = 6113.27$



When $f_4 = 1222654$



- Changing the input frequency to f_2 , f_3 , and f_4 provided valuable insights into how the circuit responds to different frequency stimuli.
- By plotting $V_{out}(t)$ for each frequency change and comparing the results, we could observe variations in amplitude, phase shift, and waveform shape. These differences reflect the circuit's frequency-dependent characteristics.

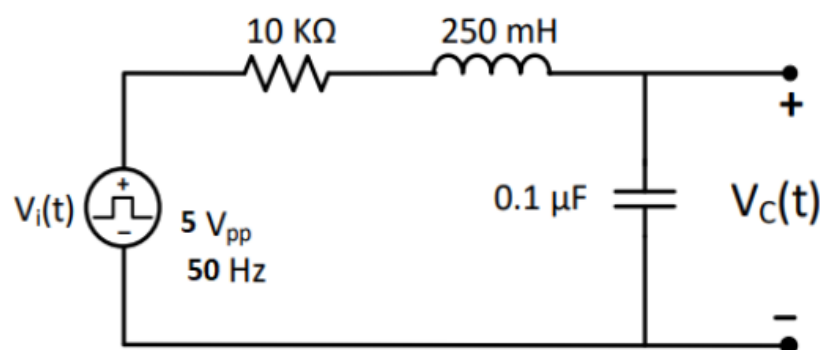
Discussion of Frequency Response:

- The comparison of $V_{out}(t)$ at different frequencies revealed how the circuit's output is influenced by changes in the input frequency. Variations in frequency can impact the circuit's response, resonance behavior, and filtering characteristics.
- Analyzing the results obtained at different frequencies helps in understanding the circuit's frequency response and identifying any frequency-dependent phenomena.

Question #2:

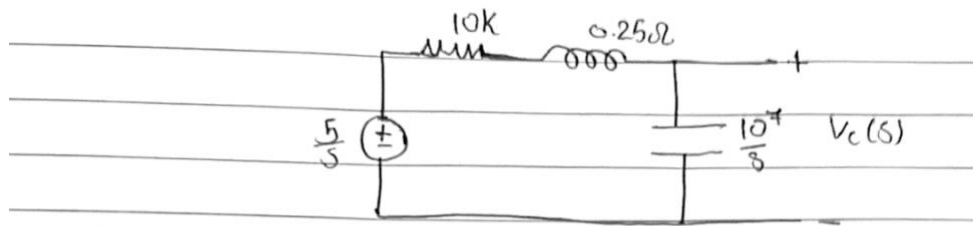
Second-Order RLC Circuit Analysis

it:



Part A:

Find $V_c(t)$ using Laplace transform but assume the input voltage $V_i(t) = 5$ volt (step function)



$$V_c(s) = \frac{10^{-4}s}{10 \times 10^3 + 0.25s + 10^{-4}/s} \times \frac{5}{s}$$

$$= \frac{5 \times 10^{-4}}{s(0.25s^2 + 10 \times 10^3s + 10^7)}$$

$$= \frac{20 \times 10^{-7}}{s(s^2 + 40 \times 10^3s + 4 \times 10^7)}$$

$$= \frac{K_1}{s} + \frac{K_2s + K_3}{s^2 + 40 \times 10^3s + 4 \times 10^7}$$

$$20 \times 10^{-7} = K_1s^2 + 40 \times 10^3K_1s + 4 \times 10^7K_1 + K_2s^2 + K_3s$$

$$4 \times 10^7K_1 = 20 \times 10^{-7} \Rightarrow A = 5$$

$$A + B = 0 \Rightarrow B = -5$$

$$40 \times 10^3A + C = 0 \Rightarrow C = -2 \times 10^5$$

$$V_c(s) = \frac{5}{s} + \frac{-5s - 2 \times 10^5}{s^2 + 40 \times 10^3s + 4 \times 10^7}$$

$$\frac{-b \pm \sqrt{b^2 - 4ac}}{2a} \rightarrow s_1 = -1026.3, s_2 = -38973.6$$

$$\frac{-5s - 2 \times 10^5}{s^2 + 40 \times 10^3s + 4 \times 10^7} = \frac{X_1}{s + 1026.3} + \frac{X_2}{s + 38973.6}$$

$$X_1 \Big|_{s=-1026.3} = -5.135 \quad X_2 \Big|_{s=-38973.6} = 0.135$$

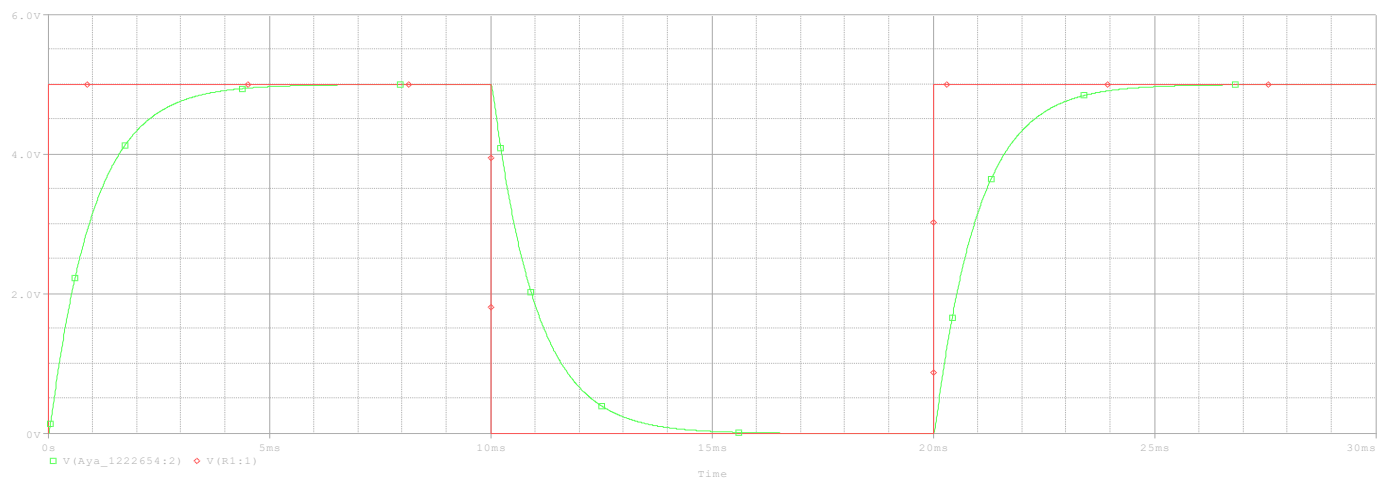
$$V_c(s) = \mathcal{L}^{-1} \left\{ \frac{5}{s} - \frac{5.135}{s + 1026.3} + \frac{0.135}{s + 38973.6} \right\}$$

$$V_c(t) = (5 - 5.135e^{-1026.3t} + 0.135e^{-38973.6t}) u(t) \text{ V}$$

Part B:

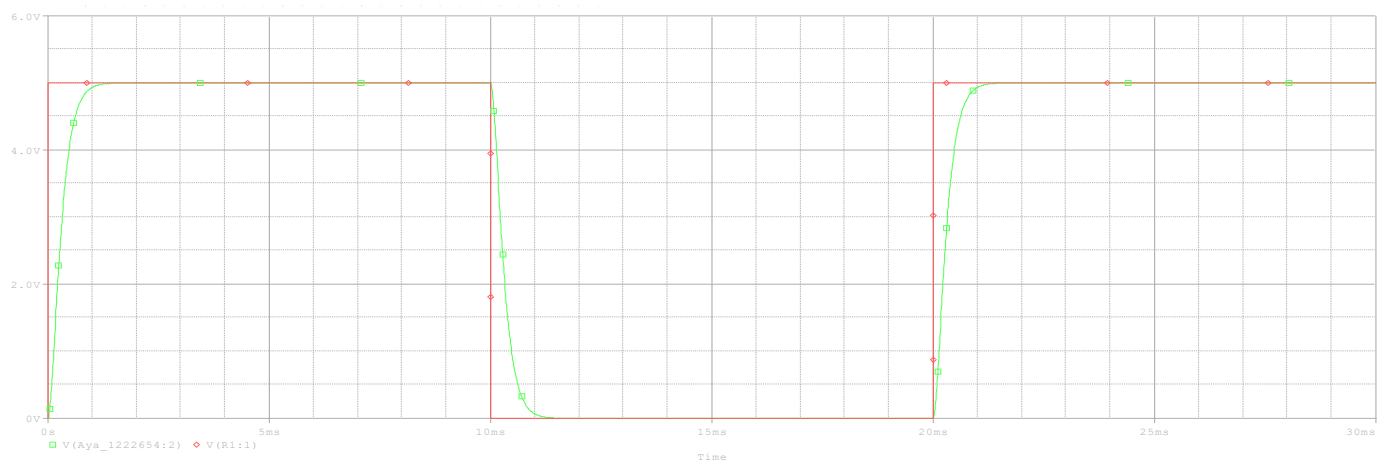
The input voltage is square signal with 5 Vpeak-peak (0 V to 5 V) and frequency of 50Hz.

1. Use Pspice software to plot both $V_i(t)$ and $V_c(t)$ (on the same graph).



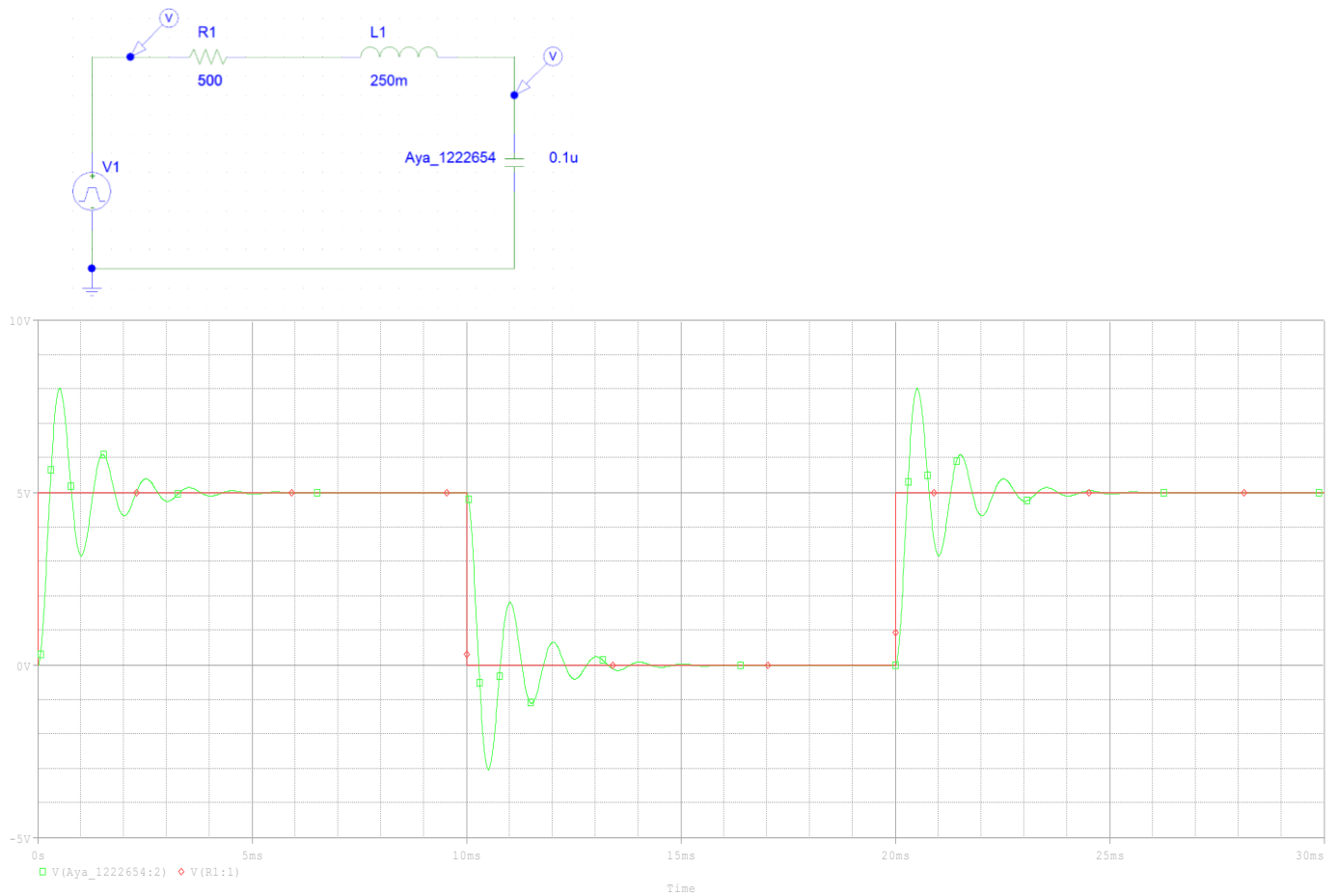
Based on the graph, exhibits characteristics of an Over-damping response

2. Change the Value of R to 3.162 k Ω , repeat step 1.



Based on the graph, exhibits characteristics of an critical-damping response

3. Change the Value of R to 500 Ω , repeat step 1.



Based on the graph, exhibits characteristics of an underdamped response

4. Comment on each result: is it over-damping, critical-damping, or under-damping response.

Over-damping: The system returns to equilibrium without oscillating but takes a longer time to reach stability.

Critical-damping: The system returns to equilibrium as quickly as possible without oscillating.

Under-damping: The system oscillates before settling to the final value.