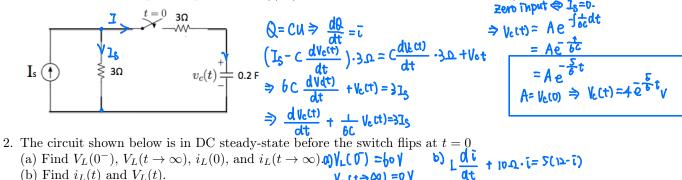
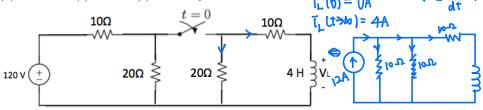
Zhejiang University – University of Illinois at Urbana-Champaign Institute

ECE-210 Analog Signal Processing Spring 2022 Homework #4: Submission Deadline 16 th March (10:00 PM)

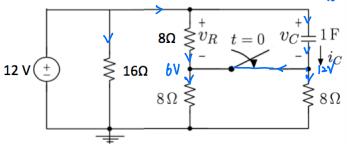
1. In the next circuit, v(t) = 4V for t < 0. Determine v(t) for t > 0 after the switch is closed, and identify the zero-state and zero-input components of v(t). (Hint: I_s donates a DC current source)



- - V_(t→∞) =0 Y
 - (b) Find $i_L(t)$ and $V_L(t)$.
 - (c) Sketch $i_L(t)$ and $V_L(t)$. Identify discontinuities. $A(I) = (0) \cup I$



- 3. The circuit shown below has been in DC steady-state before the switch closes at t=0s. Find and sketch V
- $V_c(t)$ and $i_c(t)$. >> 16= PV



$$c \frac{dV_c}{dt} + \frac{8 \cdot D}{V_c} = \frac{8 \cdot D}{12 \cdot V_c} + \frac{8 \cdot D}{12 \cdot V_c}$$

$$C\frac{d v_c}{dt} + \frac{3 v_c}{8} = 3$$

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$$V_c = V_R$$

⇒
$$V_c = V_R = \int_{0}^{3} \frac{dt}{dt} dt + t = \frac{3t}{8}$$

= $(8e^{\frac{1}{8}} + c)e^{\frac{3t}{8}}$
= $8 + ce^{\frac{1}{8}}V$

4. Assume linear operation and $V_c(0) = -1V$, determine $V_0(t)$ in the following circuit:

as
$$V_c(0) = PV = C = 4$$

 $V_c = 8 + 4e^{-\frac{3t}{6}} cV$

$$V_{0}(t) |_{t=0} = -4V$$

$$\Rightarrow \frac{4 \cdot V_0(t)}{2 + \Omega} = \frac{V_0(t) - V_0(t)}{2 + \Omega} \Rightarrow V_0(t) = 2V_0(t) - 4$$

$$-C \frac{d V_{c}(t)}{olt} = \frac{V_{c}(t)}{R} \Rightarrow \frac{d V_{c}(t)}{dt} + \frac{1}{RC} V_{c}(t) = 0$$

$$\Rightarrow V_{c}(t) = Ae^{-\frac{1}{RC}t}$$

$$\Rightarrow V_0(t) = -2e^{-\frac{t}{RC}} - 4 V$$

$$= -2e^{-t\sigma_{00}t} - 4(V)$$