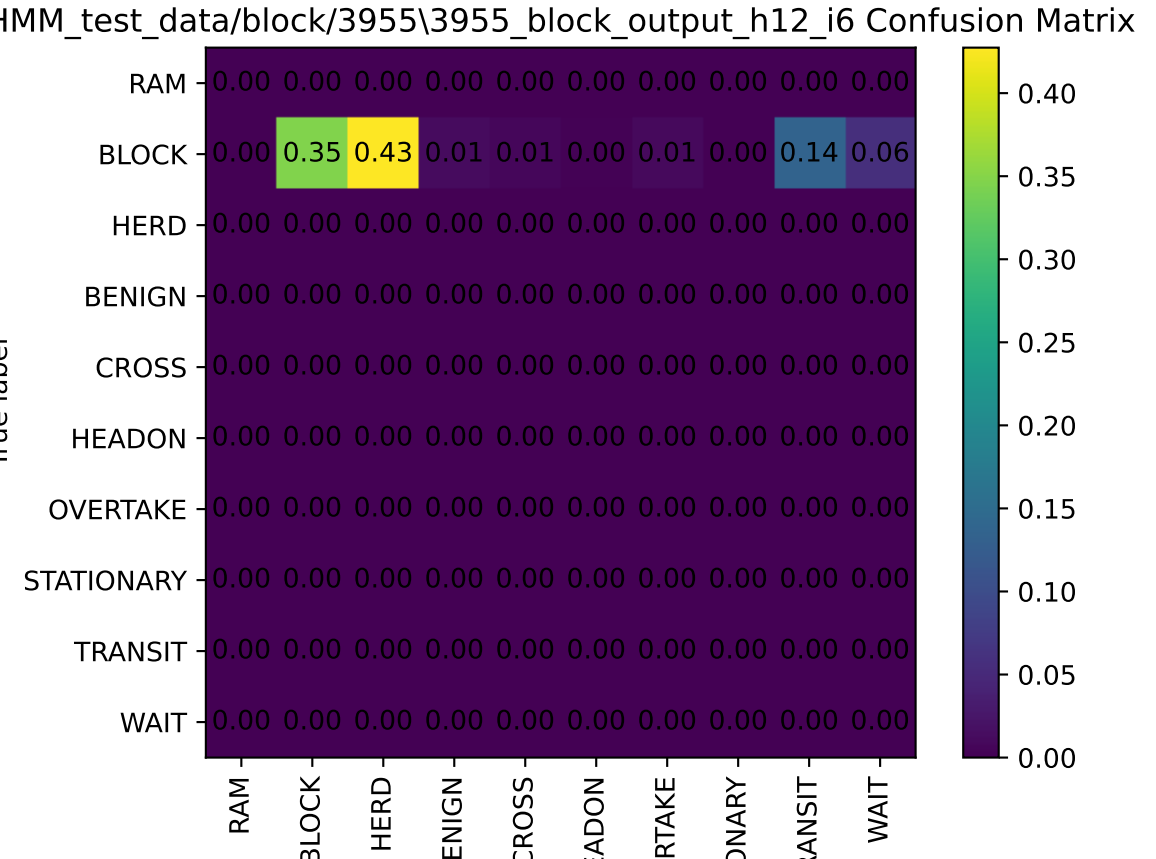
HMM_test_data/block/3955\3955_block_output_h12_i6 Confusion Matrix
RAM
BLOCK
HERD
BENIGN
CROSS
HEADON
OVERTAKE
STATIONARY
TRANSIT
WAIT
0.00 0.00 0.00 0.00 0.00 0.00 0.00 0.00 0.00 0.00
0.00 0.35 0.43 0.01 0.01 0.00 0.01 0.00 0.14 0.06
0.40
0.35
0.30
0.25
0.20
0.15
0.10
0.05
0.00