

SuperSignalizacjaDomiLechLuka ▶ PLC_1 [CPU 1215C AG/D/RIy]

Topology view | Network view | Device view

PLC_1 [CPU 1215C]

Rack_0

Slot	Module	Slot	I address	Q address	Type
103		102			
101		101			
1	PLC_1	1			CPU 121...
2	DI 14/DQ 10_1	11	0...1	0...1	DI 14/DQ...
3	AI 2/AQ 2_1	12	64...67	64...67	AI 2/AQ 2
4		13			
5	HSC_1	116	1000...10...		HSC
6	HSC_2	117	1004...10...		HSC
7	HSC_3	118	1008...10...		HSC
8	HSC_4	119	1012...10...		HSC
9	HSC_5	120	1016...10...		HSC
	HSC_6	121	1020...10...		HSC
	Pulse_1	132	1000...10...		Pulse ge...
	Pulse_2	133	1002...10...		Pulse ge...
	Pulse_3	134	1004...10...		Pulse ge...
	Pulse_4	135	1006...10...		Pulse ge...
	OPC UA	1254			OPC UA
	PROFINET interface_1	1 X1			PROFINE...
		2			
		3			
		4			

PROFINET interface_1 [Module]

General | IO tags | System constants | Texts

General

Ethernet addresses

Time synchronization

Operating mode

Advanced options

Web server access

Ethernet addresses

Interface networked with

Subnet: PN/IE_1

Add new subnet

IP protocol

☒ Set IP address in the project

IP address: 192 . 168 . 22 . 139

Subnet mask: 255 . 255 . 255 . 0

☐ Use router

Router address: 0 . 0 . 0 . 0

☐ IP address is set directly at the device

SuperSygnalizacjaDomilechLuka ▶ PLC_2 [CPU 1215C AC/DC/Rly]

PLC_2 [CPU 1215C]

Topology view | Network view | Device view

Device overview

Module	Slot	I address	Q address	Type
PLC_2	1			CPU 1215C
DI 14/DQ 10_1	1.1	0...1	0...1	DI 14/DQ 1
AI 2/AQ 2_1	1.2	64...67	64...67	AI 2/AQ 2
	1.3			
HSC_1	1.16	1000...10...		HSC
HSC_2	1.17	1004...10...		HSC
HSC_3	1.18	1008...10...		HSC
HSC_4	1.19	1012...10...		HSC
HSC_5	1.20	1016...10...		HSC
HSC_6	1.21	1020...10...		HSC
Pulse_1	1.32		1000...10...	Pulse gen
Pulse_2	1.33		1002...10...	Pulse gen
Pulse_3	1.34		1004...10...	Pulse gen
Pulse_4	1.35		1006...10...	Pulse gen
OPC UA	1.254			OPC UA
PROFINET interface_1	1 X1			PROFINET
	2			
	3			

PROFINET interface_1 (Module)

General | IO tags | System constants | Texts

Ethernet addresses

Interface networked with

Subnet: PN/IE_1

Add new subnet

IP protocol

☒ Set IP address in the project

IP address: 192 . 168 . 22 . 138

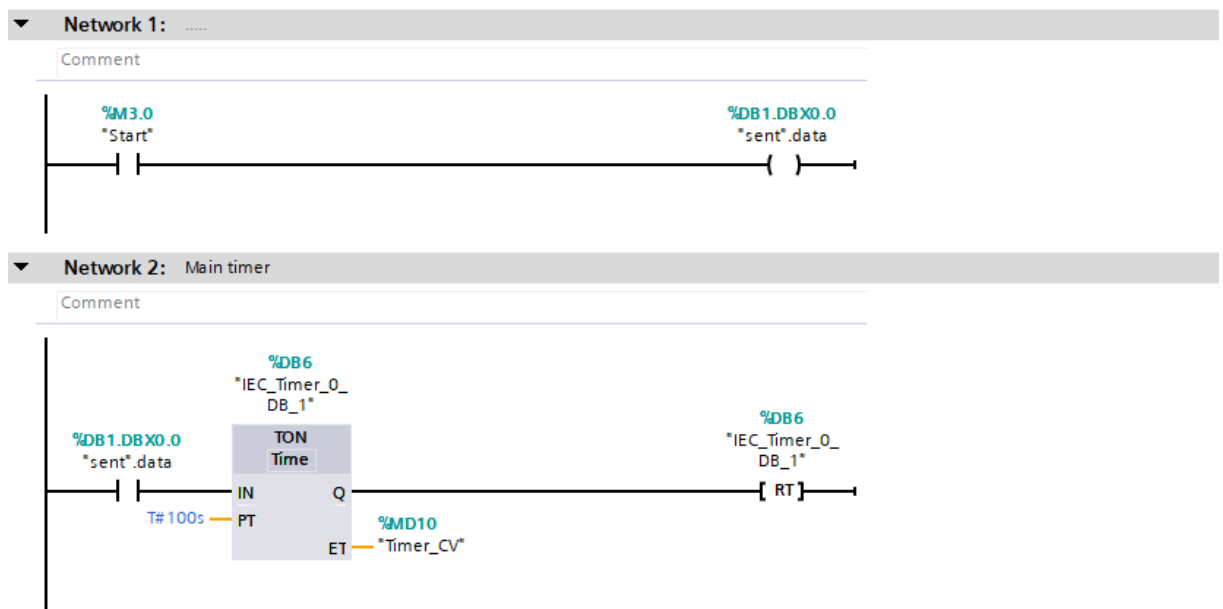
Subnet mask: 255 . 255 . 255 . 0

☐ Use router

Router address: 0 . 0 . 0 . 0

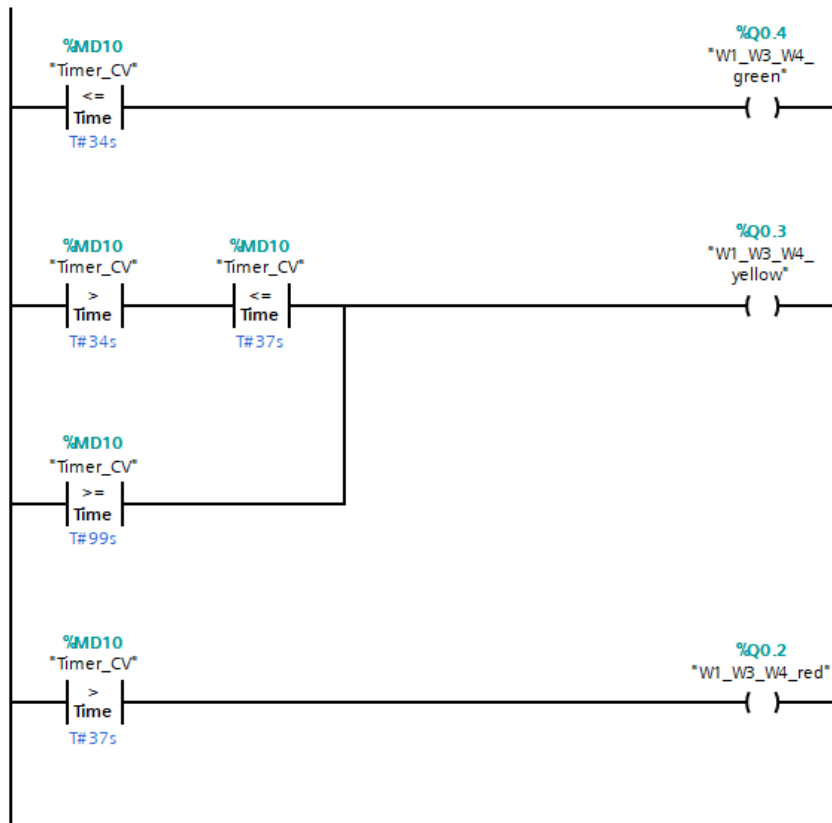
☐ IP address is set directly at the device

Main 1:



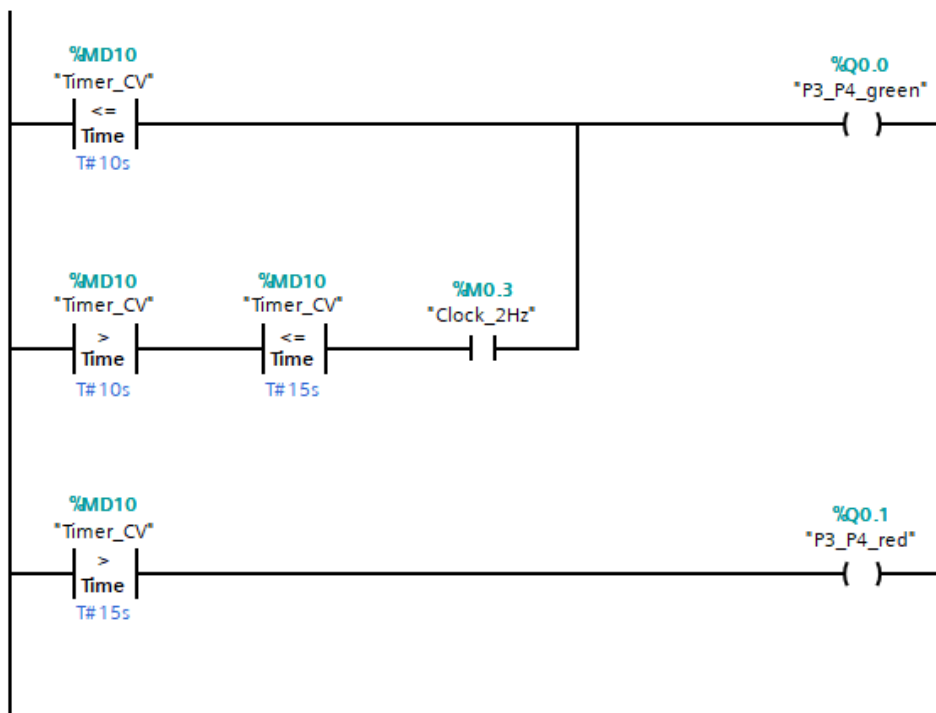
Network 3: W1, W2 and W4

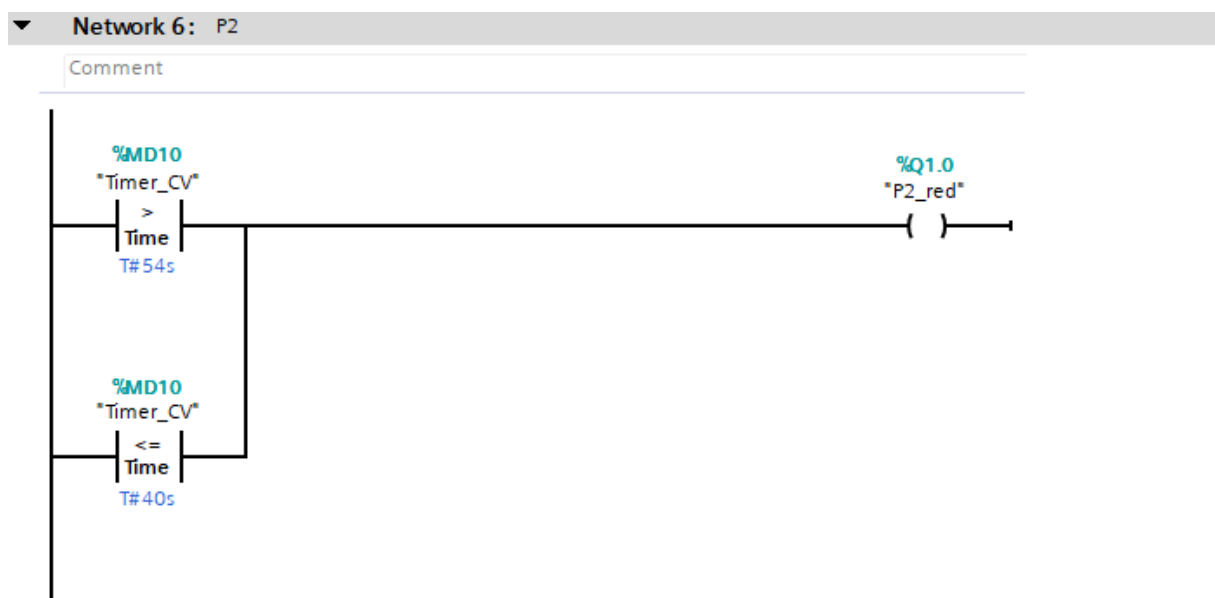
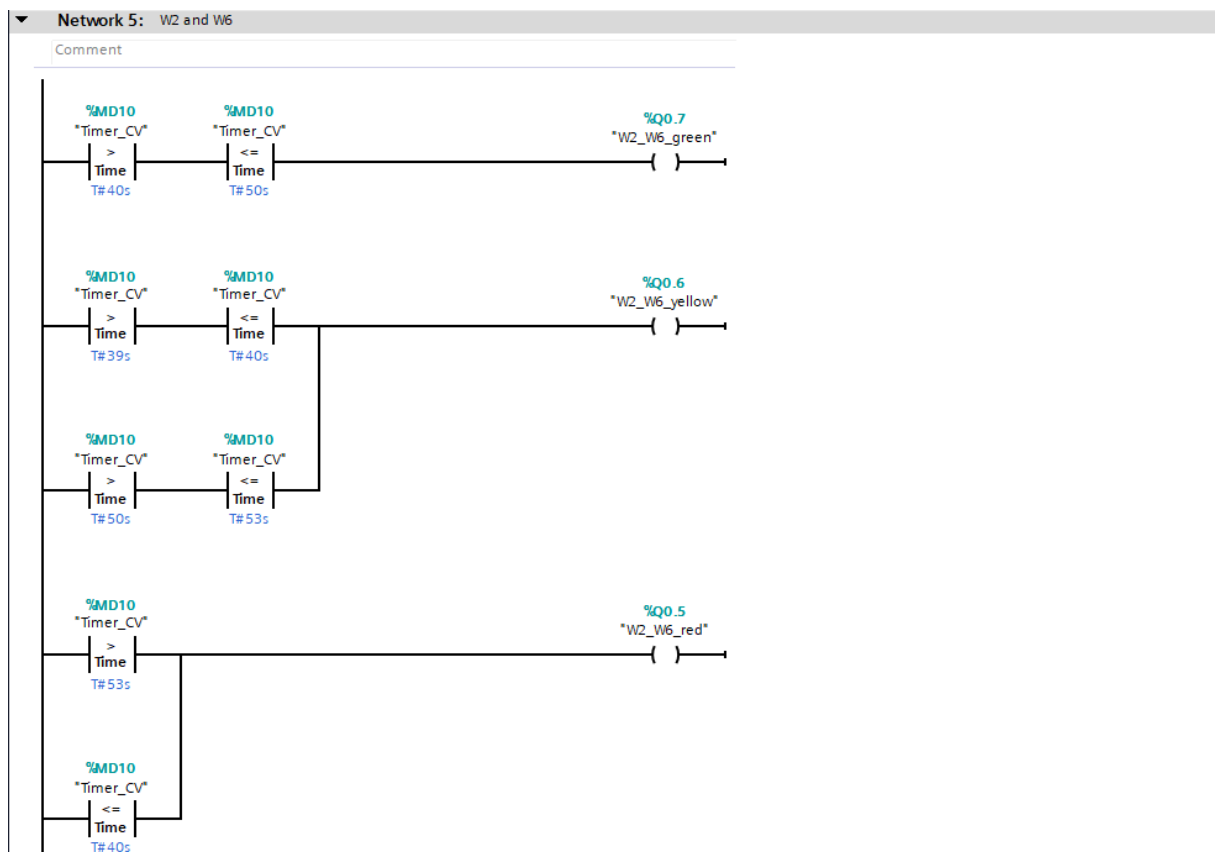
Comment



Network 4: P3 and P4

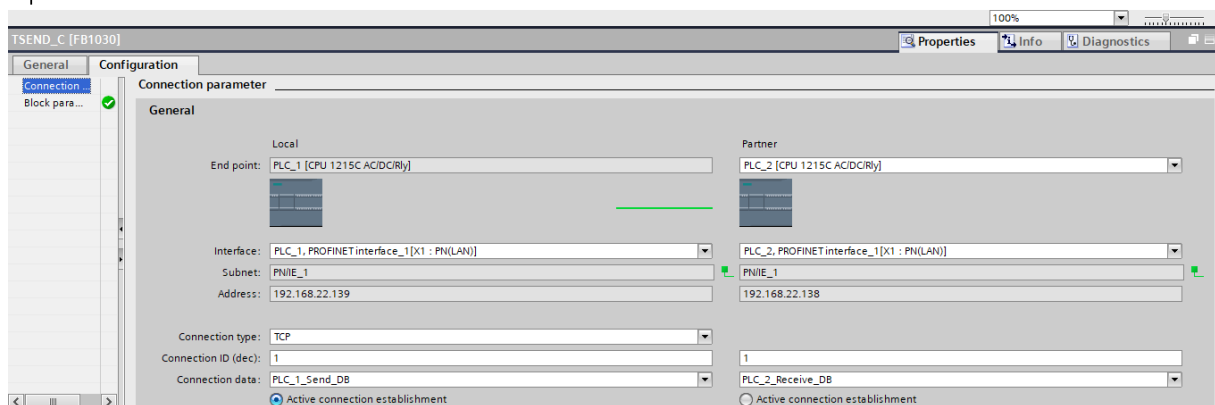
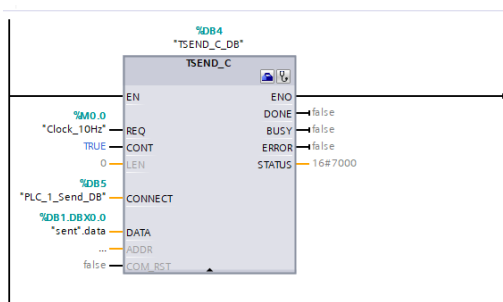
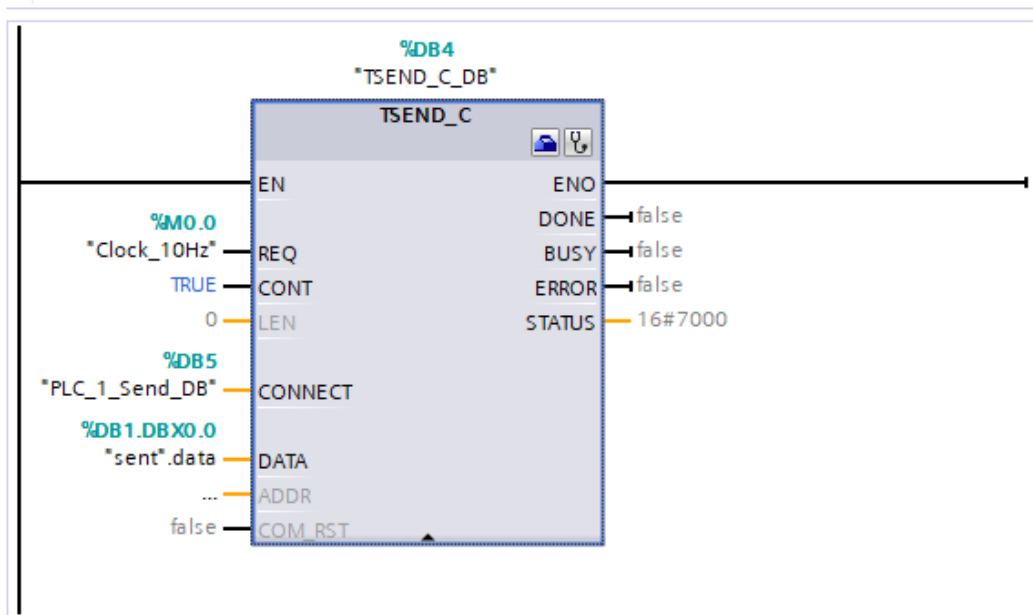
Comment



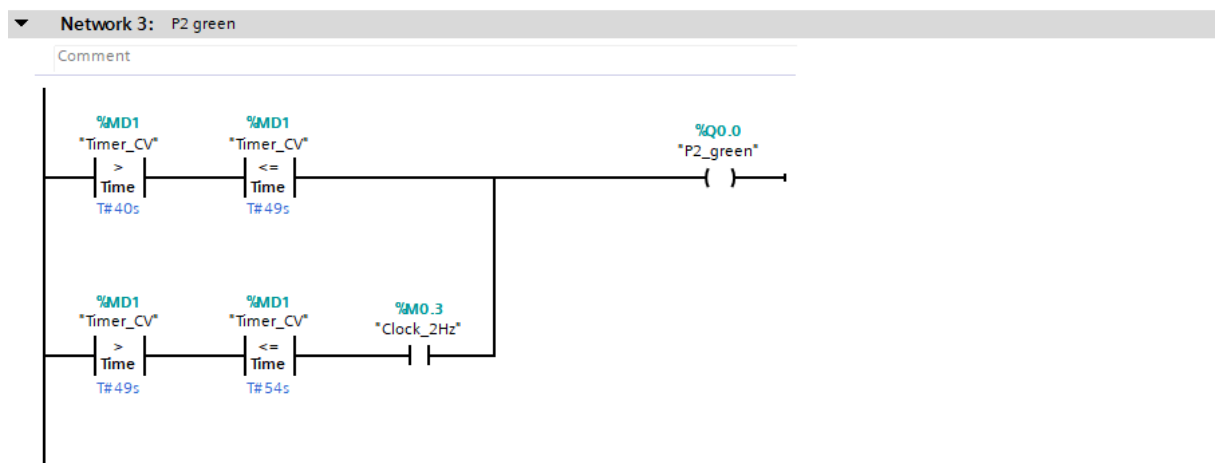
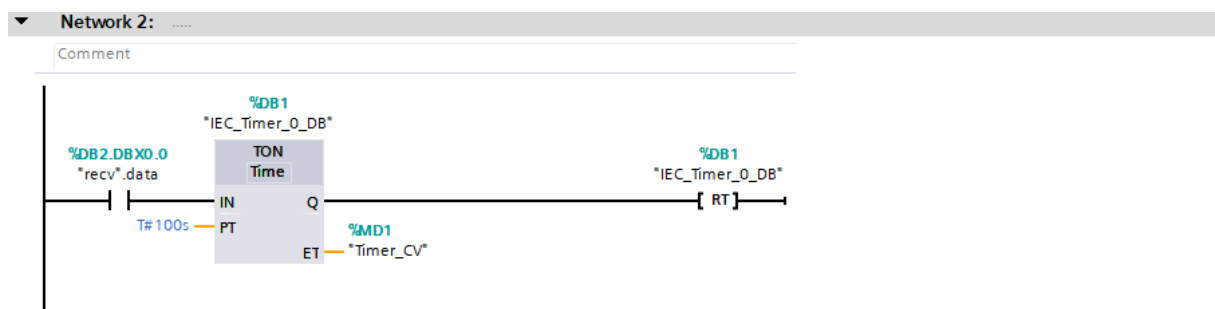
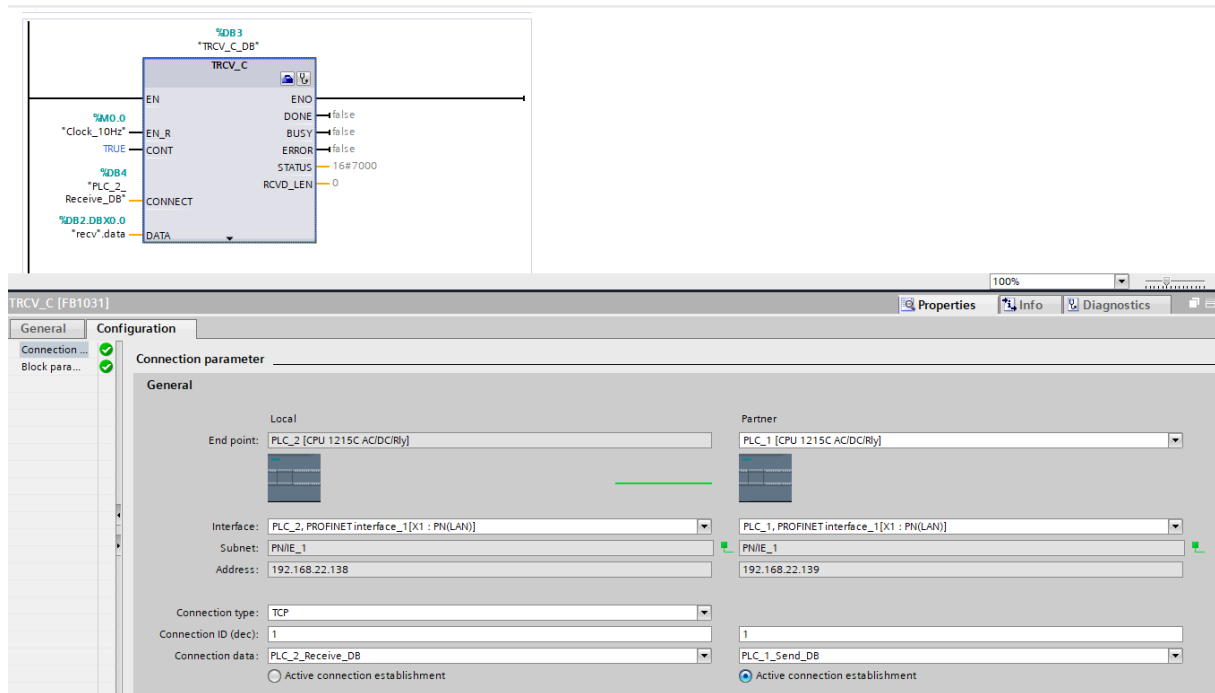


▼ Network 7:

Comment

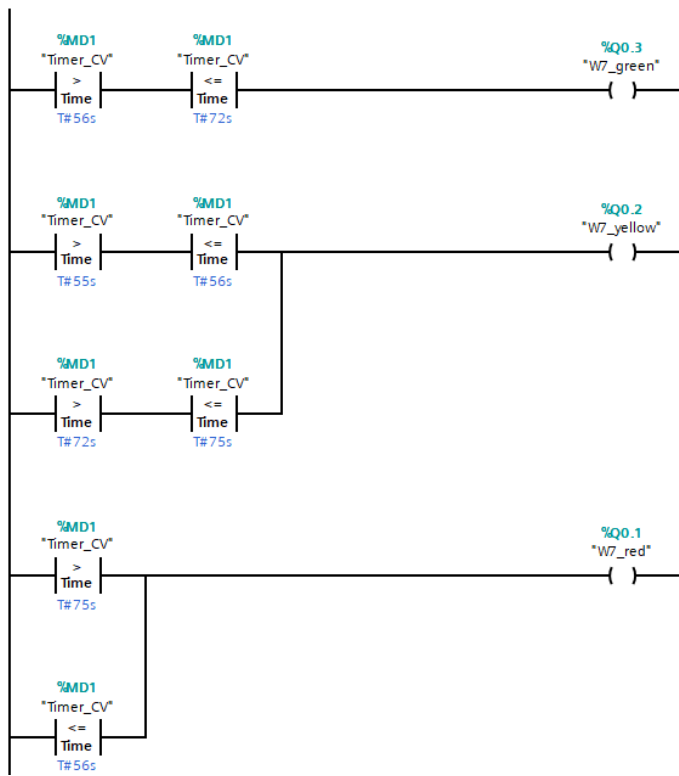


Main 2:



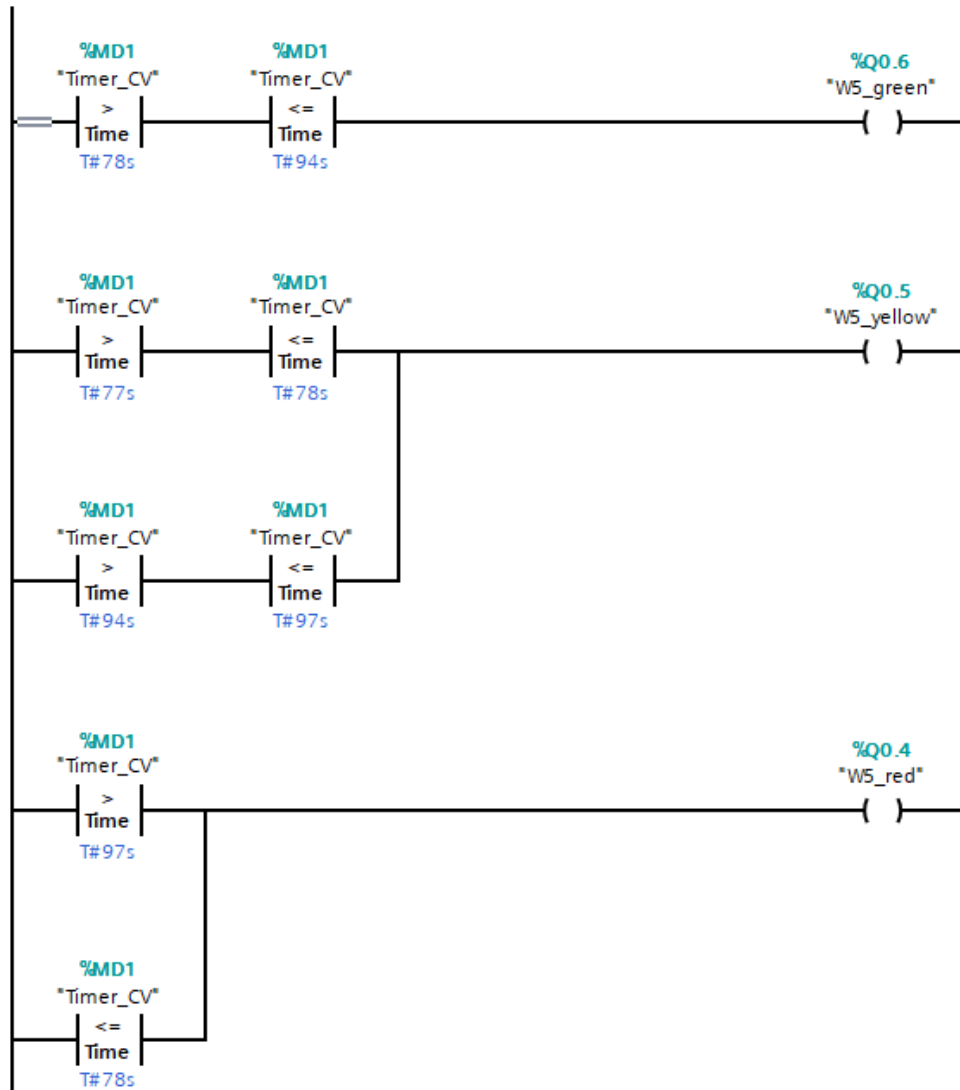
Network 4: W7

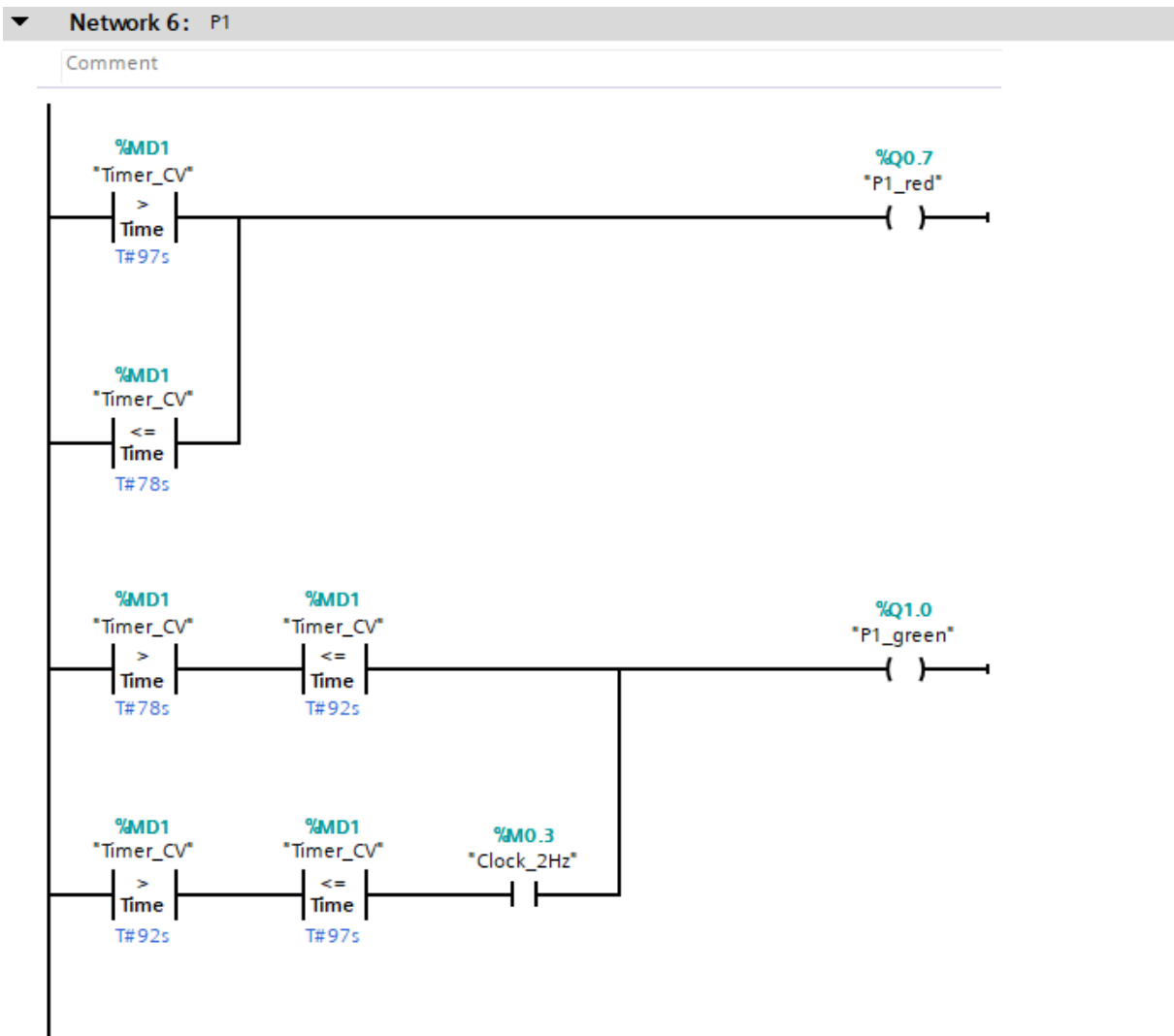
Comment



▼ Network 5: W5

Comment





Sent:

Project tree									
SuperSignalizacjaDomilechLuka ▶ PLC_1 [CPU 1215C AC/DC/Rly] ▶ Program blocks ▶ sent [DB1]									
<div> <div> <div>Devices</div> <div> <div>SuperSignalizacjaDomilechLuka</div> <div> <div>Add new device</div> <div>Devices & networks</div> <div>PLC_1 [CPU 1215C AC/DC/Rly]</div> <div> <div>Device configuration</div> <div>Online & diagnostics</div> <div>Program blocks</div> <div> <div>Add new block</div> <div>Main [DB1]</div> <div>sent [DB1]</div> <div>System blocks</div> <div>Technology objects</div> <div>External source files</div> <div>PLC tags</div> <div> <div>Show all tags</div> <div>Add new tag table</div> <div>Default tag table [50]</div> <div>PLC data types</div> <div>Watch and force tables</div> <div>Online backups</div> <div>Traces</div> <div>OPC UA communication</div> <div>Device proxy data</div> <div>Program info</div> <div>PLC alarm text lists</div> <div>Local modules</div> <div>PLC_2 [CPU 1215C AC/DC/Rly]</div> <div> <div>Device configuration</div> <div>Online & diagnostics</div> <div>Program blocks</div> <div> <div>Add new block</div> <div>Main [DB1]</div> <div>recv [DB2]</div> </div> </div> </div> </div> </div> </div> </div> </div></div>									
sent									
Name	Data type	Offset	Start value	Retain	Accessible f...	Write...	Visible in ...	Setpoint	Comment
1 Static									
2 data	Bool	0.0	false		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	

Rcv:

Project tree: SuperSygnalizacjaDomiLechLuka > PLC_2 [CPU 1215C AC/DC/Rly] > Program blocks > recv [DB2]

Devices: PLC_2 [CPU 1215C AC/DC/Rly]

recv

	Name	Data type	Offset	Start value	Retain	Accessible f...	Writa...	Visible in ...	Setpoint	Comment
1	Static									
2	data	Bool	0.0	false		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	

Def:

Project tree: SuperSygnalizacjaDomiLechLuka > PLC_1 [CPU 1215C AC/DC/Rly] > PLC tags > Default tag table [50]

Devices: SuperSygnalizacjaDomiLechLuka

Default tag table

	Name	Data type	Address	Retain	Acces...	Writa...	Visibl...	Comment
1	Clock_Byte	Byte	%MB0	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
2	Clock_10Hz	Bool	%M0.0	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
3	Clock_5Hz	Bool	%M0.1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
4	Clock_2.5Hz	Bool	%M0.2	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
5	Clock_2Hz	Bool	%M0.3	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
6	Clock_1.25Hz	Bool	%M0.4	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
7	Clock_1Hz	Bool	%M0.5	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
8	Clock_0.625Hz	Bool	%M0.6	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
9	Clock_0.5Hz	Bool	%M0.7	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
10	P3_P4_green	Bool	%Q0.0	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
11	P3_P4_red	Bool	%Q0.1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
12	W1_W3_W4_red	Bool	%Q0.2	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
13	W1_W3_W4_yellow	Bool	%Q0.3	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
14	W1_W3_W4_green	Bool	%Q0.4	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
15	W2_W6_red	Bool	%Q0.5	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
16	W2_W6_yellow	Bool	%Q0.6	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
17	W2_W6_green	Bool	%Q0.7	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
18	P2_red	Bool	%Q1.0	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
19	Start	Bool	%M0.0	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
20	Timer_CV	DWord	%MD10	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
21	<Add new>			<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	

Project tree
SuperSignalizaDomiLechLuka > PLC_2 [CPU 1215C AC/DC/Rly] > PLC tags > Default tag table [49]

Devices

Watch and force tables

Online backups

Traces

OPC UA communication

Device proxy data

Program info

PLC alarm text lists

Local modules

PLC_2 [CPU 1215C AC/DC/Rly]

Device configuration
 Online & diagnostics

Program blocks

Add new block

Main [OB1]
 recv [DB2]

System blocks
 Technology objects

External source files

PLC tags

Show all tags
 Add new tag table

Default tag table [49]

PLC data types
 Watch and force tables
 Online backups
 Traces
 OPC UA communication
 Device proxy data
 Program info
 PLC alarm text lists
 Local modules

PLC_2 [CPU 1215C AC/DC/Rly]

Ungrouped devices

Default tag table

	Name	Data type	Address	Retain	Acces...	Writa...	Visibl...	Comment
1	Clock_Byte	Byte	%M0.0	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
2	Clock_10Hz	Bool	%M0.0	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
3	Clock_5Hz	Bool	%M0.1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
4	Clock_2.5Hz	Bool	%M0.2	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
5	Clock_2Hz	Bool	%M0.3	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
6	Clock_1.25Hz	Bool	%M0.4	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
7	Clock_1Hz	Bool	%M0.5	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
8	Clock_0.625Hz	Bool	%M0.6	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
9	Clock_0.5Hz	Bool	%M0.7	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
10	P2_green	Bool	%Q0.0	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
11	W7_red	Bool	%Q0.1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
12	W7_yellow	Bool	%Q0.2	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
13	W7_green	Bool	%Q0.3	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
14	W5_red	Bool	%Q0.4	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
15	W5_yellow	Bool	%Q0.5	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
16	W5_green	Bool	%Q0.6	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
17	P1_red	Bool	%Q0.7	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
18	P1_green	Bool	%Q1.0	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
19	Timer_CV	DWord	%MD1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
20	<Add new>			<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	