

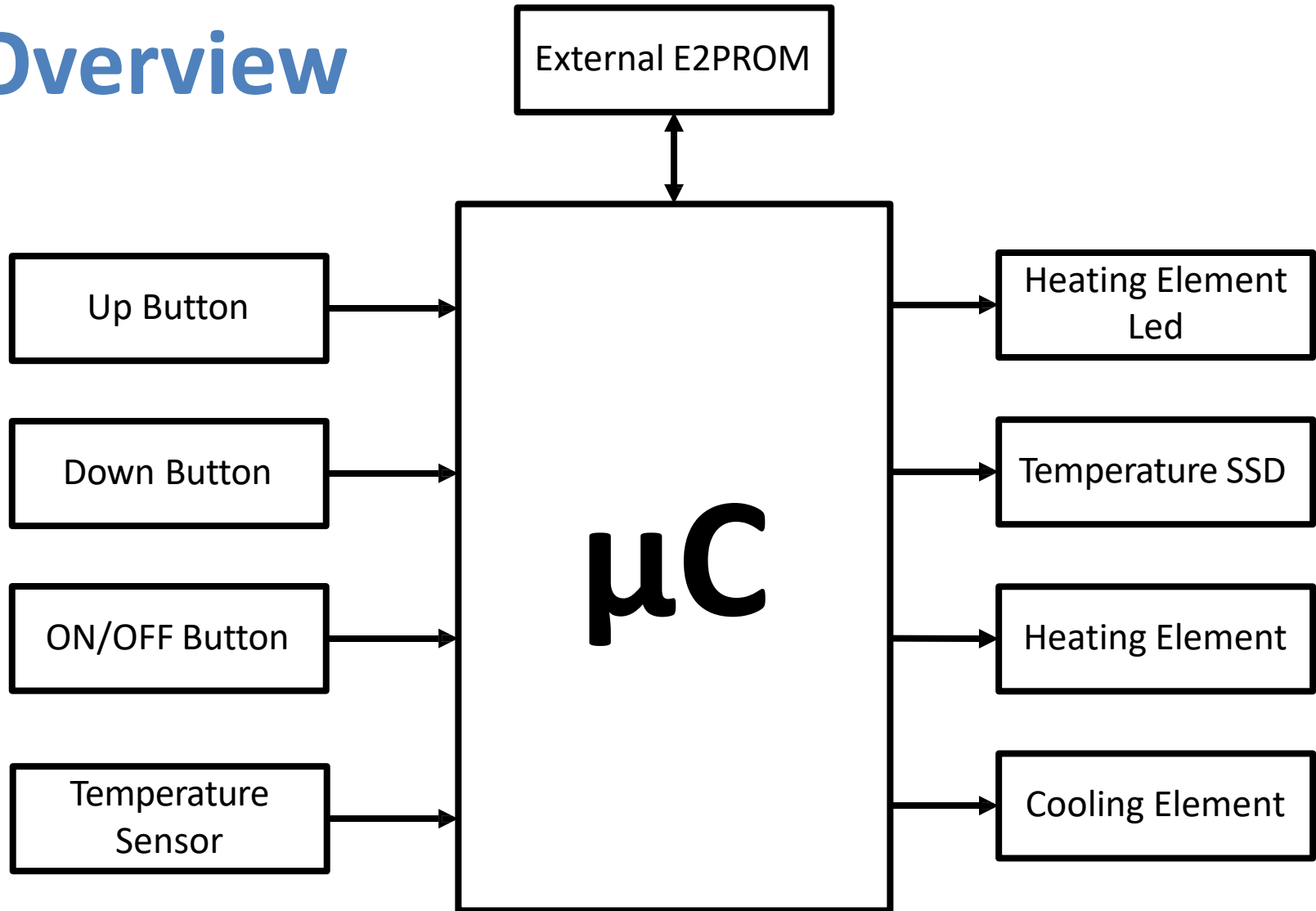
Electric Water Heater

Swift Act challenge

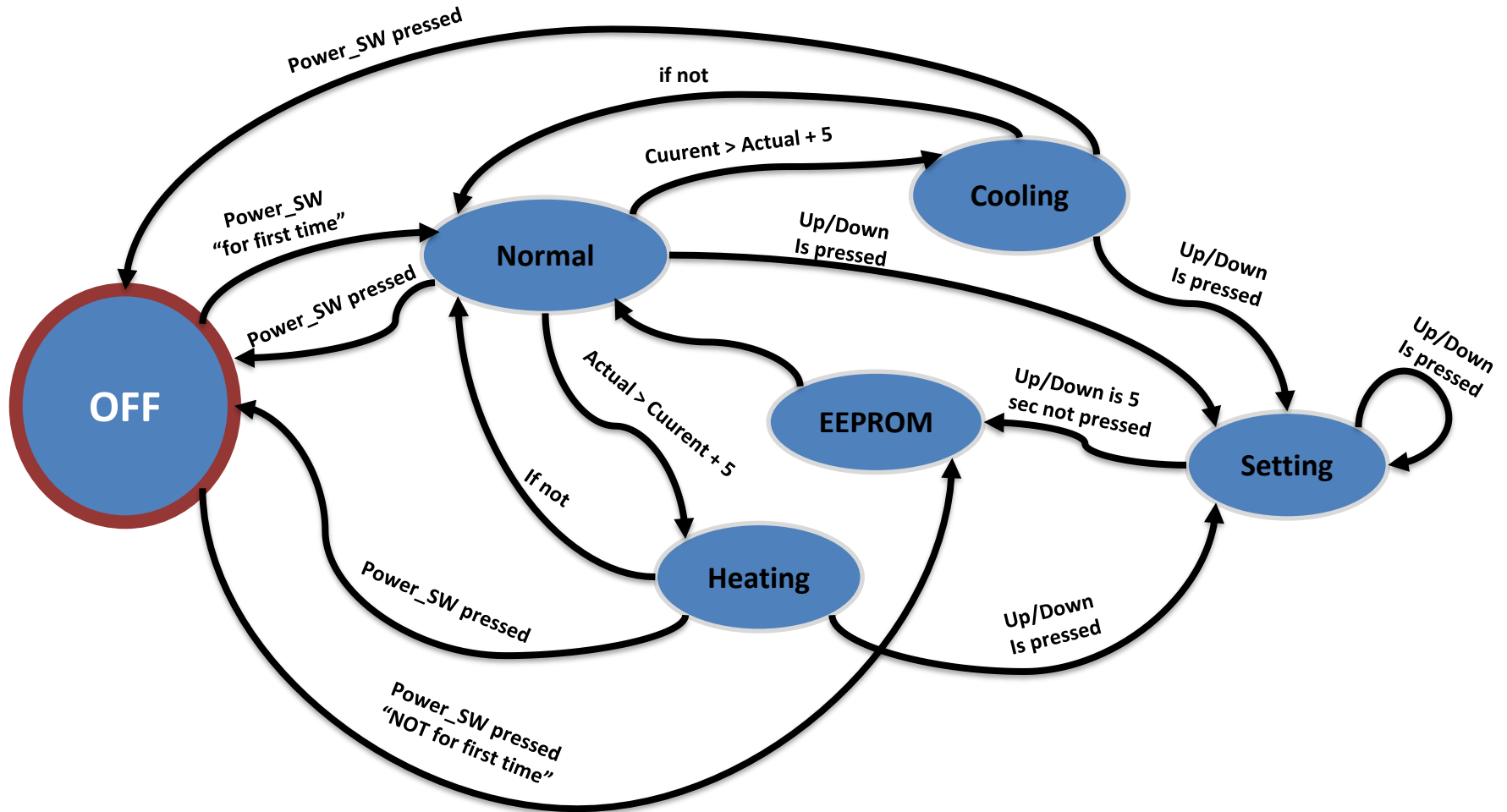
By : Ayman Elhaddad



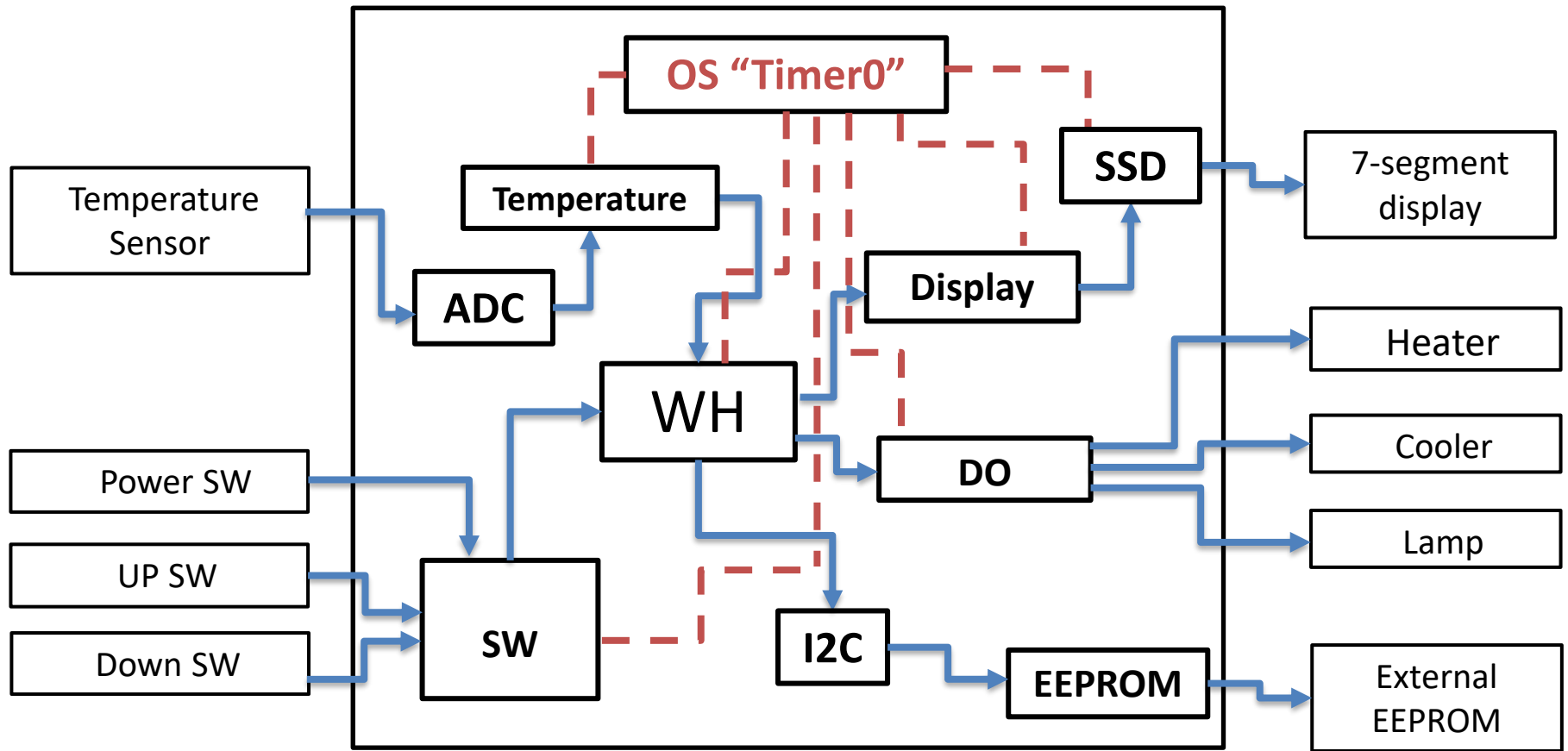
Overview



State Machine



Context-Diagram



Detailed Design

☐ SW

- ☐ SW_Init
- ☐ SW_Update
- ☐ SW_GetState

☐ Timer

- ☐ TMR_Init
- ☐ TMR_start
- ☐ TMR_stop
- ☐ TMR_Update

☐ Tempertaure

- ☐ Tempertaure_Init
- ☐ Tempertaure_Update
- ☐ Tempertaure_GetValue

☐ Display

- ☐ DISP_Init
- ☐ DISP_Update

☐ SSD

- ☐ SSD_Init
- ☐ SSD_Update
- ☐ SSD_SetSymbol
- ☐ SSD_GetSymbol
- ☐ SSD_SetState
- ☐ SSD_GetState



Detailed Design

☐ DO

- ☐ void DO_Init
- ☐ void DO_Update
- ☐ void Set_Led_State
- ☐ void DO_SetState

☐ EEPROM

- ☐ EEPROM_Write
- ☐ EEPROM_Write_Page
- ☐ EEPROM_Read
- ☐ EEPROM_Read_Page

☐ ADC

- ☐ ADC_Init
- ☐ ADC_Convert

☐ WH

- ☐ WH_Update
- ☐ WH_Get_Current_state

☐ I2C

- ☐ I2C_Master_Init
- ☐ I2C_Master_Wait
- ☐ I2C_Master_Start
- ☐ I2C_Master_Stop
- ☐ I2C_ACK
- ☐ I2C_NACK
- ☐ I2C_Master_Write
- ☐ I2C_Read_Byte



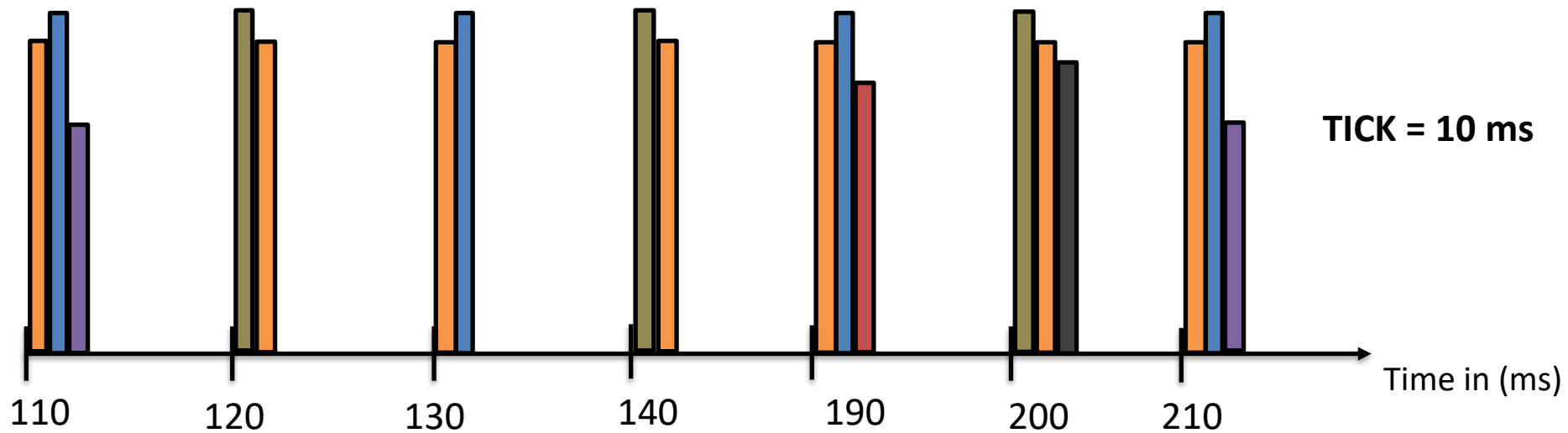
Timing Analysis







Task	Actions	BCET (ms)	WCET(ms)	Period of Action (ms)	Period of task (ms)
SW	Update samples	~0	~0	20	20
	Update SW state	~0	~0	20	
DO	Update DO state	~0	~0	100	100
Display	Update display state	~0	~0	100	100
SSD	Update SSD	~0	~0	10	10
WH	Update WH state	~0	~0	20	20
Temperature	Update Temperature	~0	~0	100	100
Tick (ms)					10
Major Cycle (ms)					100



Schedulability Check

* Tasks blockage occurs at the rise of the system to make sure that the sum of tasks execution times per TICK is less than TICK period



- | | | | | | |
|--|-------------|---------------------------|---|---------|----------------------------|
|  | SW | @20 ms "blockage = 0 ms" |  | WH | @20 ms "blockage = 10 ms" |
|  | Temperature | @100 ms "blockage = 0 ms" |  | Display | @100 ms "blockage = 10 ms" |
|  | SSD | @10 ms "blockage = 0 ms" |  | DO | @100 ms "blockage = 90 ms" |



