

#### **EDUCATION**

# M.Sc. in Electrical Engineering and Information Technology Technical University of Munich, Germany

04/2024 - Present

- Core areas: Electronic Circuits and Systems, Embedded and Computer Systems
- Relevant course contents: fundamental semiconductor process technology, essentials of analog CMOS and digital integrated circuits design, timing analysis and testing of digital circuits, embedded systems design and security

# **B.Sc. in Electrical Engineering and Information Technology**

Technical University of Munich, Germany 11/2020 - 03/2024

- Dissertation topic: Analysis and Verification of Stress Dependence of Silicon Chips Embedded in Polymer Foils on Chip Thickness
- Project title of engineering practice: Code Generator For Instruction Monitoring



#### EXPERIENCE

## **Working Student E-mobility Development in Automotive Industry** ARRK Engineering GmbH, Munich 01/2023 - 10/2023 (14h/week)

- Project: EoL-Test Automation of HiL-Boxes
- Used CanEasy to manage DBC files including messages configuration within CAN/CAN FD buses, to monitor and analyze real-time data flow of boxes, and to tune hardware devices for
- Developed CAPL programs to generate HTML test reports and integrating them into CanEasy as plugins
- Created and documented HW/SW solutions to fully automated testing which increased efficiency and reduced costs significantly



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# **SKILLS**

- MATLAB / LTspice Vivado / CanEasy COMSOL
- C / Python / VHDL Git / CAPL / LaTex
- Mac / Windows Linux

### LANGUAGES

- English C1/C2
- German C1
- Chinese Native

# **INTERESTS**

Photography

Oin tillun