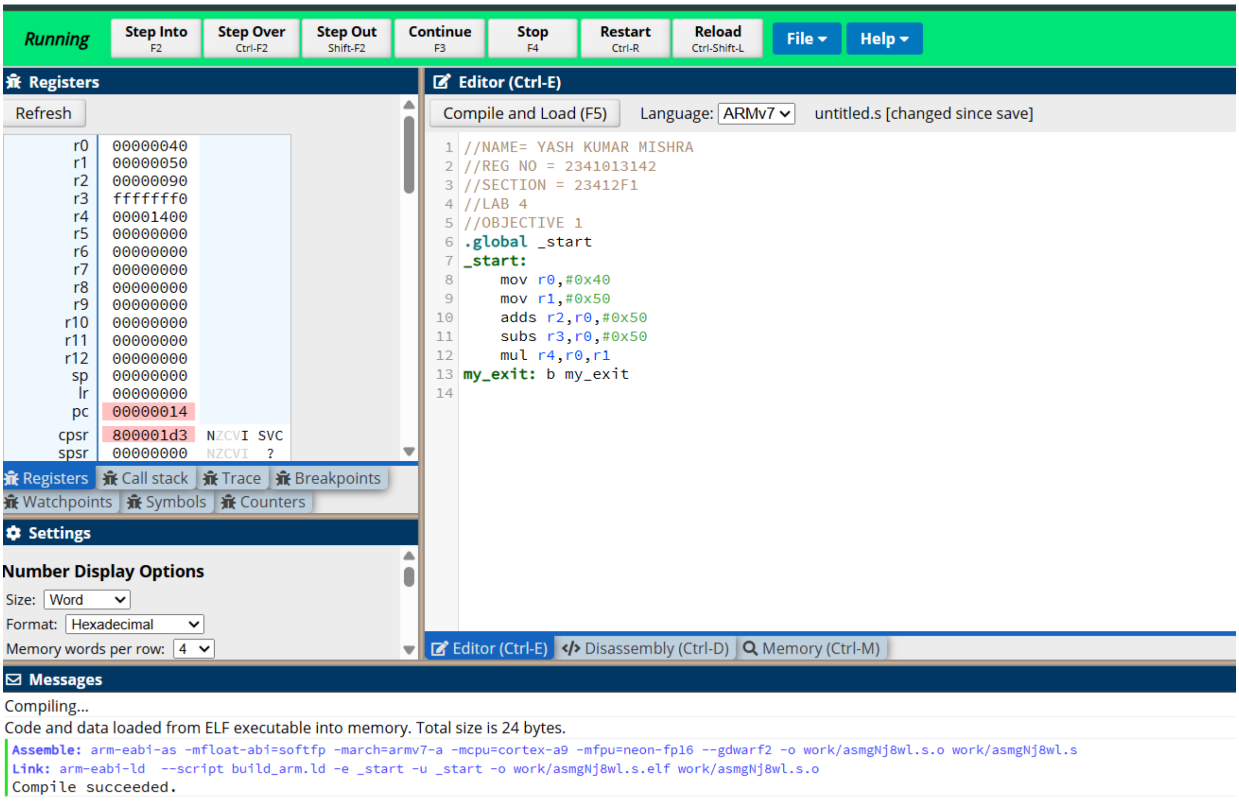


Language: ARMv7 untitled.s [changed since save]

```
r0    00000040
r1    00000050
r2    00000090
r3    fffffff0
r4    00001400
r5    00000000
r6    00000000
r7    00000000
r8    00000000
r9    00000000
r10   00000000
r11   00000000
r12   00000000
sp    00000000
lr    00000000
pc    00000014
cpsr  800001d3  NZCVI SVC
spsr  00000000  NZCVI  ?
```

```
//NAME= YASH KUMAR MISHRA
//REG NO = 2341013142
//SECTION = 23412F1
//LAB 4
//OBJECTIVE 1
.global _start
_start:
    mov r0,#0x40
    mov r1,#0x50
    adds r2,r0,#0x50
    subs r3,r0,#0x50
    mul r4,r0,r1
my_exit: b my_exit
```

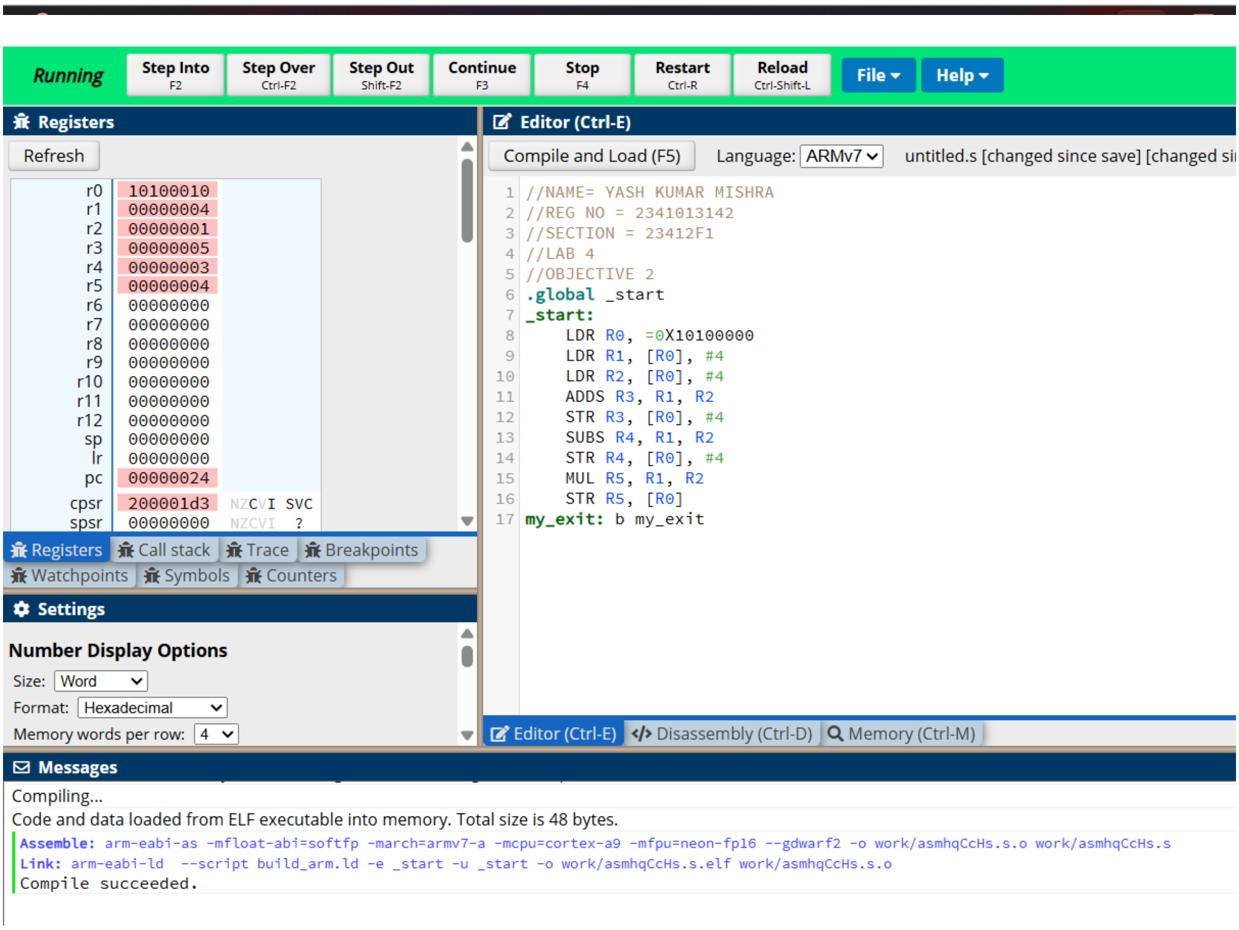
Number Display Options

Size: Word

Format: Hexadecimal

Memory words per row: 4

```
Compiling...
Code and data loaded from ELF executable into memory. Total size is 24 bytes.
Assemble: arm-eabi-as -mfloat-abi=softfp -march=armv7-a -mcpu=cortex-a9 -mfpu=neon-fp16 --gdwarf2 -o work/asmgNj8wl.s.o work/asmgNj8wl.s
Link: arm-eabi-ld  --script build_arm.ld -e _start -u _start -o work/asmgNj8wl.s.elf work/asmgNj8wl.s.o
Compile succeeded.
```



Language: ARMv7 untitled.s [changed since save] [changed si

```
r0    10100010
r1    00000004
r2    00000001
r3    00000005
r4    00000003
r5    00000004
r6    00000000
r7    00000000
r8    00000000
r9    00000000
r10   00000000
r11   00000000
r12   00000000
sp    00000000
lr    00000000
pc    00000024
cpsr  200001d3  NZCVI SVC
spsr  00000000  NZCVI  ?
```

```
//NAME= YASH KUMAR MISHRA
//REG NO = 2341013142
//SECTION = 23412F1
//LAB 4
//OBJECTIVE 2
.global _start
_start:
    LDR R0, =0X10100000
    LDR R1, [R0], #4
    LDR R2, [R0], #4
    ADDS R3, R1, R2
    STR R3, [R0], #4
    SUBS R4, R1, R2
    STR R4, [R0], #4
    MUL R5, R1, R2
    STR R5, [R0]
my_exit: b my_exit
```

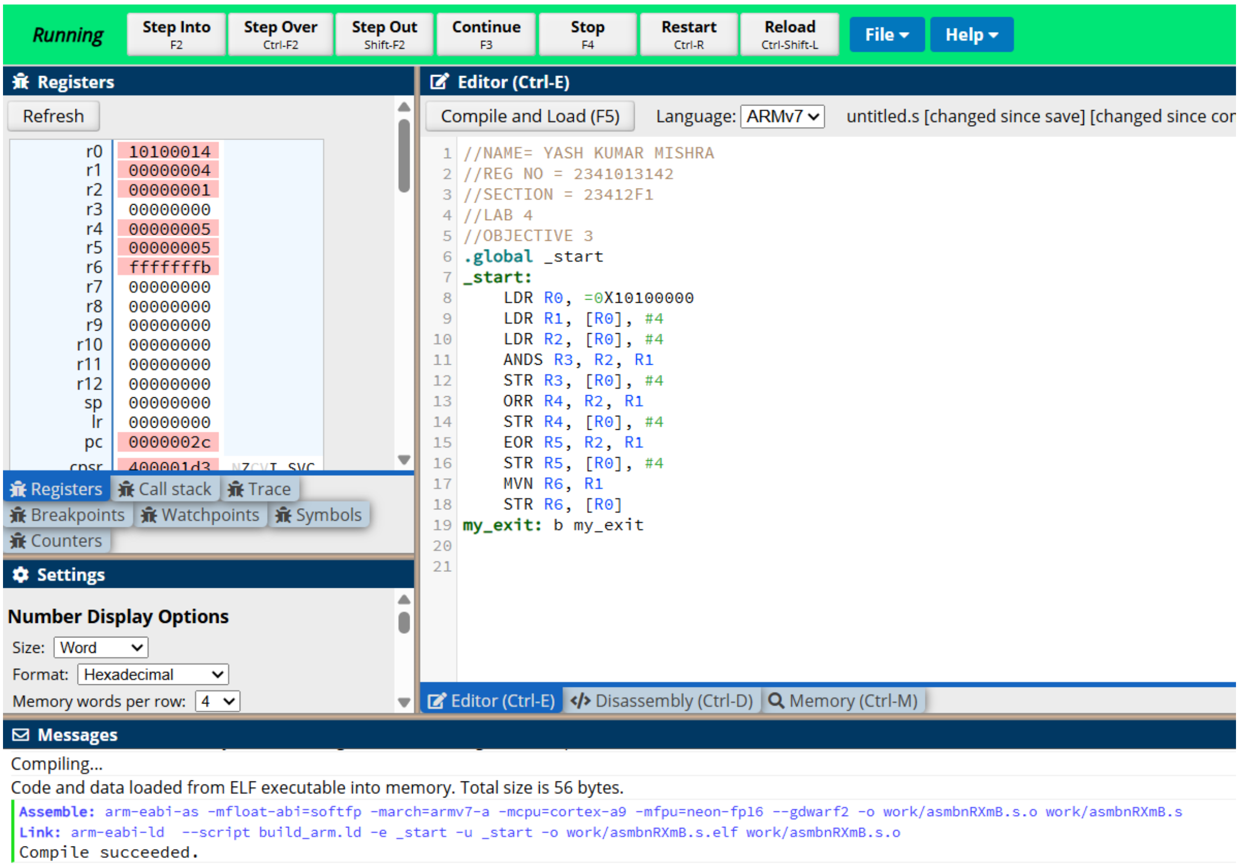
Number Display Options

Size: Word

Format: Hexadecimal

Memory words per row: 4

```
Compiling...
Code and data loaded from ELF executable into memory. Total size is 48 bytes.
Assemble: arm-eabi-as -mfloat-abi=softfp -march=armv7-a -mcpu=cortex-a9 -mfpu=neon-fp16 --gdwarf2 -o work/asmhqCcHs.s.o work/asmhqCcHs.s
Link: arm-eabi-ld  --script build_arm.ld -e _start -u _start -o work/asmhqCcHs.s.elf work/asmhqCcHs.s.o
Compile succeeded.
```



```
r0   10100014
r1   00000004
r2   00000001
r3   00000000
r4   00000005
r5   00000005
r6   fffffffb
r7   00000000
r8   00000000
r9   00000000
r10  00000000
r11  00000000
r12  00000000
sp   00000000
lr   00000000
pc   0000002c
cpsr 400001d3  NZCVI SVC
```

Language: ARMv7 untitled.s [changed since save] [changed since cor

```
//NAME= YASH KUMAR MISHRA
//REG NO = 2341013142
//SECTION = 23412F1
//LAB 4
//OBJECTIVE 3
.global _start
_start:
    LDR R0, =0X10100000
    LDR R1, [R0], #4
    LDR R2, [R0], #4
    ANDS R3, R2, R1
    STR R3, [R0], #4
    ORR R4, R2, R1
    STR R4, [R0], #4
    EOR R5, R2, R1
    STR R5, [R0], #4
    MVN R6, R1
    STR R6, [R0]
my_exit: b my_exit
```

Number Display Options

Size: Word

Format: Hexadecimal

Memory words per row: 4

```
Compiling...
Code and data loaded from ELF executable into memory. Total size is 56 bytes.
Assemble: arm-eabi-as -mfloat-abi=softfp -march=armv7-a -mcpu=cortex-a9 -mfpu=neon-fp16 --gdwarf2 -o work/asmbnRXmB.s.o work/asmbnRXmB.s
Link: arm-eabi-ld  --script build_arm.ld -e _start -u _start -o work/asmbnRXmB.s.elf work/asmbnRXmB.s.o
Compile succeeded.
```