

01_Q_DLD_18Dec2020

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| I've invited you to fill out a form: | |
| 01_Q_DLD_18Dec2020 | |
| 1. Dead line: 5.00 pm. 19th December 2020. 2. Please enter roll no. in the given format. Else attendance will not be considered. | |
| MSB stands for | |
| LSB stands for | |
| The 1's complement of the binary number 1010 is | |
| The 2's complement of the binary number 1111 | |
| The hexadecimal number system has 16 characters, six of which are alphabetic charact They are | ers. |
| The decimal number 1122 is expressed in the 2's complement form as | |
| The decimal number 234 is expressed in the 2's complement form as In the 2's complement form, the binary number 10010011 is equal to the decimal numbe | |

| What is the weight of 7 in each of the following decimal numbers 1947, 1799 and 1979 |
|--|
| Express each of the following decimal numbers as a power of ten 1000, 100000, 100000000 (Ex: 100=10^2) |
| Give the value of each digit in the following decimal numbers 263, 5463 |
| add the binary numbers (a) 10 + 10 (b) 10 + 11 (c) 100 + 11(d) 111 + 101 (e) 1111 + 111 (f) 1111 + 1111 |
| Determine the 1's complement of each binary number:(a) 100 (b) 111 (c) 1100(d) 10111011 (e) 1001010 (f) 10101010 |
| Determine the 2's complement of each binary number using either method:(a) 11 (b) 110 (c) 1010 (d) 1001 |
| Express each decimal number in binary as an 8-bit sign-magnitude number:(a) +29 (b) 285 (c) +100 (d) 2123 |
| Express each decimal number as an 8-bit number in the 1's complement form:(a) 234 (b) +57 (c) 299 (d) +115 |
| Express each decimal number as an 8-bit number in the 2's complement form:(a) +12 (b) 268 (c) +101 (d) 2125 |
| Convert each decimal number to binary by using the sum-of-weights method:(a) 12 (b) 15 (c) 25 (d) 50 |
| Convert each decimal fraction to binary using the sum-of-weights method:(a) 0.26 (b) 0.762 (c) 0.0975 |

| What is the drawback with half adder |
|---|
| |
| can we implement subtraction operation using addition circuit |
| What function can be implemented using XOR gate |
| If all the inputs are HIGH, output is HIGH. This gate is |
| Atleast if you have one ID card you will be allowed inside the shopping mall. What gate logic is this |
| Which gates are universal gates |
| Can we have 16 bit FA circuit |
| Directional gyro – set. Engine idle – checked. Flaps – as required. Flight controls – free and correct. Fuel gauges – checked. Instruments and radios – checked and set. Then the air plane can fly. what is this logic condition |
| |
| 2020 Batch Please enter last five digits of your roll number. 2018 Batch please enter your attendance as 18xxxxx, xxxxx are the last five digits of your roll number. 2019 Batch please enter your attendance as 19xxxxx, xxxxx are the last five digits of your roll number. |
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