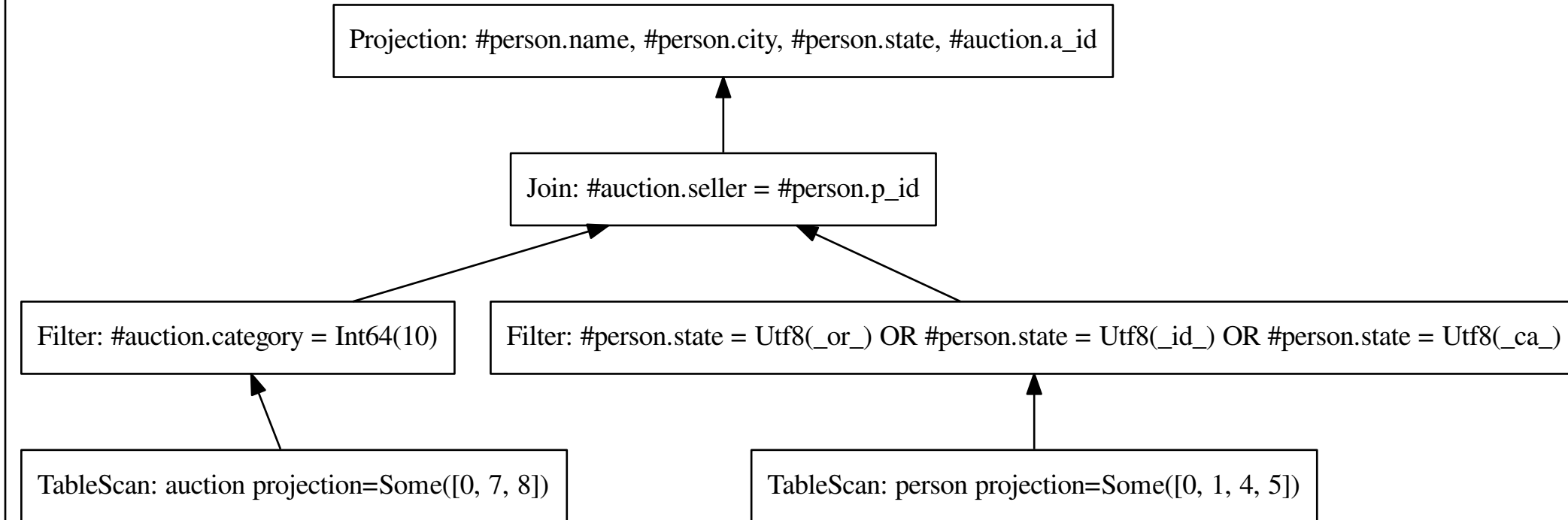


LogicalPlan



Detailed LogicalPlan

