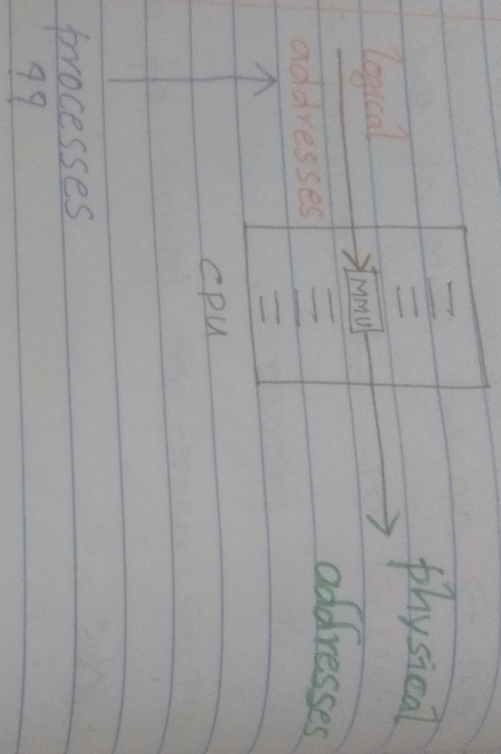


Logical addresses / range of logical addresses are defined by the processor



→ program  
→ logical  
→ Active  
→ physical