

AYUSH GOSWAMI

+91 7017564119 | ayushgoswami9536869394 | [GitHub](#) | [LinkedIn](#) |

SKILLS

- **Languages:** Java, JavaScript, Python, Verilog, G programming
- **FIGMA**
- **BASIC FRONT END**
- **ASSEMBLY LANGUAGE**

WORK EXPERIENCE

V- Guide Team Member , VIT-AP

- Wrote a blog on "is it worth pursuing a master's degree" and "computer security" .
- Our team hosted an event as "INTERVIEW ODESSY", where i was in interview panel team.

Social Entrepreneurship internship

VLSI Design Methodologies

- Designed an alarm clock using verilog HDL
- Implemented specs as LCD display, LCD driver, alarm register, time generator, counter, counter unit.
- Optimized it for the smooth running

EDUCATION

Sep 2022 - Present

Vellore Institute Of Technology

Bachelor of Technology in Electronics and Communication Engineering

- Relevant Coursework: Object Oriented Programming, Digital logic Design, Semiconductor devices, Data Structures and Algorithms, Data acquisition and transmission system, Analog Devices and Circuits, Signals and System, Fundamentals of Electronics, Microprocessor and Microcontroller.
- Currently working on a research paper on SEMICONDUCTORS

Aug 2014 - May 2021
90 Percentage

JAWAHAR NAVODAYA VIDYALAYA

(CBSE)

PROJECTS

CHAT-BOT

- Developed a voice-activated web navigation feature, enabling blind users to surf the web and get their queries answered efficiently.
- Implemented an object classification system using computer vision, helping users identify and understand their surroundings with 25% more accuracy.
- Integrated a face recognition module, enhancing social interactions by accurately identifying individuals and providing contextual information.

ALARM CLOCK

- Designed an alarm clock using verilog HDL
- Implemented specs as LCD display, LCD driver, alarm register, time generator, counter, counter unit.
- Optimized it for the smooth running

Portfolio

- Developed a portfolio website for showcasing my resume and all..

INTERESTS

- Problem Solving
- Verilog simulation
- UI/UX Design
- VERILOG

CERTIFICATES

- MAVEN SILICON - FOUNDATION COURSE ON EMBEDDED SYSTEM DESIGN
- MAVEN SILICON - VLSI Design Methodologies
- CSI, VIT-AP - EVENT ORGANISER
- CISCO - JavaScript Essentials 1 (JSE)