SHRI D. N. INSTITUTE OF COMPUTER APPLICATIONS B.Sc (CA & IT) (SEM-II)

US02IDCIT05 – Digital Electronics MODEL PAPER-MARCH-2024

Total Marks:25

Q-1 Multiple choice questions.					[5]
1. Invert gate h	as only	input and			
A	Two, One		В.	One, Two	
C	One, One		D.	None of these	
2. Ais a c	combinational circu	uit that converts	binar	y information from the"2)n''
Coded input	s to "n" outputs.				
A.	Half Adder		B.	Decoder	
C.	Encoder		D.	Comparator	
3. The number of be	of Flip-Flops requi	ired to construct	8-bit	shift register will	
A.	4		B.	16	
C.	8		D.	32	
4. The basic bu	ilding block of sec	quential logic ci	rcuit _		
A.	Flip -Flop		B.	NAND Gate	
C.	Latches		D.	None of these	
5. The D flip-fl	lop has	input.			
A.	1		B.	2	
C.	3		D.	4	
Q-2 What is log	gic gate? Draw ele	ctronic symbol a	and ex	xplain AND, OR, NOT, N	NOR and
NAND G	ates with truth tabl	e.			[10]
		OR			
Q-2 Explain Encoder and Decoder with circuit and truth table.					[10]
Q-3 Explain R S Flip Flop in detail with truth table and circuit diagram.					[10]
		OR			
Q-3 Explain Sh	nift –Left register a	and Shift –Right	regist	ter in detail with block di	iagram. [10]

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