Platform: FreeRTOS running on the TI TIVA board

1. Create a user-defined interrupt handler for the timer ISR and a task for processing. The timer should be scheduled on a regular basis, and the interrupt handler should signal the processing task. To ensure that the timer is being triggered with the correct periodicity, pass the interrupt timing to the processing task.

```
lelcome to the EK-TM4C123GXL FreeRTOS Demo!
ask3 created at Time: 0
               interrupt
interrupt
                              Time:
Time:
intering timer
ntering timer
                           at
                                     7500
10000
intering timer interrupt at
                              Time:
                              Time:
Time:
ntering timer
                interrupt
                          at
ntering timer
                interrupt
                           at
ntering
        timer
                interrupt
                              Time:
ntering
                interrupt
         timer
                              Time:
                           at
                interrupt
ntering
         timer
                               Time:
ntering timer
                interrupt
                           at
                              Time:
ntering timer
                interrupt
                              Time:
                          at
                              Time:
Time:
ntering timer
                interrupt
                          at
                interrupt
         timer
```

2. Create a pair of tasks that signal each other. The first task performs some computation, signals the other task, and waits for a signal from that task. The second task repeats the same pattern so that they alternate. Each task should complete a defined amount of work, such as computing a specified number of Fibonacci values. Profile each task so that one task is executing for 10 ms and the other for 40 ms.

```
Velcome to the EK-TM4C123GXL FreeRTOS Demo!

Task1 create

Task2 crdeated

Time taken for Fibonacci is 10 ms

Now Task 2 should run

Time taken for Fibonacci is 40 ms

Now Task 1 should run

Time taken for Fibonacci is 10 ms

Now Task 2 should run

Time taken for Fibonacci is 40 ms

Now Task 2 should run

Time taken for Fibonacci is 40 ms

Now Task 1 should run

Time taken for Fibonacci is 10 ms

Now Task 2 should run

Time taken for Fibonacci is 10 ms
```

3. Modify the timer ISR to signal two tasks with different frequencies: one task every 30 ms and the other every 80 ms. Use your processing load from #2 to run 10 ms of processing on the 30-ms task and 40 ms of processing on the 80-ms task.

```
lelcome to the EK-TM4C123GXL FreeRTOS Demo!

[ask4 created
[ask5 created
[ask 4 runs at Time : 30
[ask 4 stops at Time : 39
[ask 4 runs at Time : 60
[ask 4 stops at Time : 69
[ask 5 runs at Time : 80
[ask 5 runs at Time : 90
[ask 6 runs at Time : 91
[ask 7 runs at Time : 117
[ask 7 runs at Time : 120
[ask 7 runs at Time : 120
[ask 7 runs at Time : 150
[ask 7 runs at Time : 150
[ask 7 runs at Time : 150
[ask 7 runs at Time : 180
[ask 7 runs at Time : 180
[ask 7 runs at Time : 180
[ask 7 runs at Time : 208
[ask 7 runs at Time : 208
[ask 7 runs at Time : 220
[ask 7 runs at Time : 220
[ask 7 runs at Time : 240
[
```