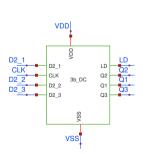
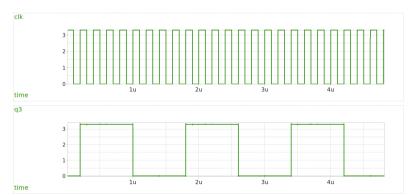
## MODELS

.include \$::180MCU\_MODELS/design.ngspice
.lib \$::180MCU\_MODELS/sm141064.ngspice typical





## NGSPICE1

.control

tran 100n 5u
plot v(Q1)
plot v(Q2)
plot v(Q3)
plot v(CLK)
plot v(LD)
write 3b\_DC\_tb.raw
\*write test\_nfet\_03v3.raw
.endc

