

```
NGSPICE1
*.option savecurrents
.control
save all
tran 100n 1u
set xbrushwidth=2
set xfontsize=3
plot v(CLK) v(Q3)+3.5 v(Q2)+7 v(Q1)+10.5 v(P2)+14 v(x1.P0)+17.5 v(OUT1)+21 v(LD)+24.5 v(Q4)+28 v(Q5)+31.5
plot v(Q1)
plot v(Q2)
plot v(Q3) v(Q2) v(Q1)
write 7b_divider_tb.raw
*write test_nfet_03v3.raw
.endc

MODELS
.include $::180MCU_MODELS/design.ngspice
.lib $::180MCU_MODELS/aml41064.ngspice typical
```

