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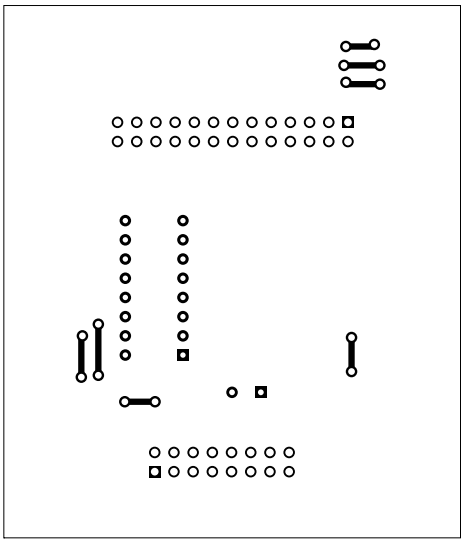
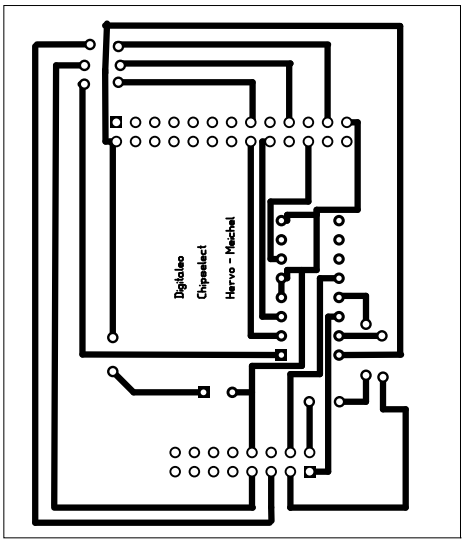
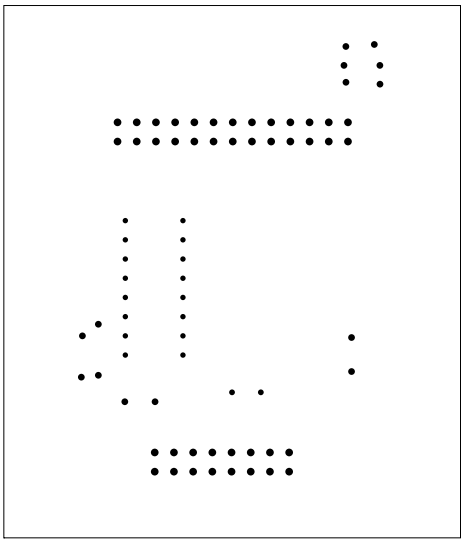


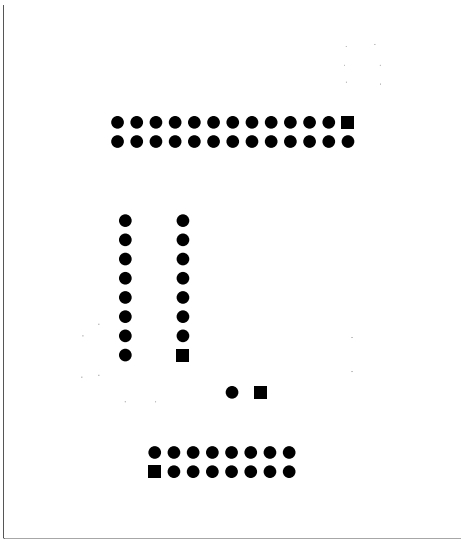
top, scale = 1:1.000
chipselect-project.pcb



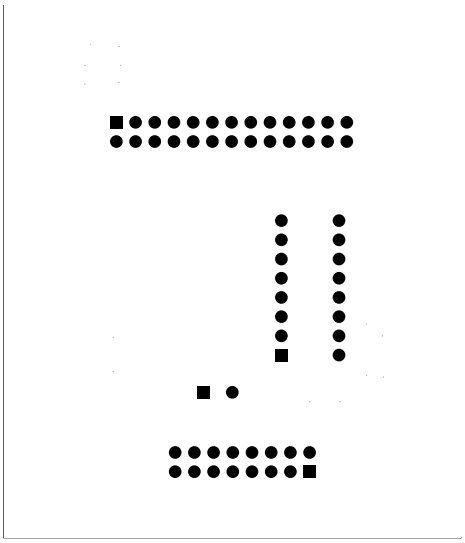

bottom (mirrored), scale = 1:1.000
chipselect-project.pcb



plated-drill, scale = 1:1.000
chipselect-project.pcb



topmask, scale = 1:1.000
chipselect-project.pcb



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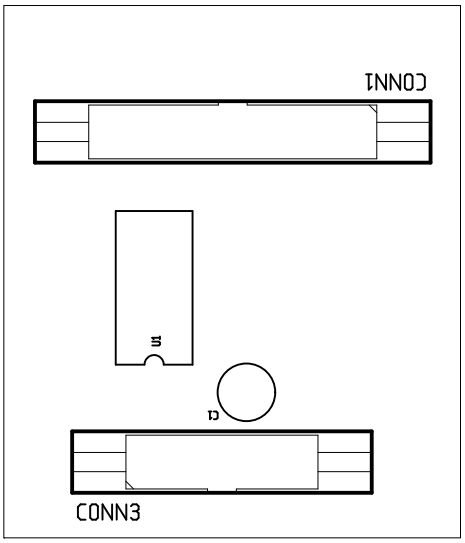
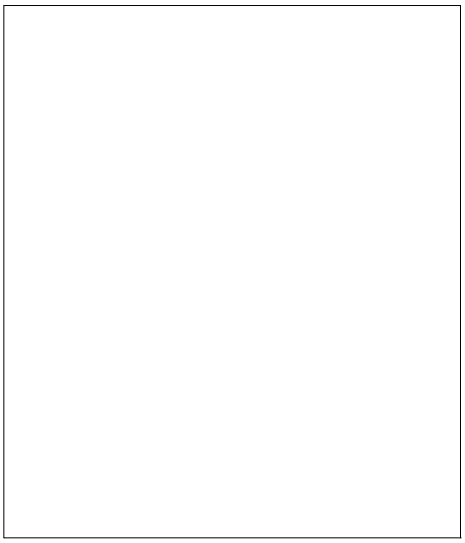
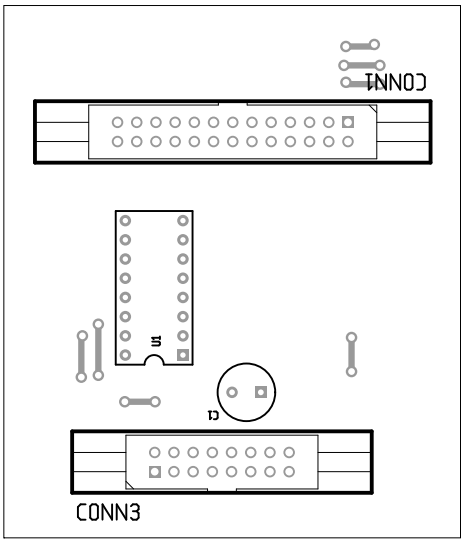
chipselect-project.pcb



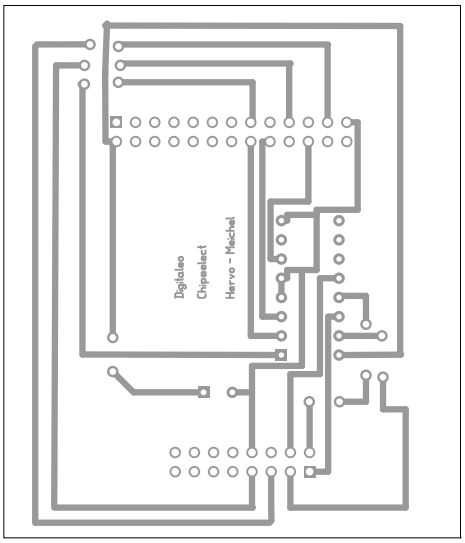

topsilk, scale = 1:1.000
chipselect-project.pcb



bottomsilk (mirrored), scale = 1:1.000
chipselect-project.pcb




topassembly, scale = 1:1.000
chipselect-project.pcb



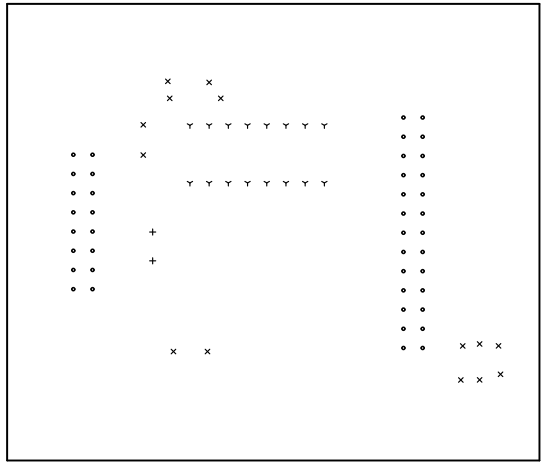

bottomassembly (mirrored), scale = 1:1.000
chipselect-project.pcb

fab, scale = 1:1.000
chipselect-project.pcb

There are 4 different drill sizes used in this layout, 74 holes total

| Symbol | Diam. (Inch) | Count | Plated? |
|---|---|---|---|
| ˟ | 0.028 | 16 | YES |
| + | 0.030 | 2 | YES |
| × | 0.035 | 14 | YES |
| ∘ | 0.040 | 42 | YES |

Title: (unknown) - Fabrication Drawing
Author: Cyrille Meichel
Date: lun. 24 juin 2013 11:49:09 GMT UTC
Maximum Dimensions: 2771.650000 mils wide, 2377.950000 mils high



Board outline is the centerline of this 8.000000 mil rectangle - 0,0 to 2771.650000,2377.950000 mils