

28.08.24

ARM Architecture:

It specifies

- i) Instruction Set
- ii) Register Set
- iii) Exception model
- iv) Memory model
- v) Debug, Trace & profiling

RISC - Reduced Instruction Set Computer.

→ floating point 12.02

→ ~~decimal~~ point 148.62

1.62

Fixed Point 12.8

18.1

17.2

Processor family

* ARM processor families group multiple processor and was chronologically.

* The name changed into cortex in 2005.

ARM (1985) to ARM II (2004)

2005 → naming scheme change of cortex

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Note:

Arm is not a chip manufacturing company.
It is used to design architecture.

* IP Core

Micro architecture is installed in software
i.e. IP core

* profile

Application - features powerful processor

Real time - example of control unit for
automotive systems

Microcontroller profile

Arm makes many with archive
cores or processor cores.

Arm (SOC)

ARM

TRM → about processors

CIM Configuration Integration manual)

SOC Datasheet → Info on specific