BRAC UNIVERSITY

Department of Computer Science and Engineering CSE 260: Digital Logic Design

Examination: **Quiz 1**Duration: 25 Minutes

Semester: Spring 2025
Full Marks: 15

Answer the following questions. You **MUST** show your workings/calculations where applicable. Figures in the right margin indicate marks.

- 1. Perform the following conversion: $(101011110001100)_{\text{Excess-5}} = (?)_7$ Note: You **must** show all the necessary conversions. [4]
- 2. Subtract $(-36)_{10}$ from 50_{10} using 7-bit 2's complement system (show the conversion first). Justify whether there is an overflow or not. [5]
- 3. Divide (32112)₄ by (33)₄. Find the quotient and remainder.

 Note: You must show the necessary calculations. [6]

		3.1	
2) 50-(-36) = 50 + 36		011 0010	
2 50	2-[36	+ 010 0100	
2 12 - 1	20-0	1010110	
2 3 - 0 2 1 - 1 2 1 - 1	2 1-0 2 1-0 0-1-2M	Here, we added two positive numbers but the amwer has negative sign.	
=> (50) ₁₀ = (110010) ₂	(36) ₁₀ = (100100) ₂	So, according to this rule,	
+50 = 011 0010	+36 = 010 0100	Adding two same signed numbers if amwer has different sign, then	
		Overflow.	

Yes, we have overflow here.

		1 .	
3) 33 32112 00331	33×0 = 0	33	4 6 11
0	33 × 1 = 33	X2 132	
- 32 - 0	33x2 = 132	132	
32			4/7/1
<u>- 23 </u> - 30	33×3 =23		1/4/
<u>-231</u> 1026		2	3
- 190 26 - 23 - 03		33	4 9 2
Quotient = (331)y		23	(8)
Remainder = (3)y			
			4 11 /2
			3