



Inspiring Excellence

CSE260: Digital Logic Design

Spring 2025

Quiz - 03

Duration: 25 Minutes

A

Name: Solution

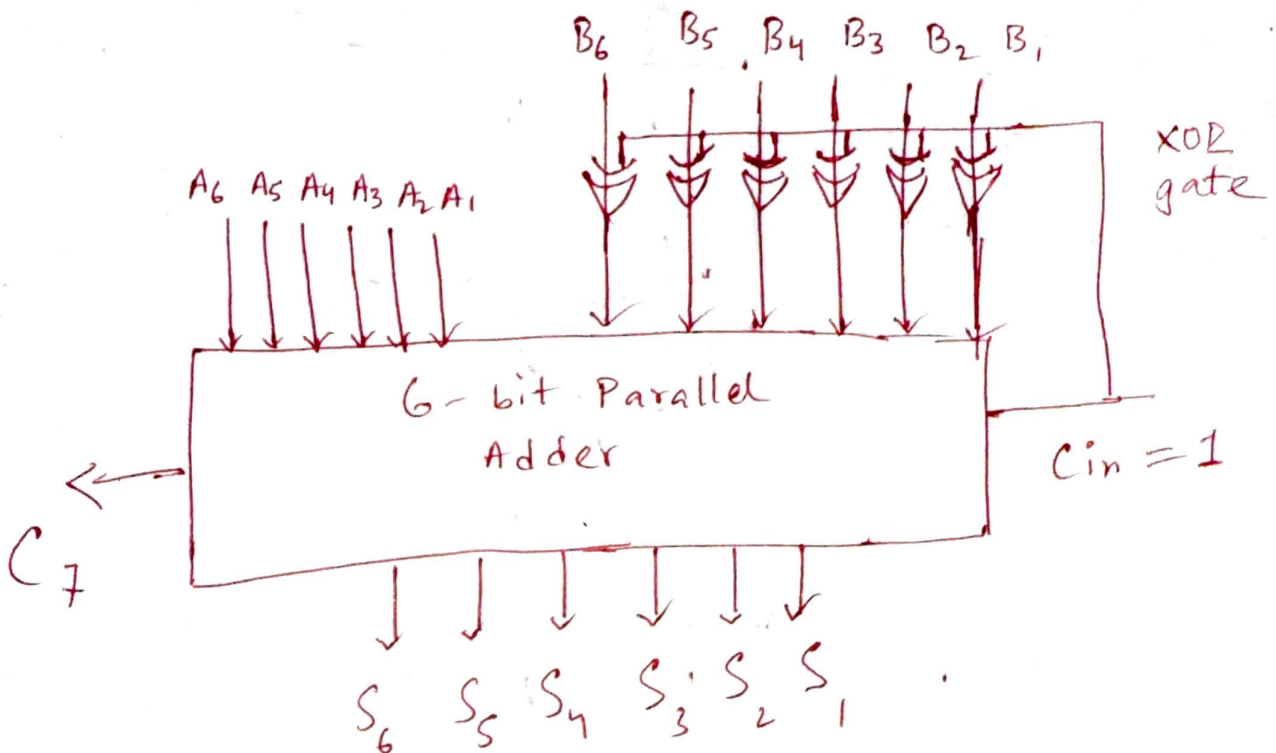
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Section: 13

Instructions: Answer in the space provided.

Question 1 [CO3]: Design (A-B) using a 6-bit parallel adder where A and B are 6-bit numbers. Use external logic gates if required.

[7.5]



Question 2 [CO3]: Implement the following boolean function using only 2x4 decoder(s). Use external logic gates if required.

[7.5]

$$F(A, B, C, D) = \sum(1, 3, 6, 9, 11, 13)$$

