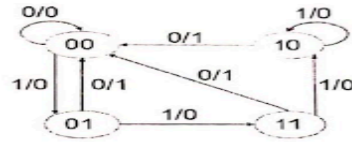


1.C04

Given state diagram as follows, get the sequential circuit using JK flip-flop:



Present State		Input	Next State		Output	Flip Flop Inputs			
A	B	X	A ⁺	B ⁺	Y	JA	KA	JB	KB
0	0	0	0	0	0	0	X	0	X
0	0	1	0	1	0	0	X	1	X
0	1	0	0	0	1	0	X	X	1
0	1	1	1	1	0	1	X	X	0
1	0	0	0	0	1	X	1	0	X
1	0	1	1	0	0	X	0	0	X
1	1	0	0	0	1	X	1	X	1
1	1	1	1	0	0	X	0	X	1

A \ B	B ^x	B ^x '	B ^x '	B ^x	B ^x '
A ¹				1	
A	X	X	X	X	X

$JA = BX$

A \ B	B ^x	B ^x '	B ^x '	B ^x	B ^x '
A ¹	X	X	X	X	X
A	1	0	0	0	1

$KA = X'$

A \ B	B ^x	B ^x '	B ^x '	B ^x	B ^x '
A ¹	0	1	X	X	X
A	0	0	X	X	X

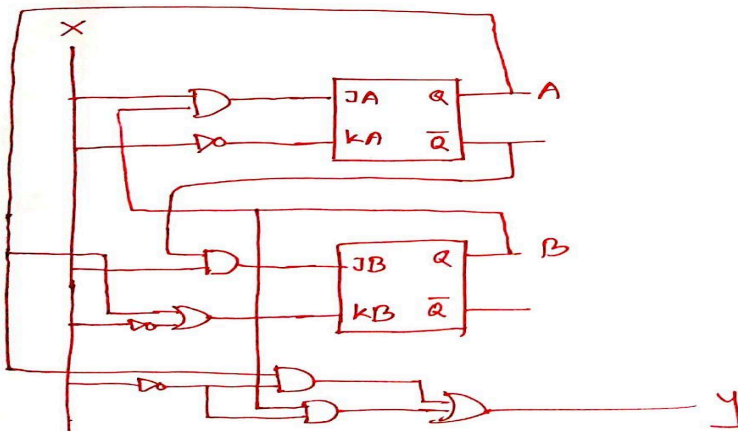
$JB = AX$

A \ B	B ^x	B ^x '	B ^x '	B ^x	B ^x '
A ¹	X	X	0	1	1
A	X	X	1	1	1

$KB = X' + A$

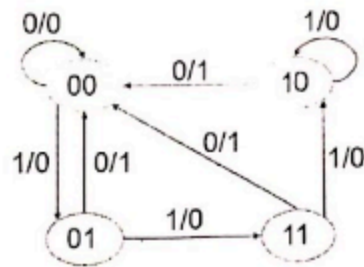
A \ B	B ^x	B ^x '	B ^x '	B ^x	B ^x '
A ¹	0	0	0	1	1
A	1	0	0	1	1

$Y = AX' + BX'$

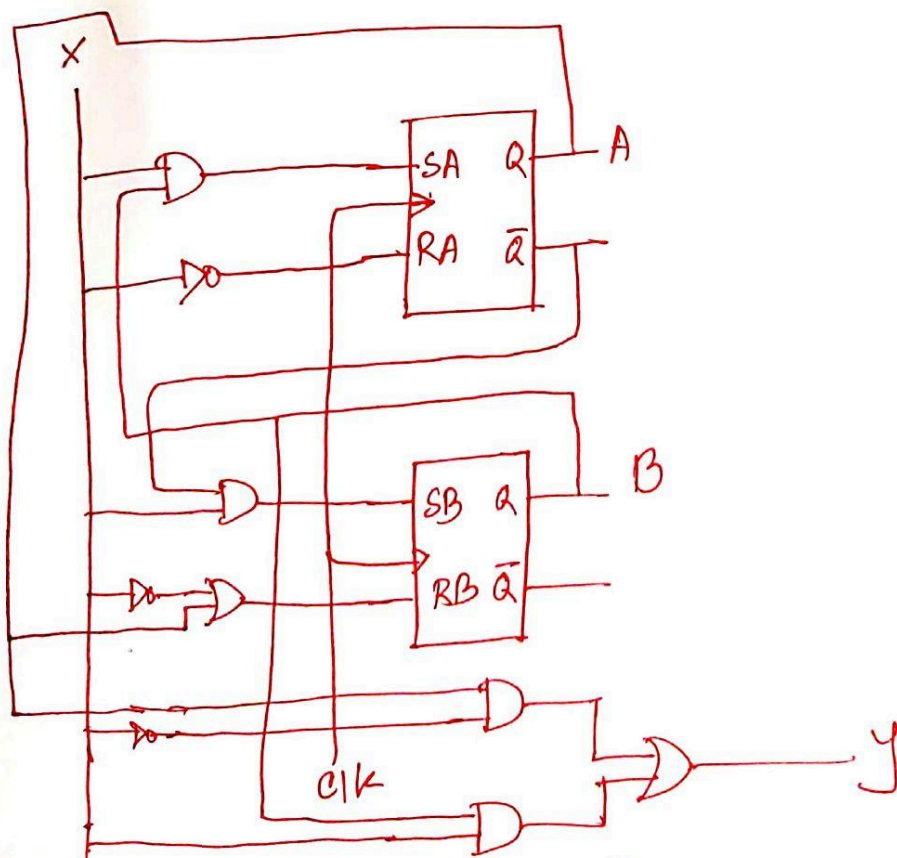
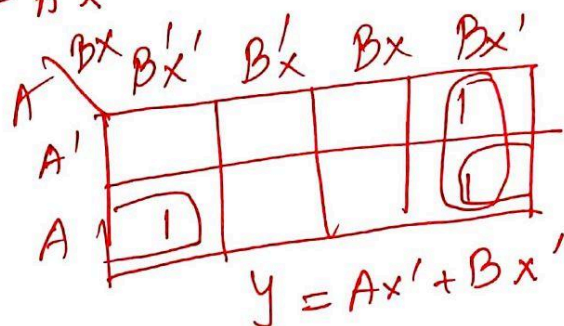
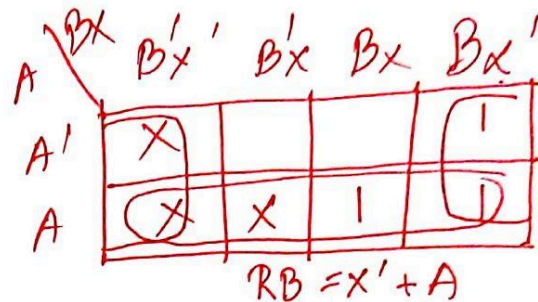
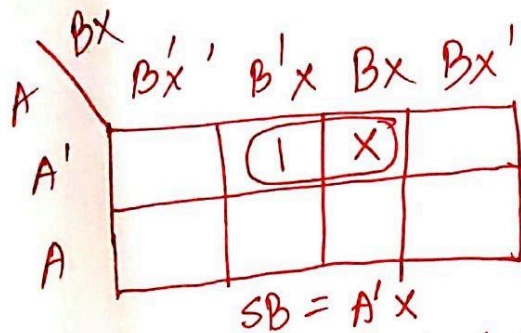
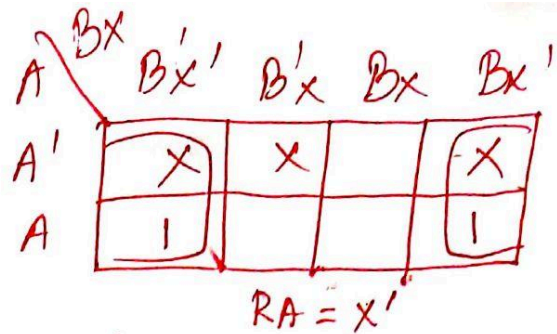
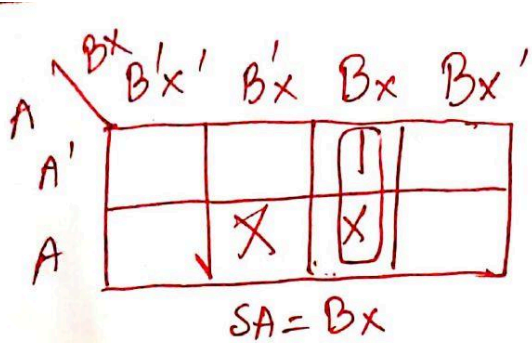


1.CO4

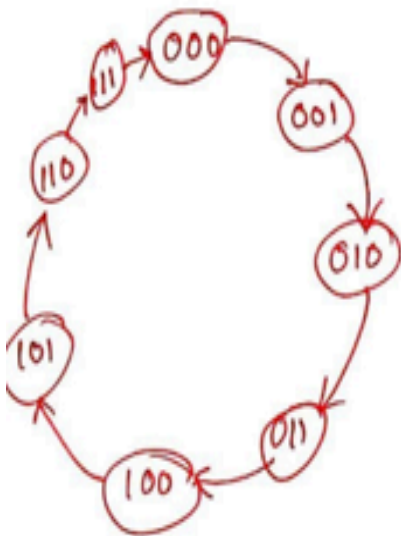
Given state diagram as follows, get the sequential circuit using SR flip-flop:



Present State		Input	Next State		Output	Flip Flop Inputs			
A	B		A ⁺	B ⁺		SA	RA	SB	RB
0	0	0	0	0	0	0	X	0	X
0	0	1	0	1	0	0	X	1	0
0	1	0	0	0	1	0	X	0	1
0	1	1	1	1	0	1	0	X	0
1	0	0	0	0	1	0	1	0	X
1	0	1	1	0	0	X	0	0	X
1	1	0	0	0	1	0	1	0	1
1	1	1	1	0	0	X	0	0	1



1.CO4 Implement the following counter using JK FF: 0→1→2→3→4→5→6→7→0



Present State			Next State			Flip Flop Inputs					
A_2	A_1	A_0	A_2^+	A_1^+	A_0^+	J_{A_2}	K_{A_2}	J_{A_1}	K_{A_1}	J_{A_0}	K_{A_0}
0	0	0	0	0	1	0	x	0	x	1	x
0	0	1	0	1	0	0	x	1	x	x	1
0	1	0	0	1	1	0	x	x	0	1	x
0	1	1	1	0	0	1	x	x	1	x	1
1	0	0	1	0	1	x	0	0	x	1	x
1	0	1	1	1	0	x	0	1	x	x	1
1	1	0	1	1	1	x	0	x	0	1	x
1	1	1	0	0	0	x	1	x	1	x	1

		$A_1 A_0$			
A_2	A_2'	$A_1' A_0'$	$A_1' A_0$	$A_1 A_0$	$A_1 A_0'$
	A_2	x	x	1	x

$$JA_2 = A_1 A_0$$

		$A_1 A_0$			
A_2	A_2'	$A_1' A_0'$	$A_1' A_0$	$A_1 A_0$	$A_1 A_0'$
	A_2		1	x	x

$$KA_2 = A_0$$

		$A_1 A_0$			
A_2	A_2'	$A_1' A_0'$	$A_1' A_0$	$A_1 A_0$	$A_1 A_0'$
	A_2	1	x	x	1

$$JA_0 = 1$$

		$A_1 A_0$			
A_2	A_2'	$A_1' A_0'$	$A_1' A_0$	$A_1 A_0$	$A_1 A_0'$
	A_2	x	x	1	x

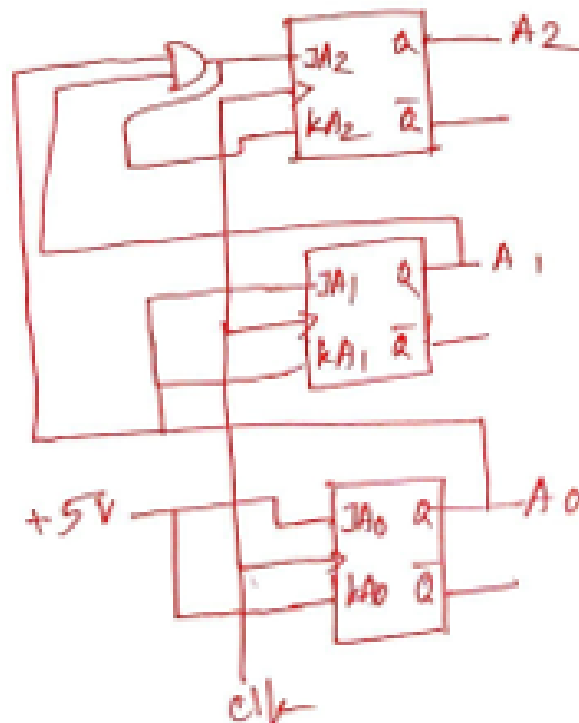
$$KA_2 = A_1 A_0$$

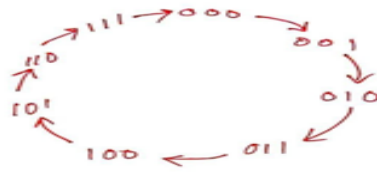
		$A_1 A_0$			
A_2	A_2'	$A_1' A_0'$	$A_1' A_0$	$A_1 A_0$	$A_1 A_0'$
	A_2	x	1	x	1

$$KA_1 = A_0$$

		$A_1 A_0$			
A_2	A_2'	$A_1' A_0'$	$A_1' A_0$	$A_1 A_0$	$A_1 A_0'$
	A_2	x	1	1	x

$$KA_0 = 1$$





Present State			Next State			Flip Flop Inputs					
A_2	A_1	A_0	A_2^+	A_1^+	A_0^+	SA_2	RA_2	SA_1	RA_1	SA_0	RA_0
0	0	0	0	0	1	0	X	0	X	1	0
0	0	1	0	1	0	0	X	1	0	0	1
0	1	0	0	1	1	0	X	X	0	1	0
0	1	1	1	0	0	1	0	0	1	0	1
1	0	0	1	0	1	X	0	0	X	1	0
1	0	1	1	1	0	X	0	1	0	0	1
1	1	0	1	1	1	X	0	X	0	1	0
1	1	1	0	0	0	0	1	0	1	0	1

A_2	A_1A_0	$A_1'A_0'$	$A_1'A_0$	A_1A_0'
A_2'	0	1	1	0
A_2	X	X	X	X

$SA_2 = A_2'A_1A_0$

A_2	A_1A_0	$A_1'A_0'$	$A_1'A_0$	A_1A_0'
A_2'	X	X		X
A_2			1	

$RA_2 = A_2A_1A_0$

A_2	A_1A_0	$A_1'A_0'$	$A_1'A_0$	A_1A_0'
A_2'		1	0	X
A_2		1		X

$SA_1 = A_1'A_0$

A_2	A_1A_0	$A_1'A_0'$	$A_1'A_0$	A_1A_0'
A_2'	X			1
A_2	X			1

$RA_1 = A_1A_0$

A_2	A_1A_0	$A_1'A_0'$	$A_1'A_0$	A_1A_0'
A_2'	1			1
A_2	1			1

$SA_0 = A_0'$

A_2	A_1A_0	$A_1'A_0'$	$A_1'A_0$	A_1A_0'
A_2'		1	1	
A_2		1	1	

$RA_0 = A_0$

