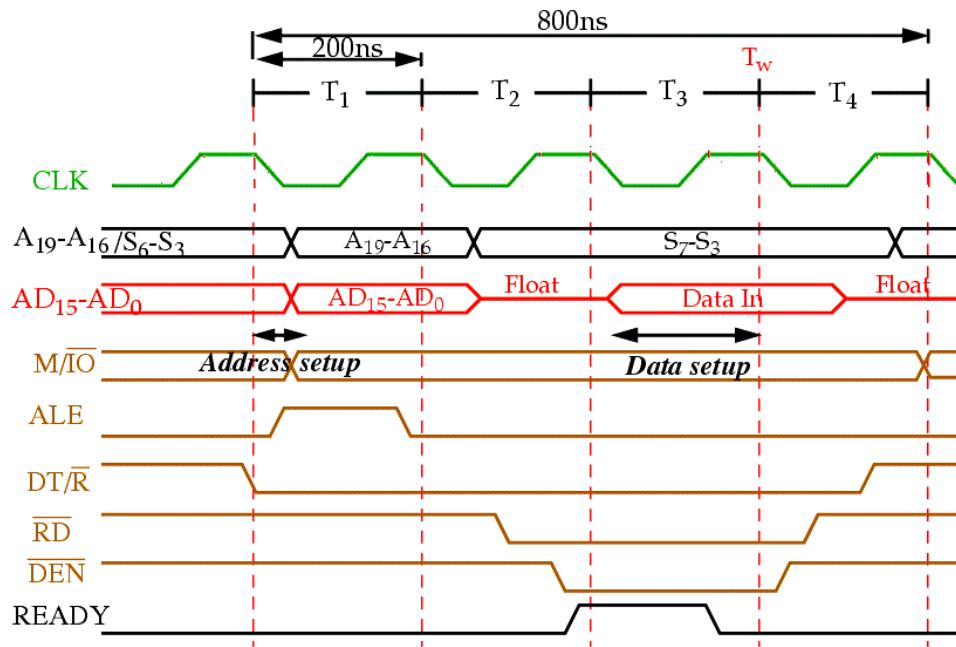
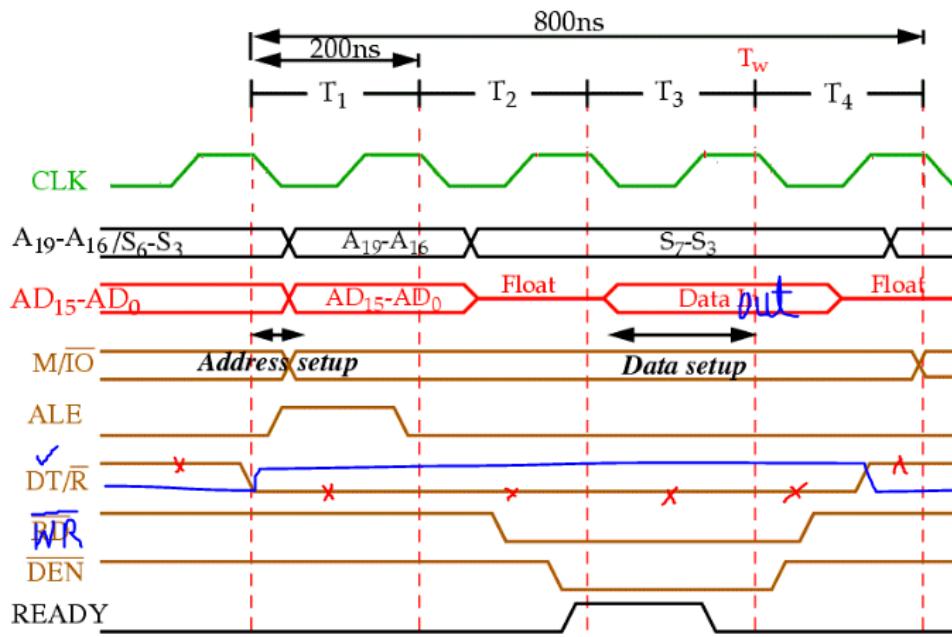


## Read Cycle Timing Diagram:



## Write Cycle Timing Diagram:



## Read Cycle Timing Diagram (With 1 Wait state):

