

Assignment

Question 1

Design a smoke detection and alarm system using a Schmitt trigger circuit. The input transducer measures smoke opacity and outputs a corresponding voltage. 0% opacity signifies no smoke and 100% opacity signifies complete smoke pervasion. The transducer converts this opacity to a range of 0 to 10V (The conversion constant is **0.1V per 1% opacity**). Design the system such that it blares an alarm when smoke opacity **surpasses 95%** and quiets down **below 5%**. You must satisfy all the mentioned criteria, and assume any component values accordingly.

(a)	Draw the complete circuit of the smoke detection and alarm system
(b)	Determine all the component values (resistors, reference voltage)
(c)	Draw the voltage transfer characteristics curve (V_{in} vs V_{out} plot). Clearly label the plot.

Hint: This is a design problem. Draw a schmitt trigger circuit with applied reference voltage, list all the related equations, then according to the given information, determine which variables are known and from there solve for the unknown component values. It might seem that there are not enough equations to solve for every unknown variable, in that case, assume the values of one or more components but make sure that all the mentioned criterias are satisfied. For instance, it is common in design problems to assume the value of one resistor then mathematically solve for the others.

Question 2

You have a light source that is too bright, so you want to illuminate it at 50% of its total illumination capacity. **Design** a square wave generator that accomplishes this by using **only 12V and 0V voltage sources**. Additionally, you must be careful such that any light flickering is **unnoticeable to the human eye** (Hint: Human eyes can notice flickering up to 500 Hz). You must satisfy all the mentioned criteria, and assume any component values accordingly.

(a)	Draw the complete circuit of the square wave generator
(b)	Determine all the component values (resistors, capacitors, saturation voltages)
(c)	Plot the time vs output voltage curve (time vs V_{in}/V_{out} plot). Clearly label the plot.

Hint: Same hint as *Question 1*.