

The operation is the equivalent of multiplying something by -1 . Applying the same operation to the two's complement representation of a negative number, yields a positive number.

8-bit - 1111 0101

16-bit - 1111 1111 1111 0101

32-bit - 1111 1111 1111 1111 1111 1111 1111 0101

64-bit - 1111 1111 1111 1111 1111 1111 1111 1111
1111 1111 1111 1111 1111 1111 1111 0101