

The schematic diagram illustrates the FT4232H-56QMINIMDL module and its connections. The module is represented by a yellow box labeled 'M1' with various pins and internal components.

Module Pins and Connections:

- Debug/Service Pins (CN2-8 to CN2-16):**
 - DEBUG_RXD (CN2-8), DEBUG_TXD (CN2-9), DEBUG_CTS (CN2-10), DEBUG_RTS (CN2-11)
 - AD0 (CN2-13), AD1 (CN2-14), AD2 (CN2-15), AD3 (CN2-16)
 - AD4 (CN2-17), AD5 (CN2-18), AD6 (CN2-19), AD7 (CN2-20)
 - BD0 (CN2-21), BD1 (CN2-22), BD2 (CN2-23), BD3 (CN2-24), BD4 (CN2-25)
 - BD5 (CN2-26), BD6 (CN2-27), BD7 (CN2-28)
- Service Pins (CN3-25 to CN3-32):**
 - SERVICE_RXD (CN3-25), SERVICE_TXD (CN3-26), SERVICE_CTS (CN3-27), SERVICE_RTS (CN3-28)
 - RECOVERY_RXD (CN3-29), RECOVERY_TXD (CN3-30), RECOVERY_CTS (CN3-31), RECOVERY_RTS (CN3-32)
- Power and Control Pins:**
 - V3V3 (CN2-2, CN2-4, CN2-6, CN2-12, CN3-11, CN3-21)
 - VIO (CN2-13, CN2-14, CN2-15, CN2-16)
 - VBUS (CN3-2, CN3-4)
 - EECS (CN3-6), EECLK (CN3-5), EEDATA (CN3-8)
 - RESET (CN2-7), NC (CN2-22), SUSPEND (CN2-26), PWREN (CN3-7)
 - GND (CN2-1, CN2-3, CN2-5, CN3-1, CN3-3)

Internal Components and Connections:

- Resistors:** R4 (33R), R5 (33R), R8 (33R), R9 (1k), R11 (1k), R12 (4K7), R13 (604R), R14 (604R).
- Diodes:** D1 (Red), D2 (Green), D3A (BAR43CFILM), D3B (BAR43CFILM).
- Switch:** SW1 (DHT-1158).
- NetTie:** NT1 (1 2).

External Connections:

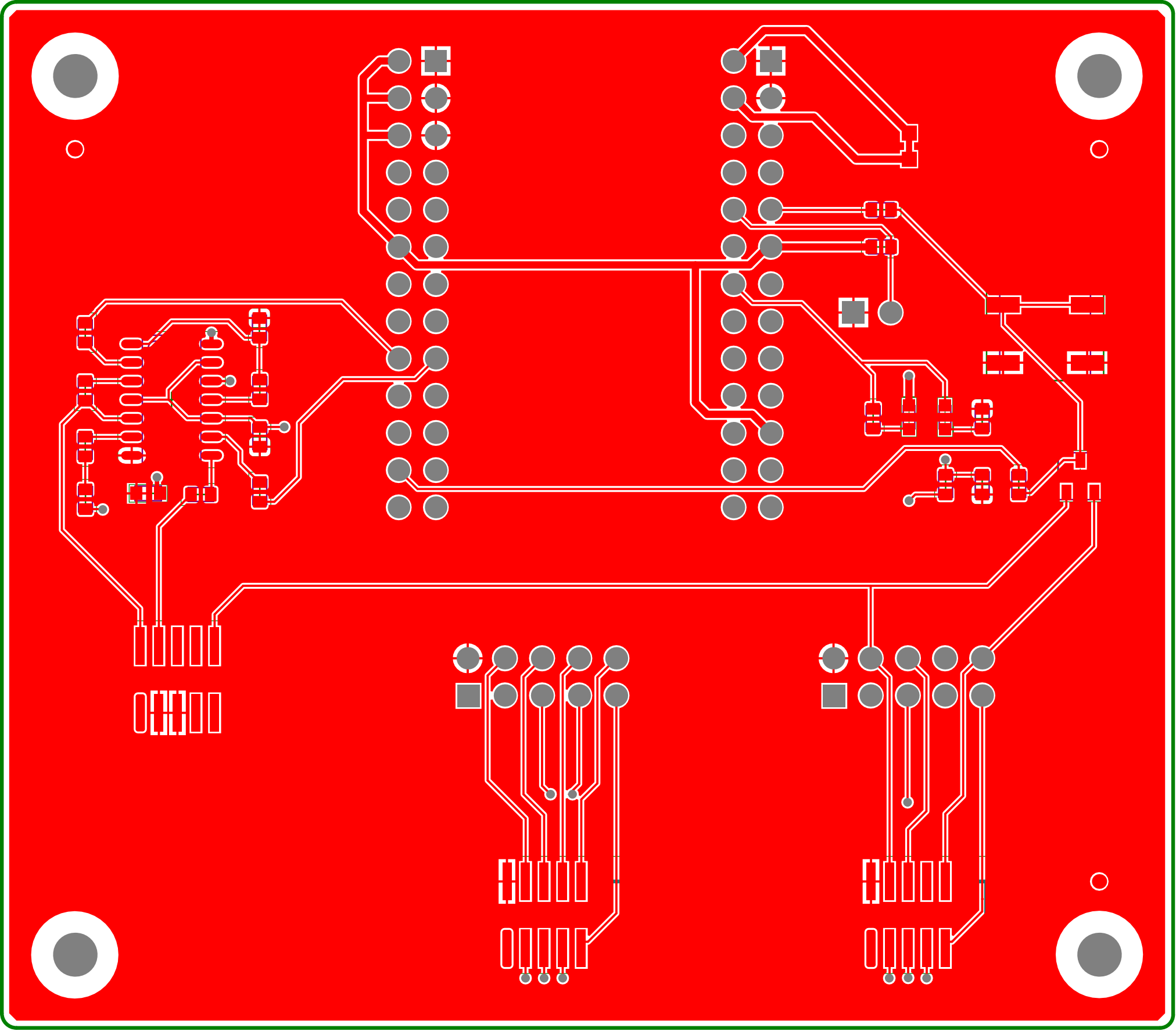
- Power:** VCC, VBUS, 3V3, GND.
- Signal:** SWCLK_F, SWDIO_OUT_F, SWDIO_IN_F, SWDIO_DIR_F, SWD_EN, FT_RST_MT, RECOVERY_FT, FT_RST_MT, DD7, DD4, DD3, DD2, DD1, DD0.
- Status:** D1 (Red), D2 (Green).

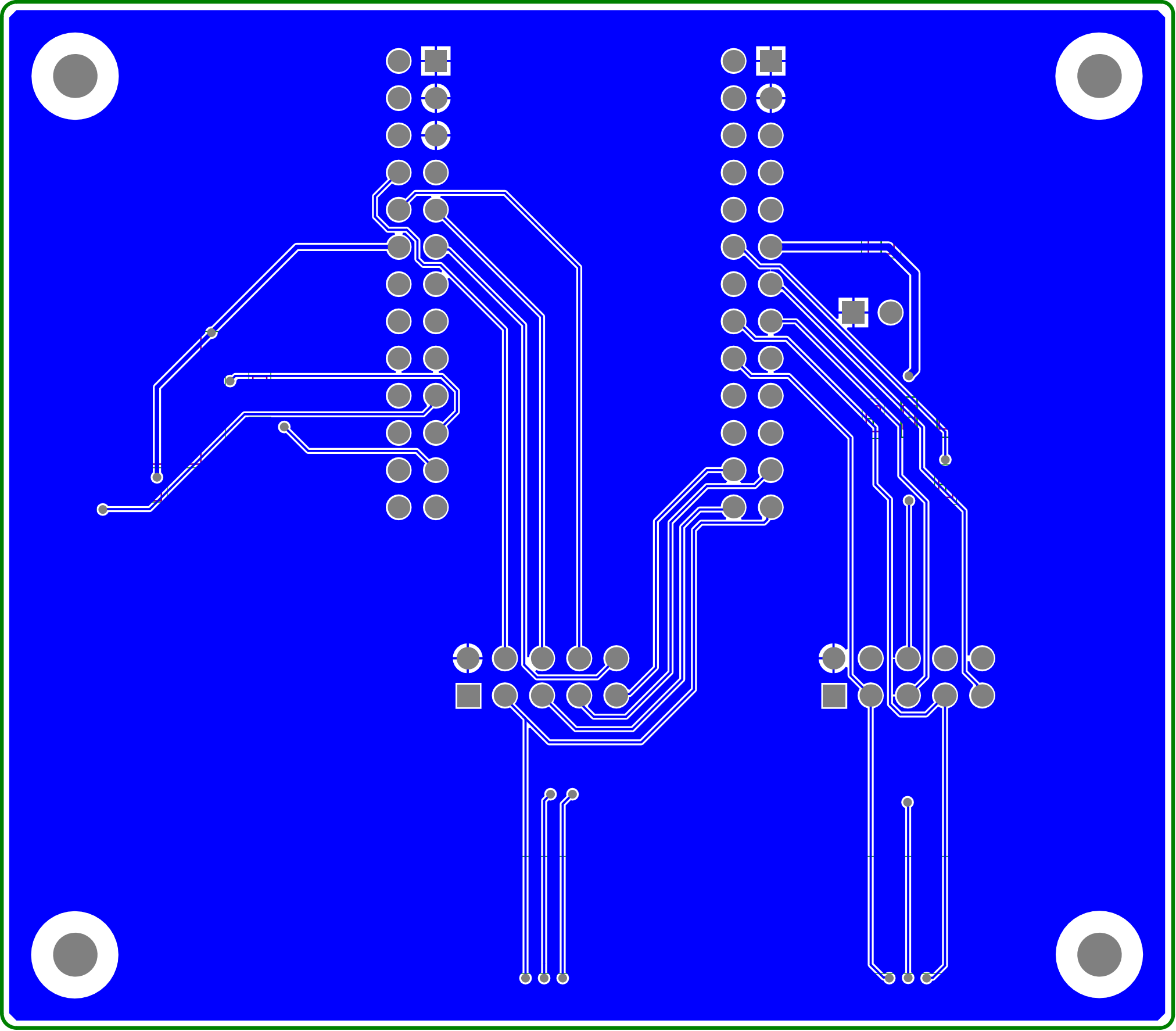
Pin Number	Bus Name	Type	Function	Description
38	CDBUS0	Output	TXD	UART Transmit to MT3620
39	CDBUS1	Input	RXD	UART Receive from MT3620
40	CDBUS2	Output	RTS	Request To Send
41	CDBUS3	Input	CTS	Clear To Send
43	CDBUS4	Output	DTR	Data Terminal Ready
44	CDBUS5	Input	DSR	Data Set Ready
45	CDBUS6	Input	DCD	Data Carrier Detect
46	CDBUS7	Input	RI/TXDEN	Ring Indicator

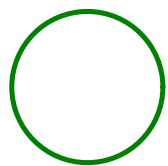
Figure 1-10 shows the pin connections for the T82110A1S100CEU. The diagram illustrates two pin headers. The left header has pins 1 through 10, with labels SERVICE_RXD, SERVICE_TXD, SERVICE_CTS, SERVICE_RTS, and GND. The right header also has pins 1 through 10, with labels SERVICE_RXD, SERVICE_TXD, SERVICE_CTS, SERVICE_RTS, and GND. The connections are as follows:

Header	Pin	Signal
Left	1	SERVICE_RXD
Left	3	SERVICE_TXD
Left	5	SERVICE_CTS
Left	7	SERVICE_RTS
Left	2, 4, 6, 8, 10	GND
Right	1	SERVICE_RXD
Right	3	SERVICE_TXD
Right	5	SERVICE_CTS
Right	7	SERVICE_RTS
Right	2, 4, 6, 8, 10	GND

Pin connection diagram for the T821110A1S100CEU module. The diagram shows two rows of pins. The left row has pins 1, 3, 5, 7, and 9, which are connected to RECOVERY_RXD, RECOVERY_TXD, RECOVERY_CTS, and RECOVERY_RTS respectively. The right row has pins 2, 4, 6, 8, and 10, which are connected to SYSRST_N, RECOVERY, WAKEUP, and another WAKEUP signal. A 2K2 resistor (R15) is connected between the RECOVERY pin and the WAKEUP pin. A 2K2 resistor (R16) is connected between the WAKEUP pin and GND.







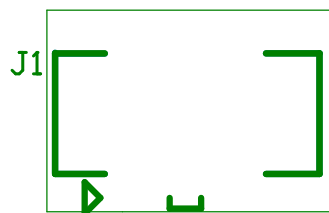
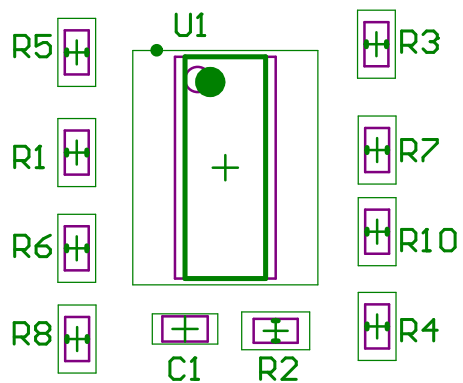
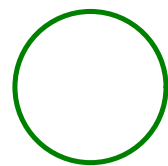
P-FTINT-2-0



M1



USB



SWD

Debug

GND RXD TXD CTS RTS

J3

Service

RXD TXD CTS RTS

GND

RESET

RECOVERY

J4

Recovery

RXD TXD CTS RTS

Debug

GND RXD TXD CTS RTS

J5

Service

RXD TXD CTS RTS

GND

RESET

RECOVERY

J6

Recovery

RXD TXD CTS RTS

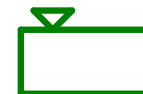
VBUS
VCC



R11

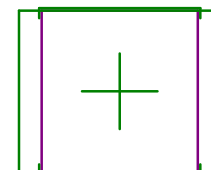


R12



J2

SW1



R14



D2



D1



R13



R15



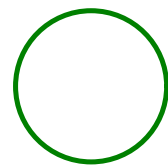
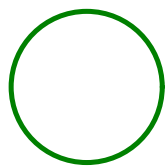
R16



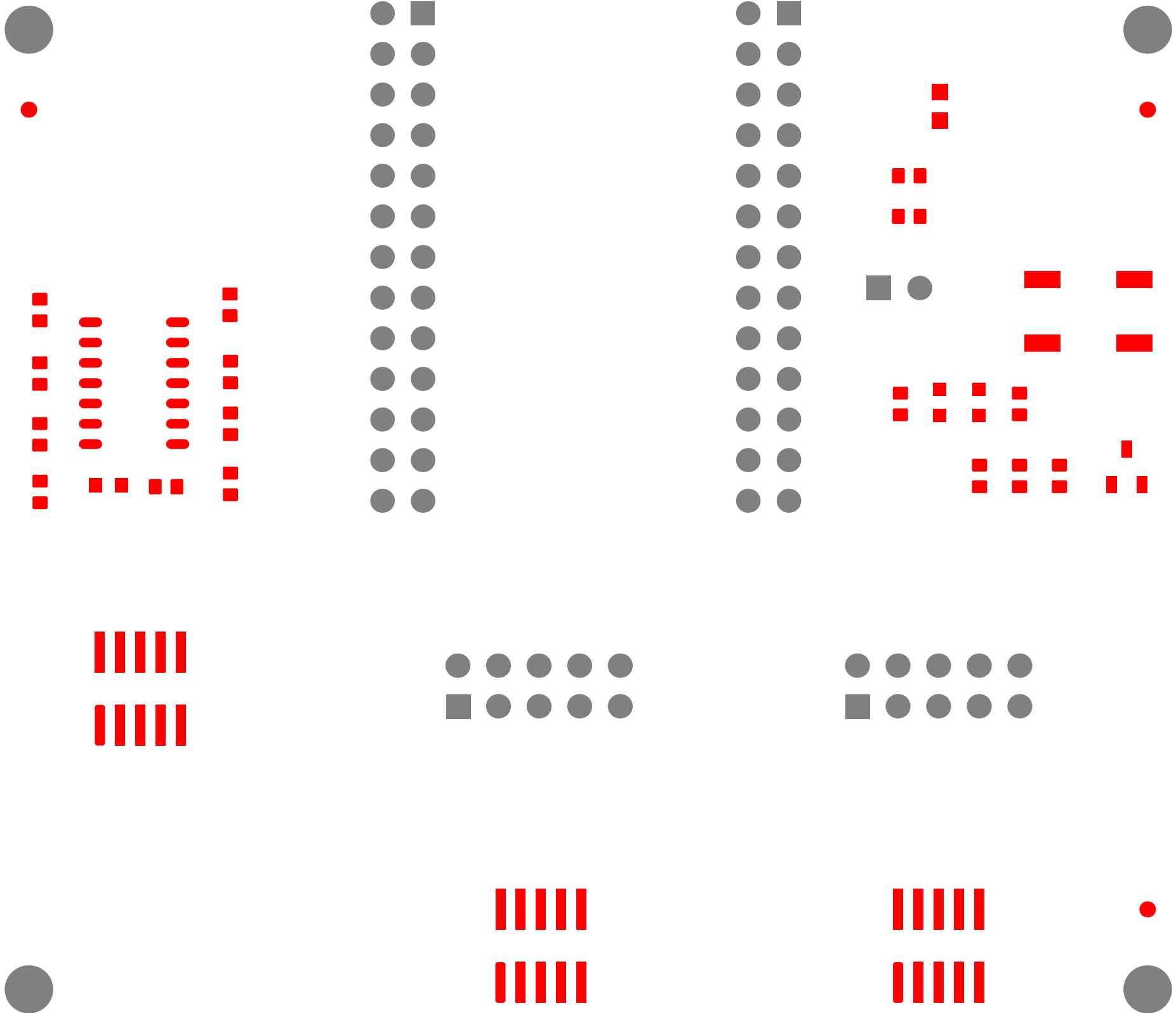
R9

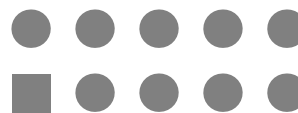
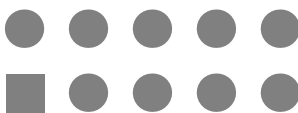
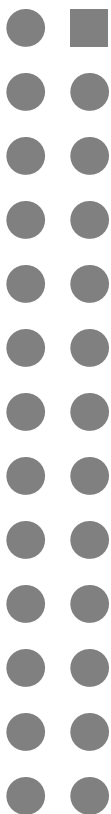


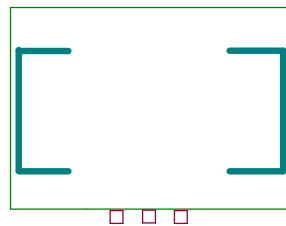
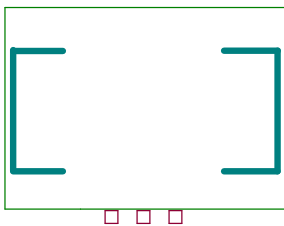
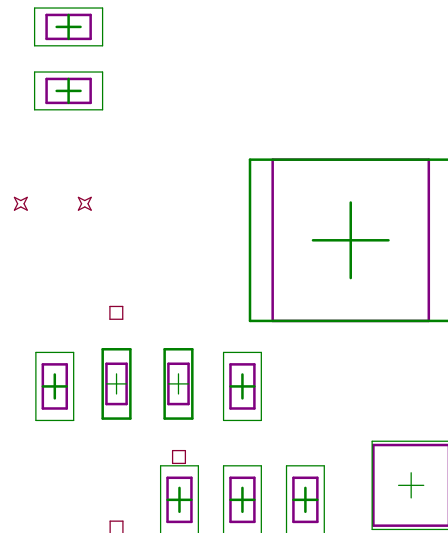
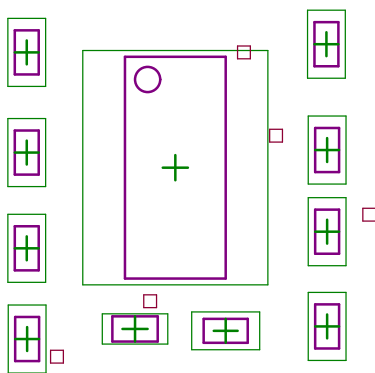
D3

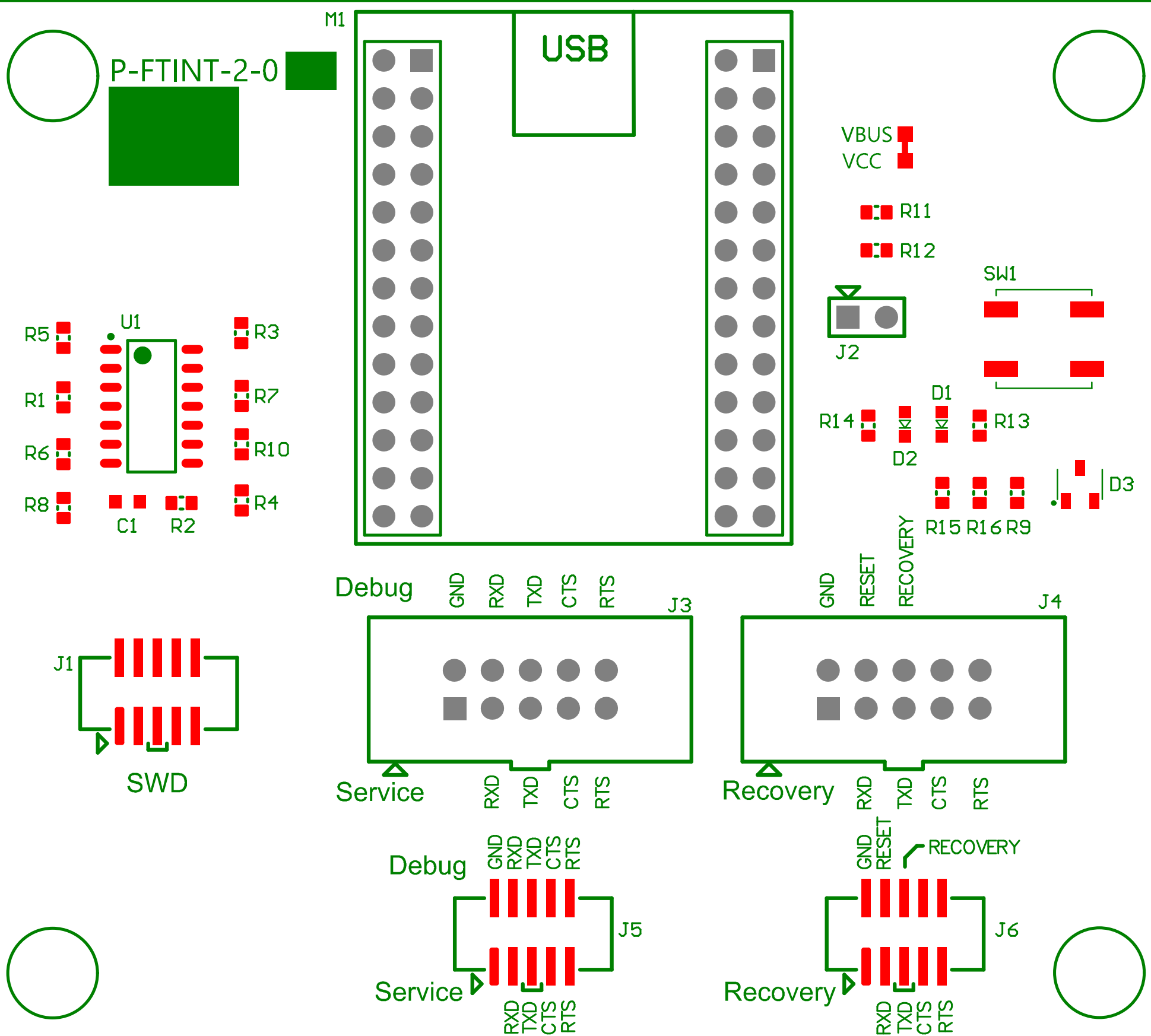


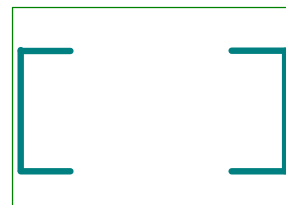
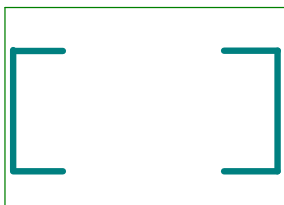
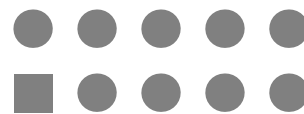
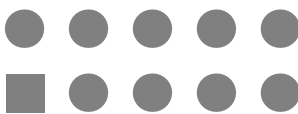
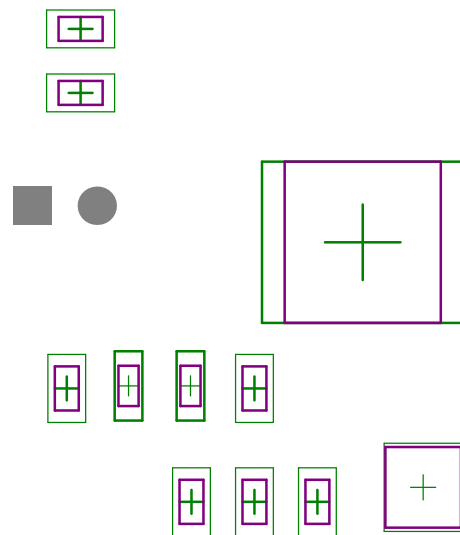
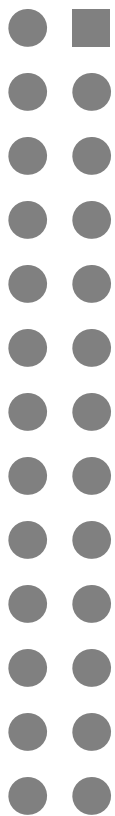
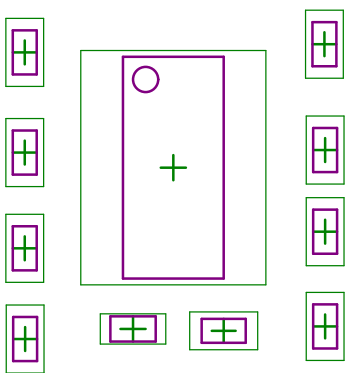







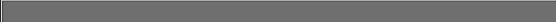













Board Stack Report

Stack Up		Layer Stack			
Layer	Board Layer Stack	Name	Material	Thickness	Constant
1		Top Paste			
2		Top Overlay			
3		Top Solder	Solder Resist	0.010mm	3.5
4		Component Side	Copper	0.036mm	
5		Dielectric 1	FR-4	1.500mm	4.3
6		Solder Side	Copper	0.036mm	
7		Bottom Solder	Solder Resist	0.010mm	3.5
8		Bottom Overlay			
9		Bottom Paste			
	Height : 1.591mm				