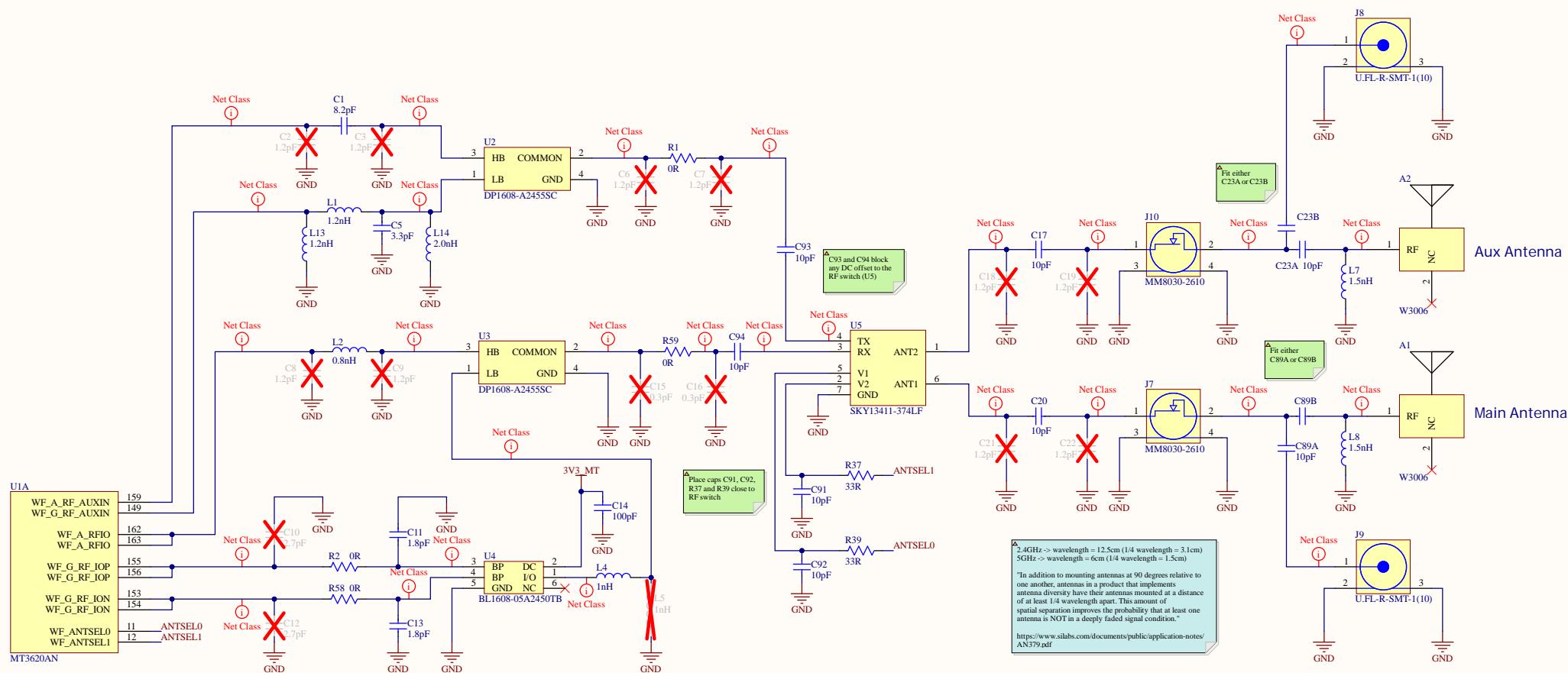
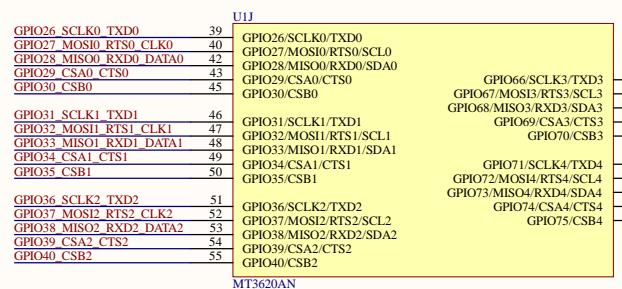


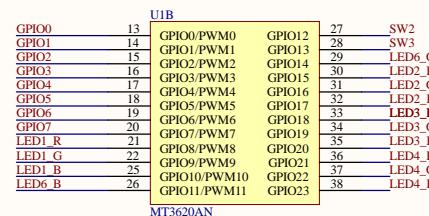
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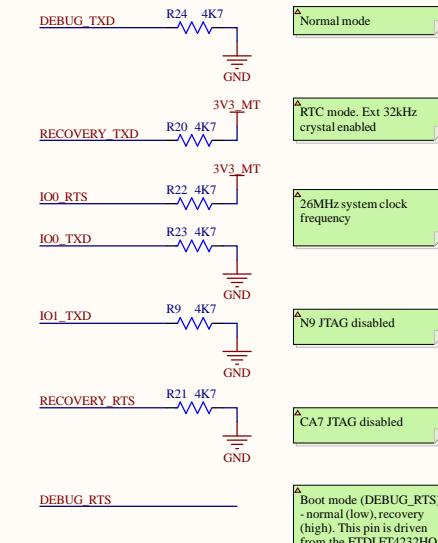
Configurable I/O Blocks



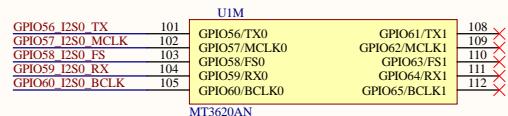
GPIOs



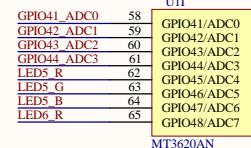
Strapping



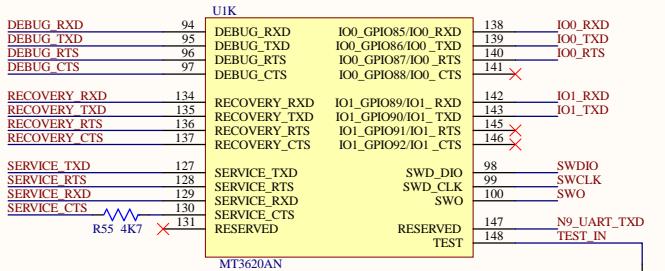
I2S Audio



ADCs and GPIOs

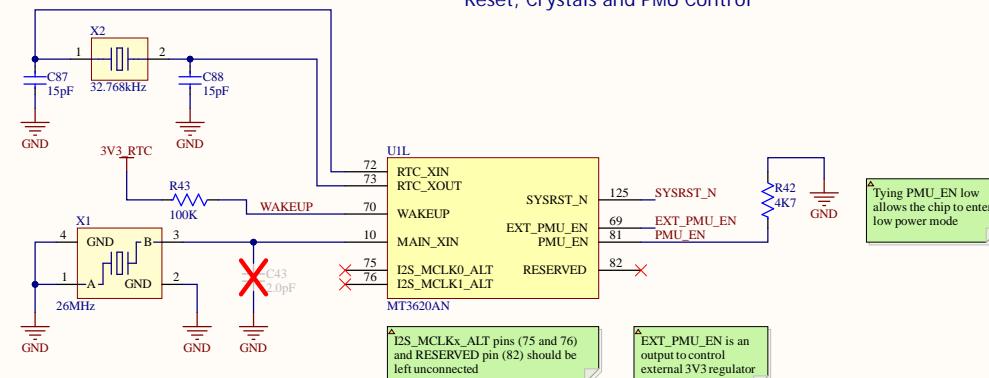


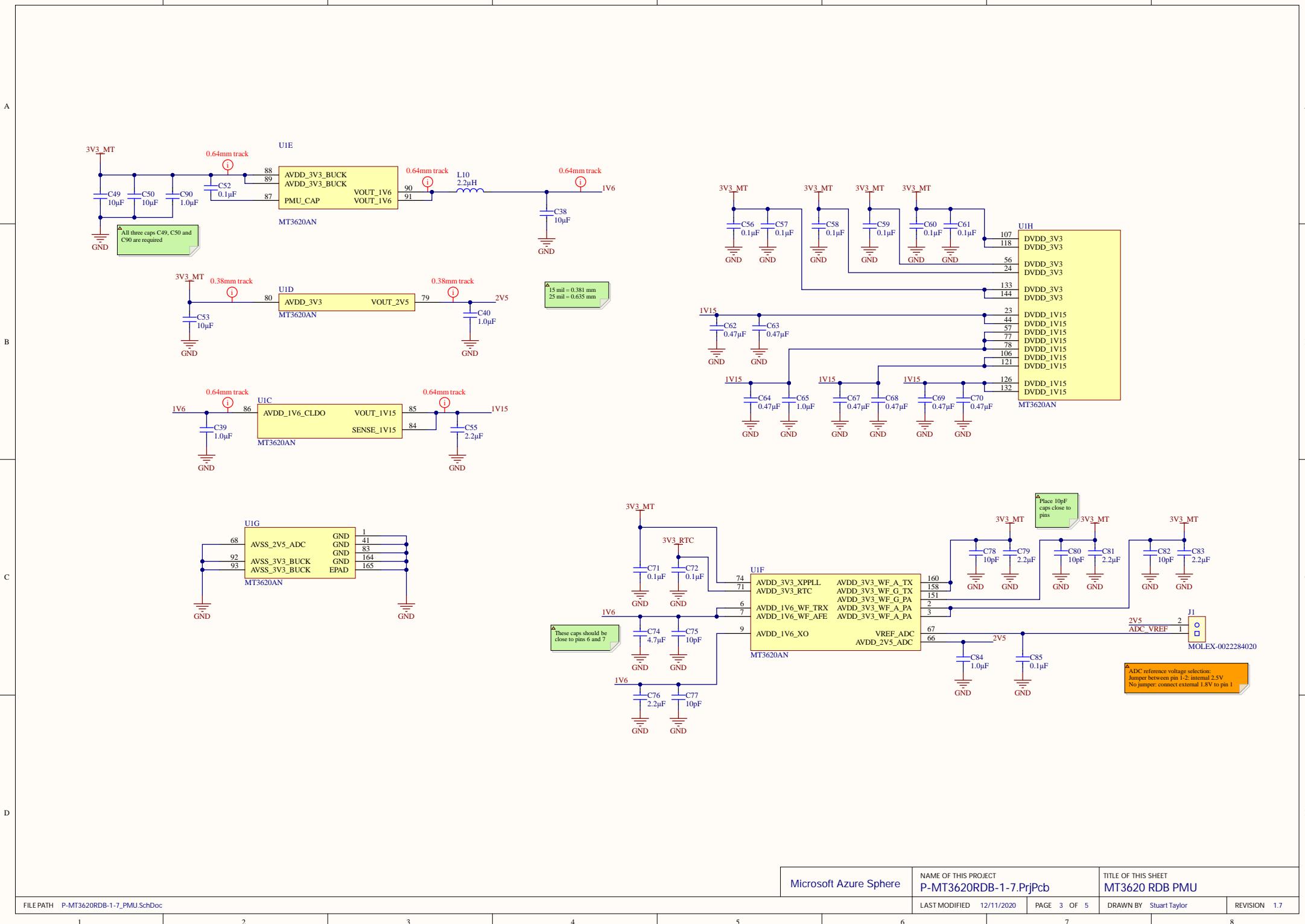
Support Interface



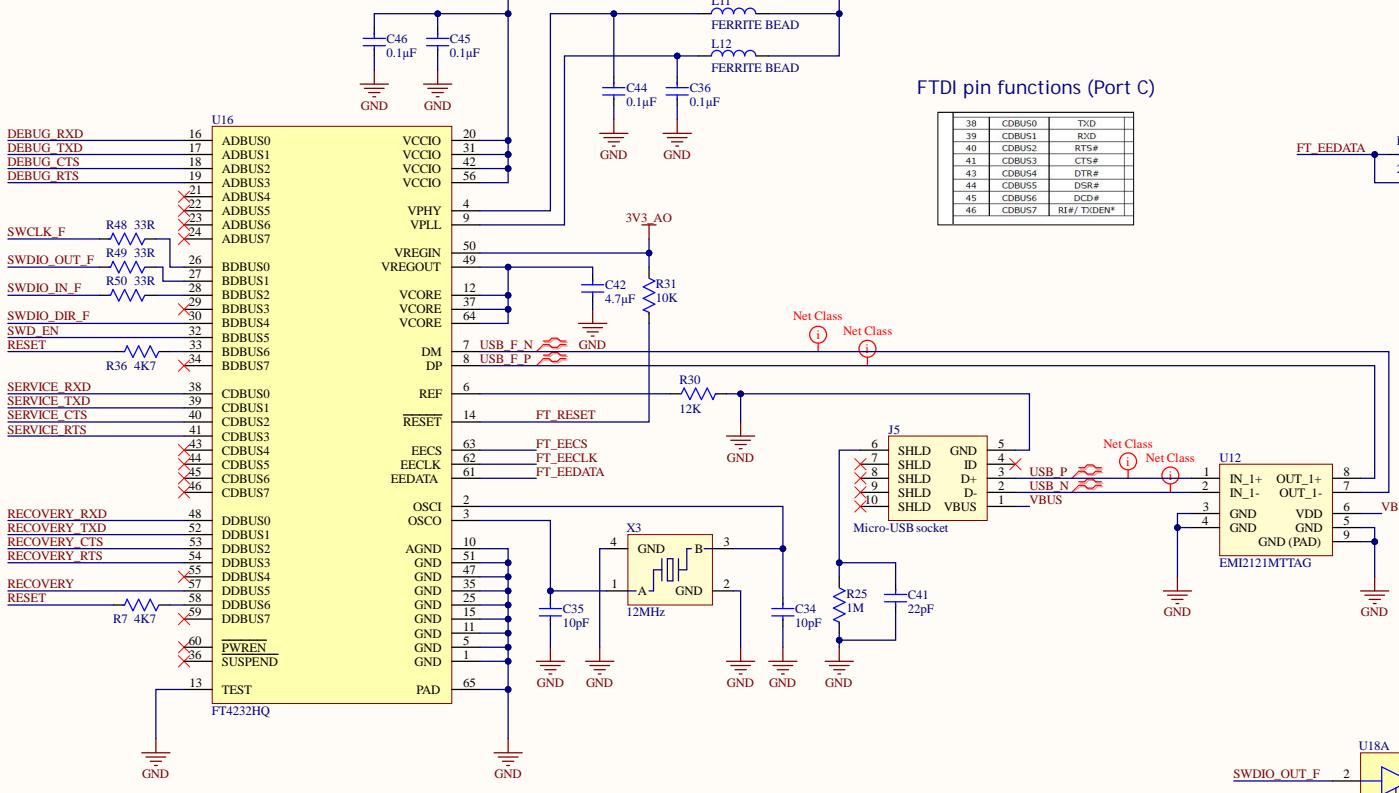
CL=12.5pF
Using the equation given in
<http://foxonline.com/pdfs/xtaldesignnotes.pdf>
(and assuming stray capacitance of 5pF), CL1 and
CL2 = 15pF

Reset, Crystals and PMU Control

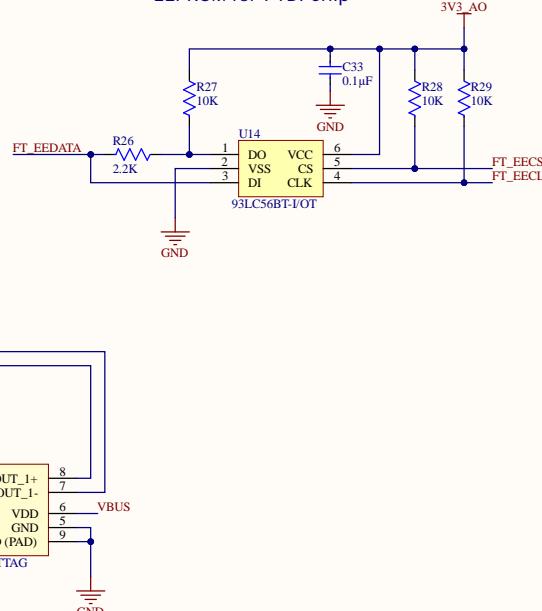




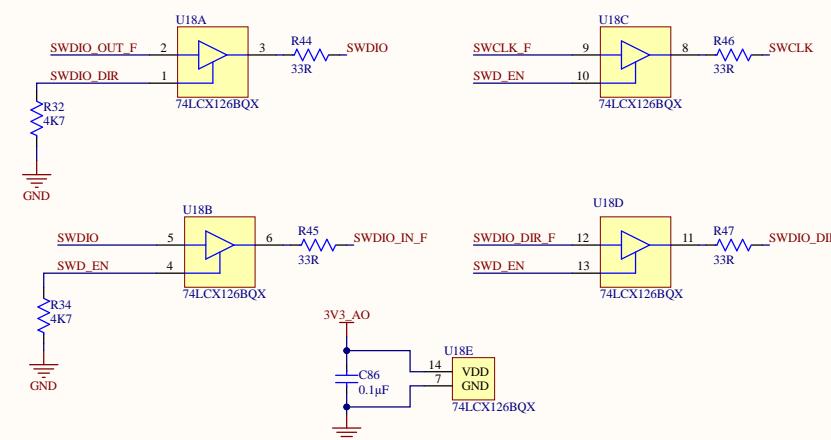
FTDI USB Interface



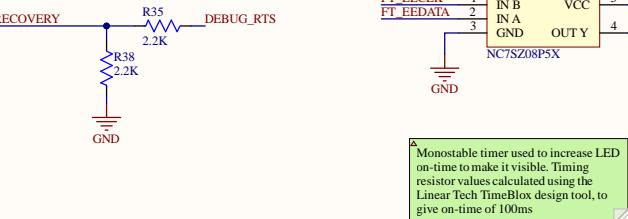
EEPROM for FTDI Chip



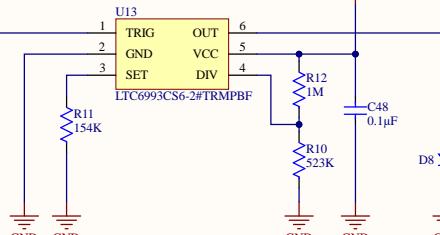
SWD_DIO Tri-state Buffer

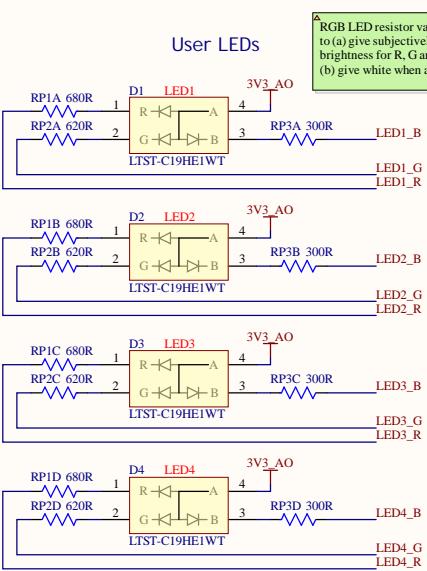
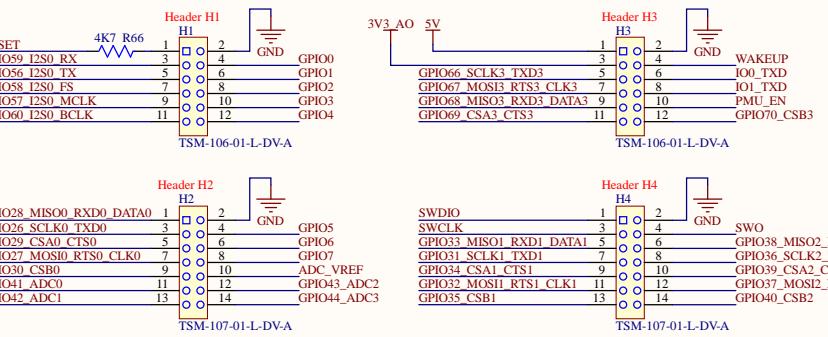


FTDI Recovery

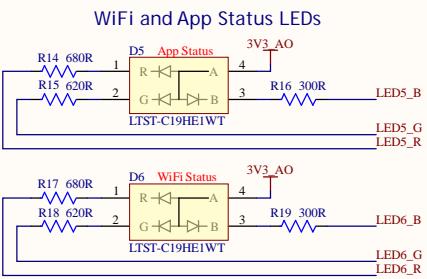
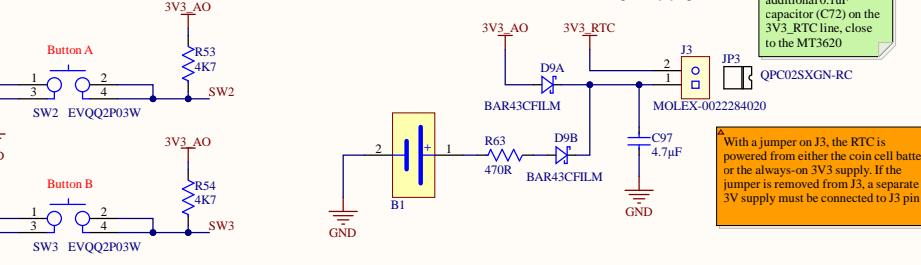
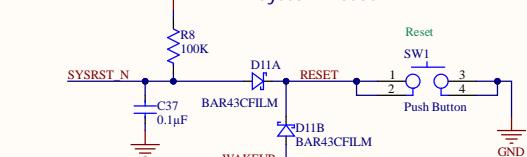
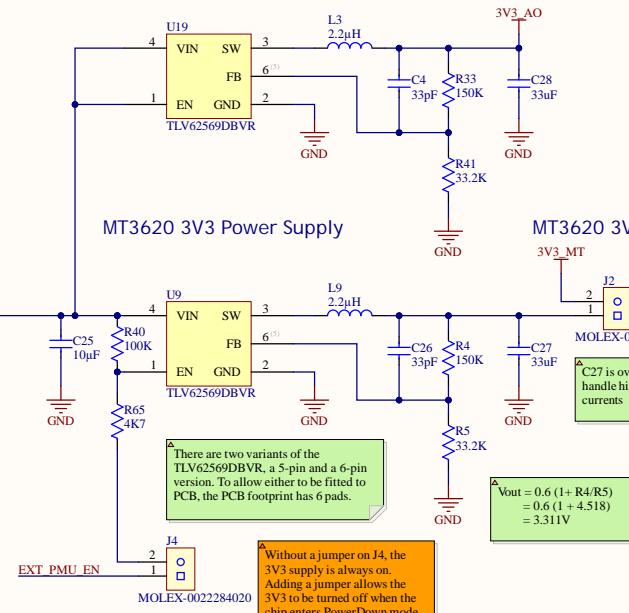
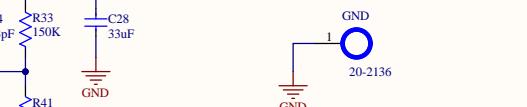
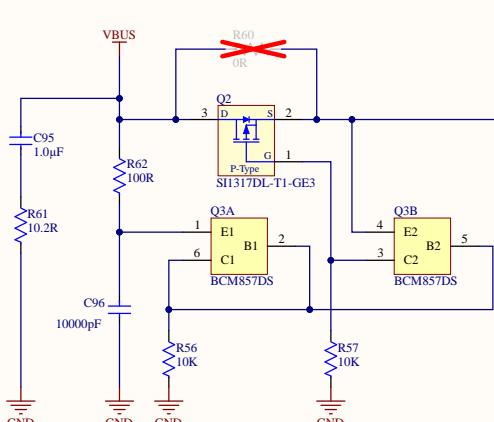
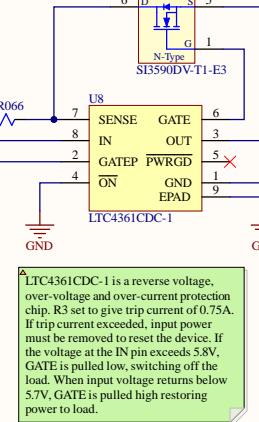


FTDI Activity LED



User LEDs**Interface Headers****Test Points**

WAKEUP	DEBUG_RTS
EXT_PMU_EN	RECOVERY_CTS
RECOVERY	SERVICE_RXD
N9_UART_TXD	SERVICE_TXD
1V15	SERVICE_RTS
1V6	SERVICE_CTS
SYSRST_N	FT_EECS
SWCLK	FT_EECLK
SWDIO	FT_EEDATA
SWO	GND
TEST_IN	FT_RESET
ANTSEL1	SWD_EN
ANTSEL0	GND
3V3_RTC	3V3_RTC
DEBUG_RXD	RECOVERY_RXD
DEBUG_TXD	RECOVERY_TXD
RECOVERY_RTS	IO1_RXD
IO0_RXD	IO1_RXD
IO1_RXD	3V3_AO
RECOVERY_RTS	3V3_AO

User Buttons**3V3 RTC Battery Supply****System Reset****Always-on 3V3 Power Supply****Ground Test Point****Ideal Diode****Negative-Voltage / Over-Voltage / Over-Current Protection****5V Power Socket**