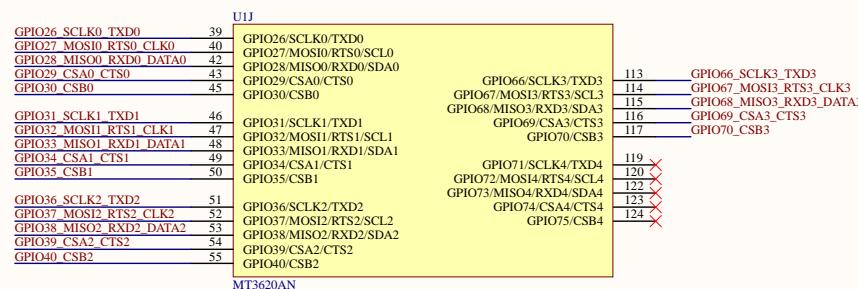
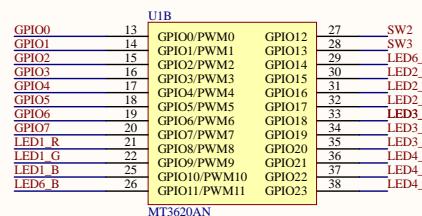


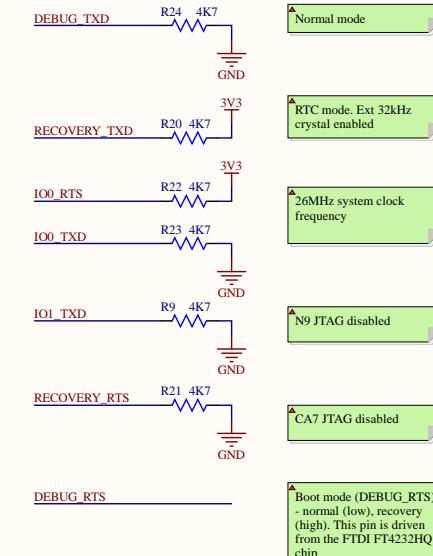
## Configurable I/O Blocks



## GPIOs



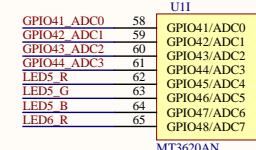
## Strapping



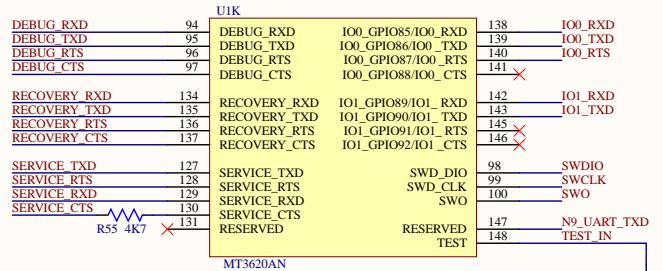
## I2S Audio



## ADCs and GPIOs

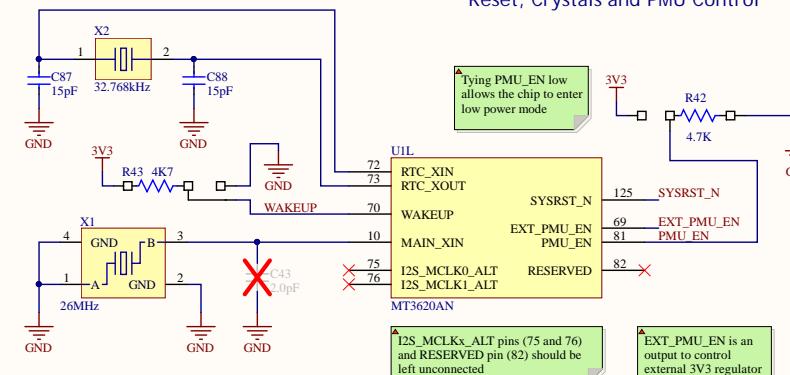


## Support Interface

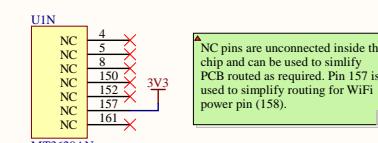


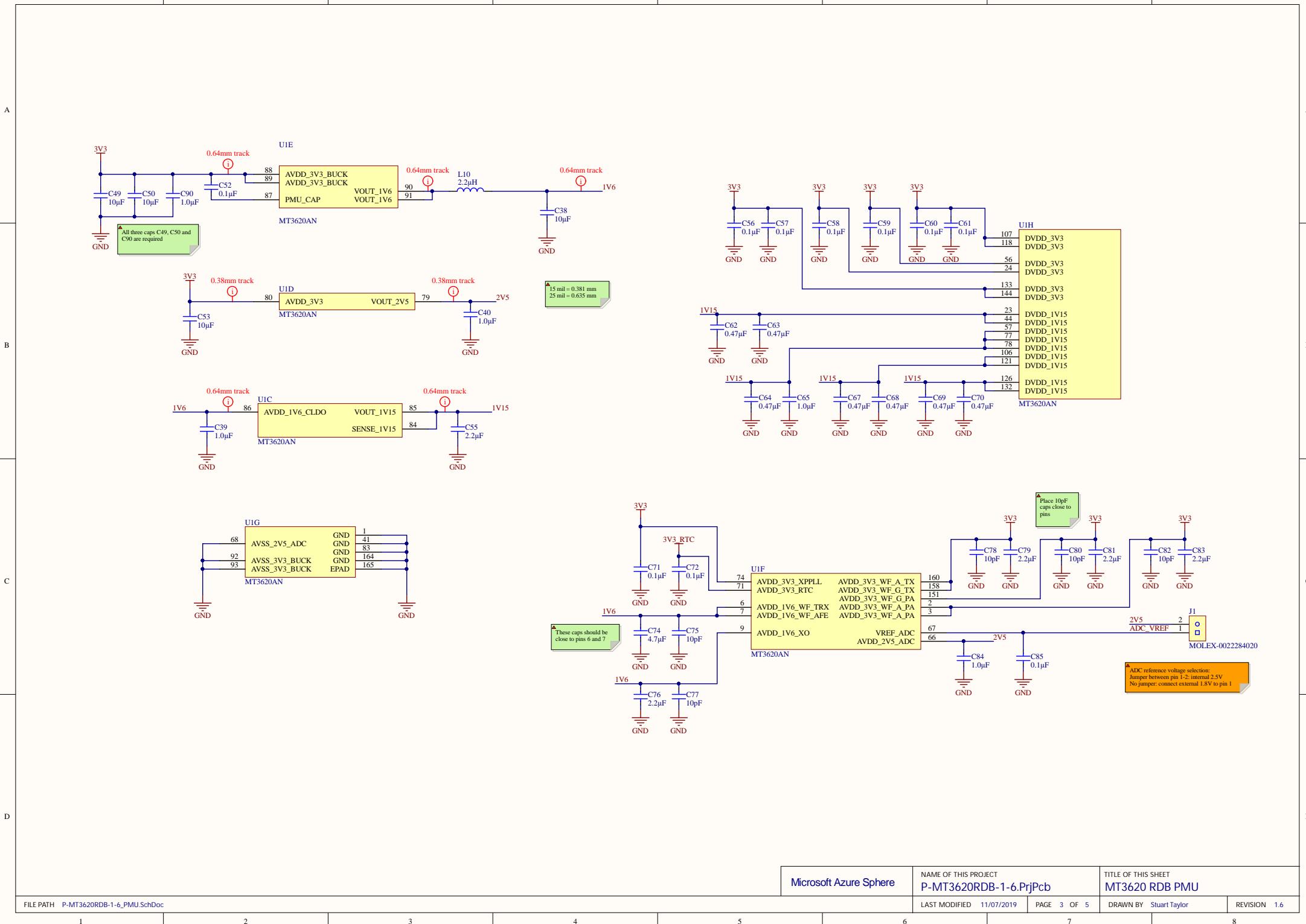
CL=12.5pF  
Using the equation given in  
<http://foxonline.com/pdfs/xtaldesignnotes.pdf>  
(and assuming stray capacitance of 5pF), CL1 and  
CL2 = 15pF

## Reset, Crystals and PMU Control

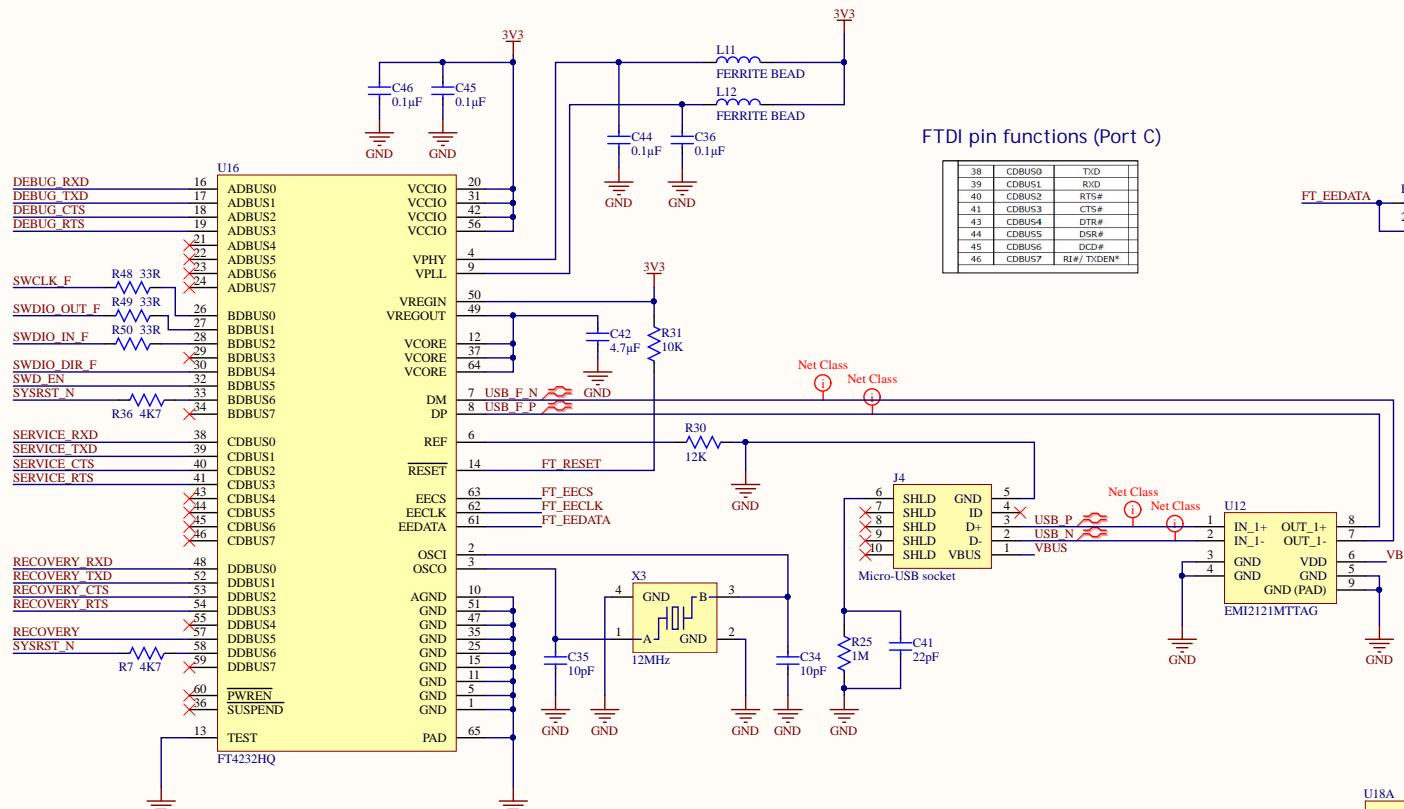


## NC Pins

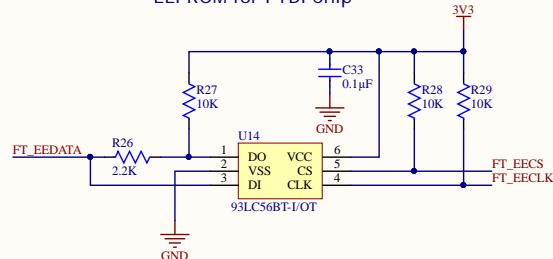




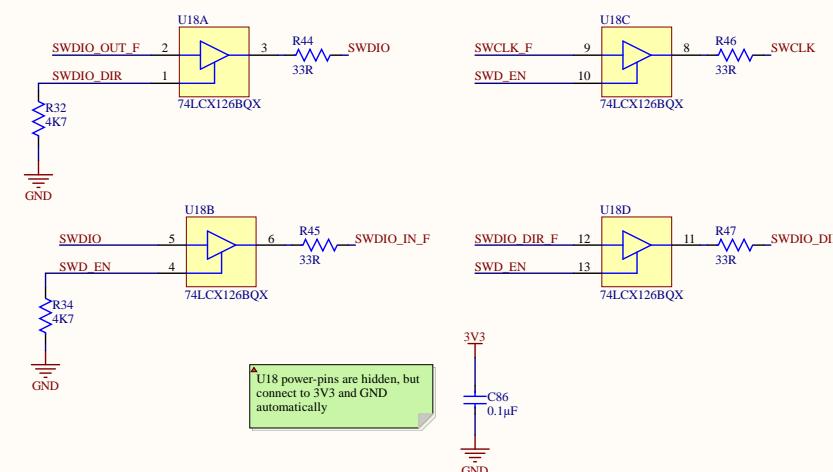
### FTDI USB Interface



### EEPROM for FTDI Chip



### SWD\_DIO Tri-state Buffer



### FTDI Recovery



### FTDI Activity LED

