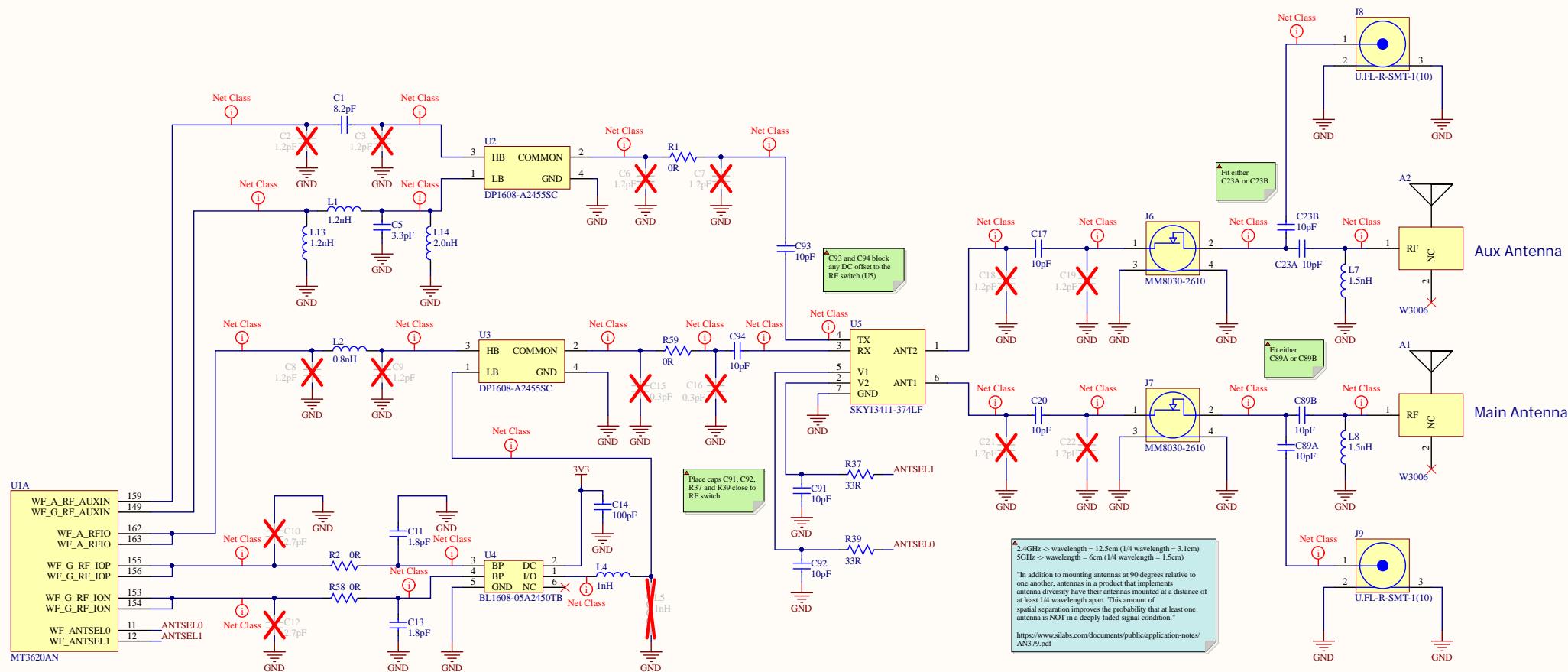


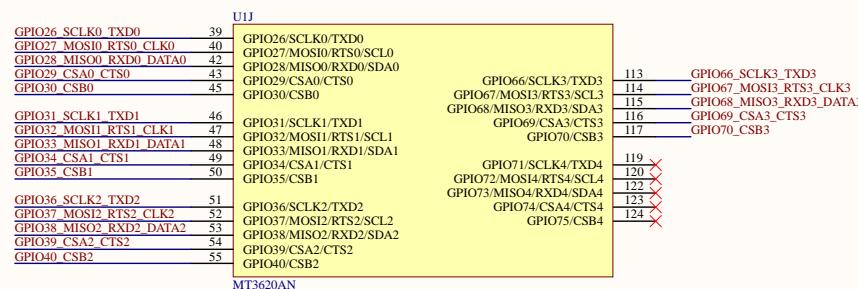
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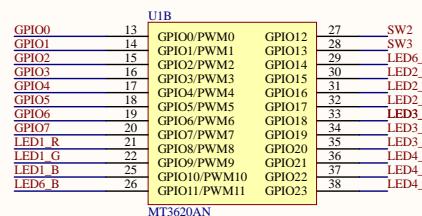
Microsoft Azure Sphere

NAME OF THIS PROJECT  
P-MT3620RDB-1-0.PnjPcbTITLE OF THIS SHEET  
MT3620 RDB WiFi

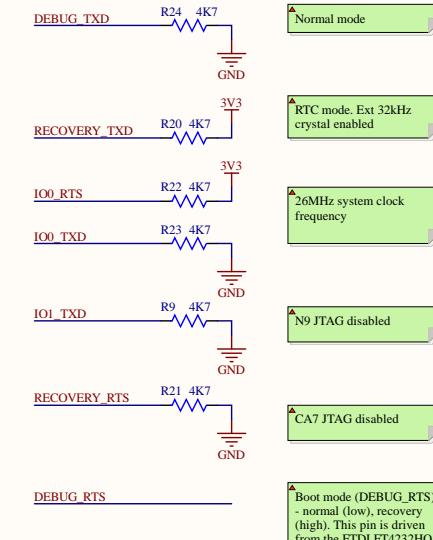
## Configurable I/O Blocks



## GPIOs



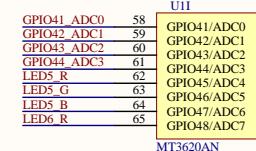
## Strapping



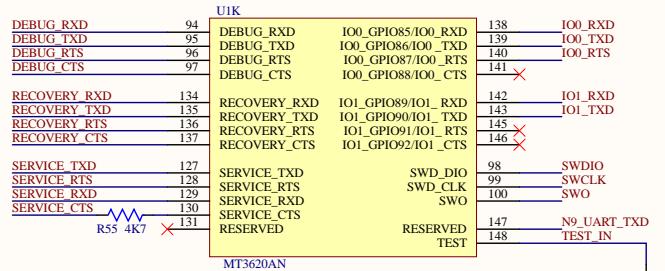
## I2S Audio



## ADCs and GPIOs

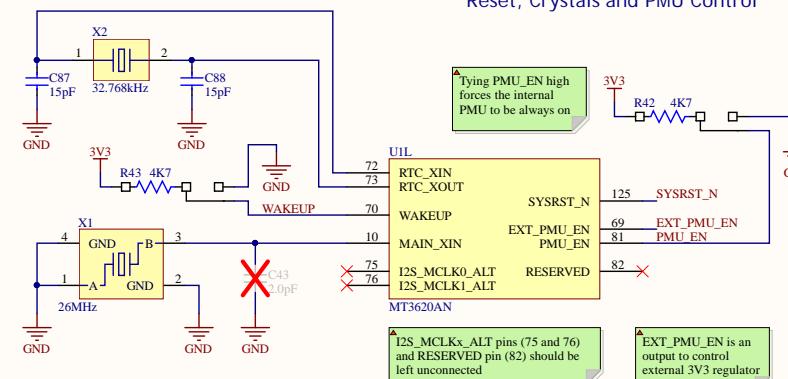


## Support Interface

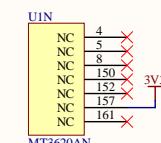


CL=12.5pF  
Using the equation given in  
<http://foxonline.com/pdfs/xtaldesignnotes.pdf>  
(and assuming stray capacitance of 5pF), CL1 and  
CL2 = 15pF

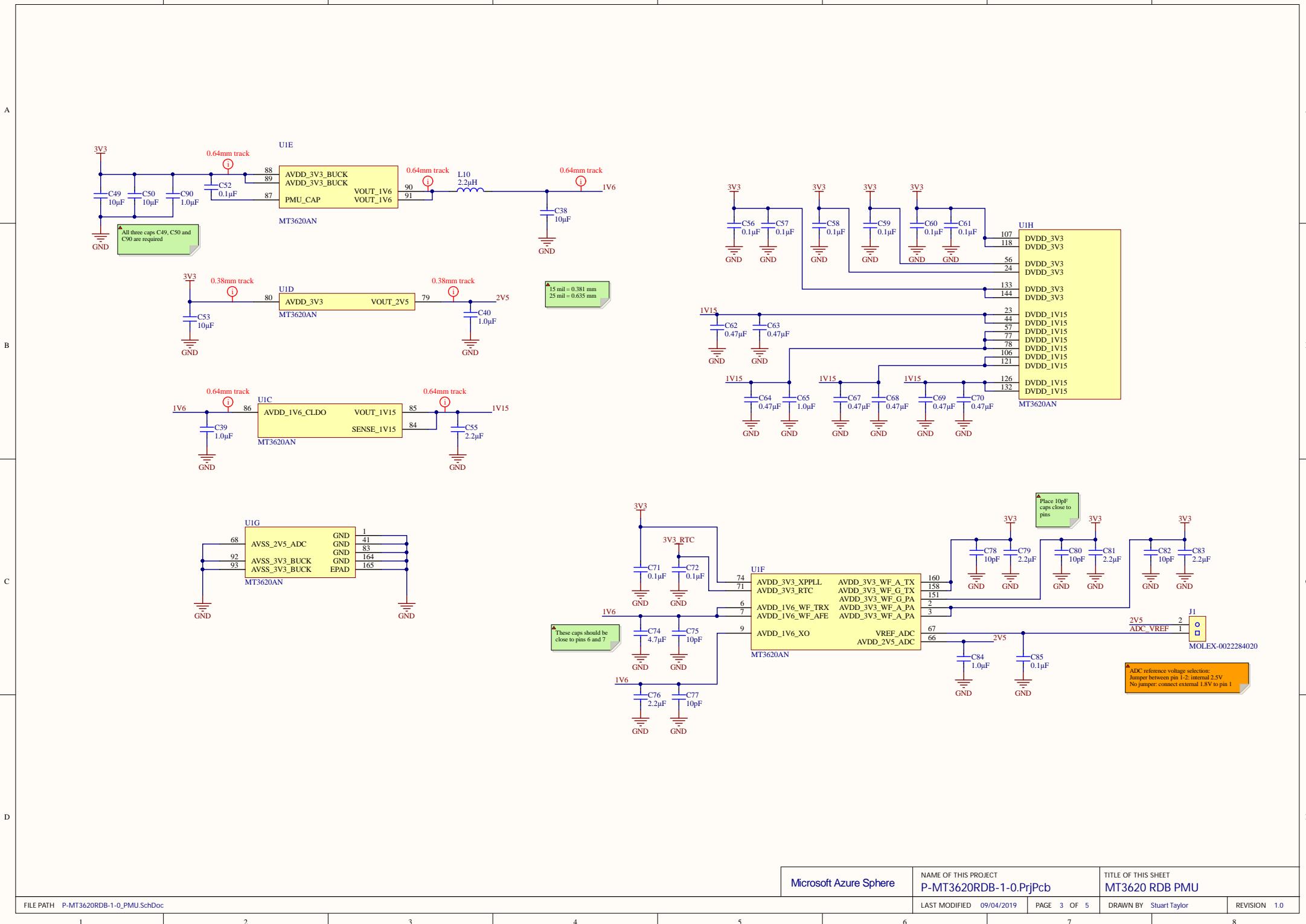
## Reset, Crystals and PMU Control



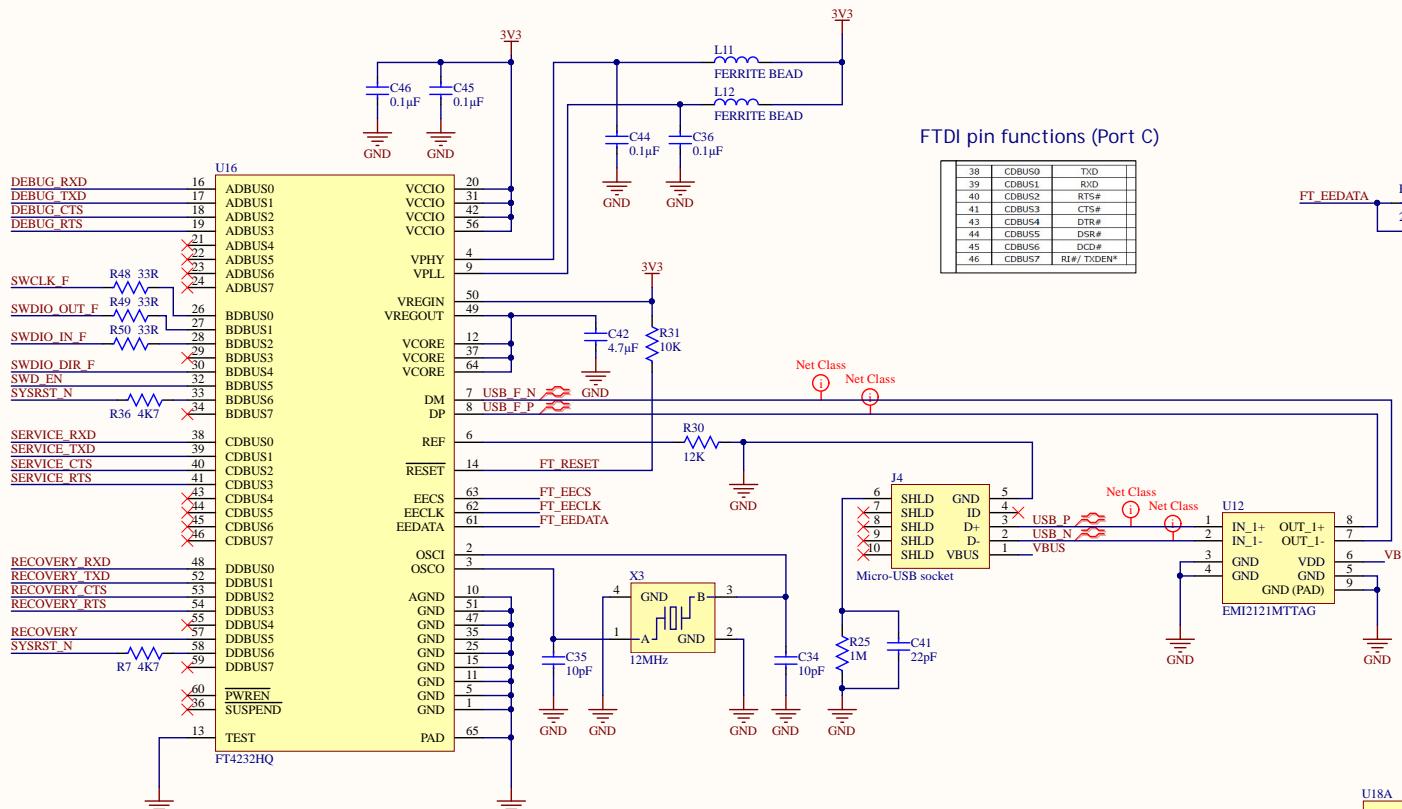
## NC Pins



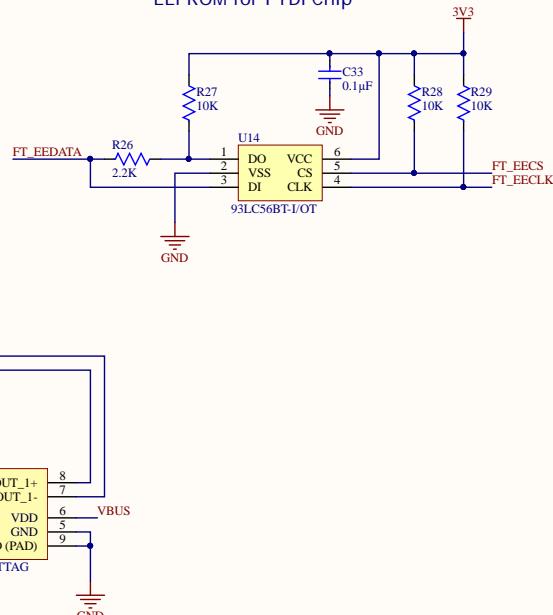
NC pins are unconnected inside the chip and can be used to simplify PCB routed as required. Pin 157 is used to simplify routing for WiFi power pin (158).



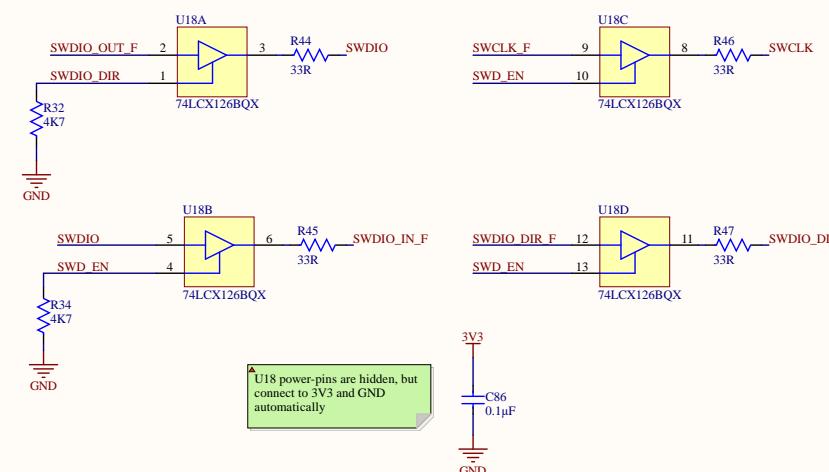
### FTDI USB Interface



### EEPROM for FTDI Chip



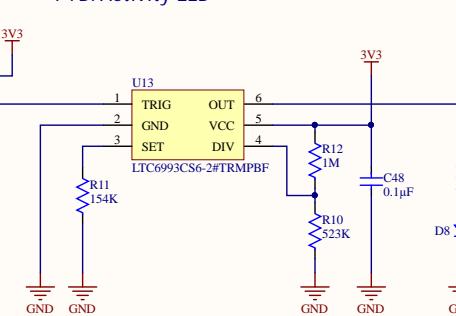
### SWD\_DIO Tri-state Buffer

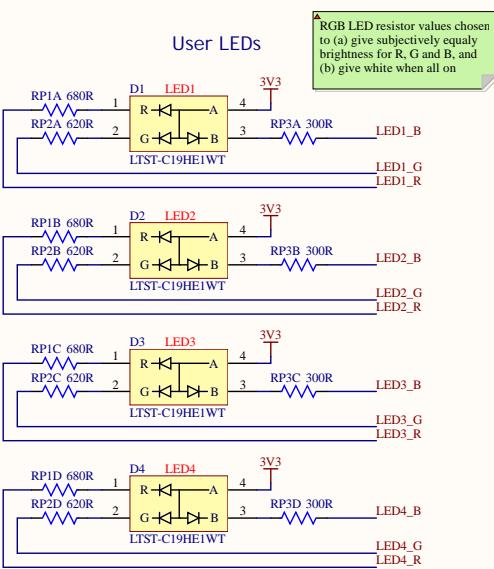
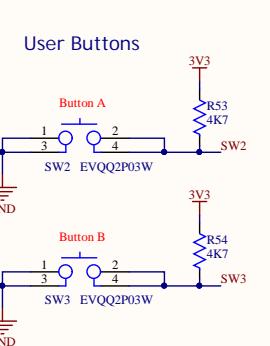
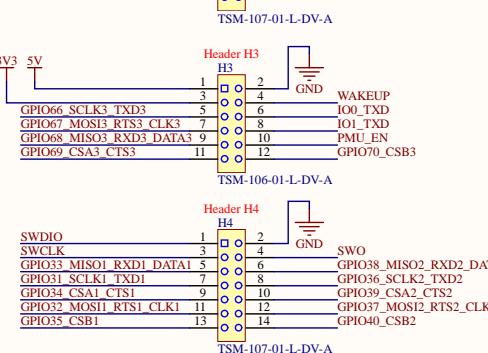
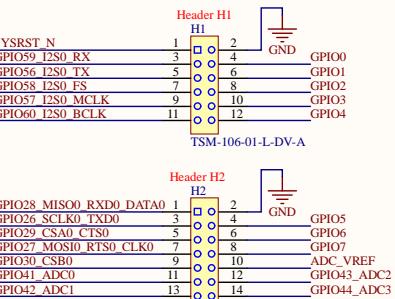
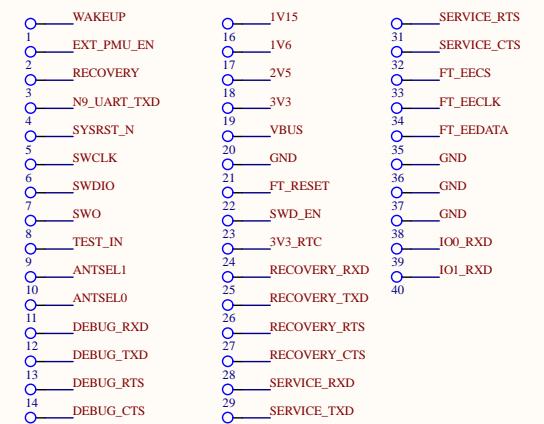
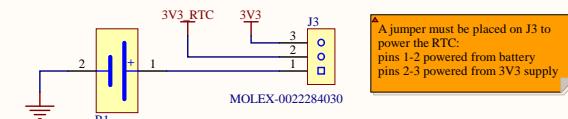
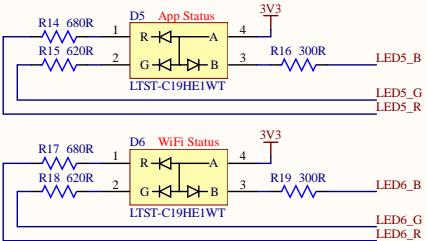
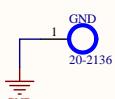
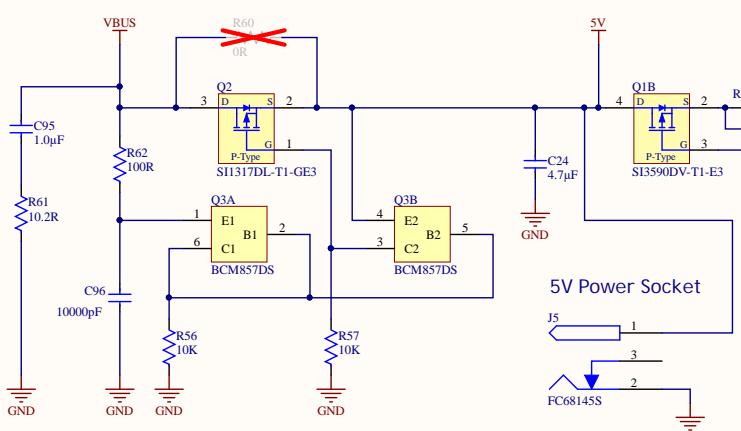
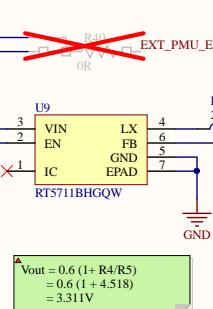


### FTDI Recovery



### FTDI Activity LED



**User LEDs****User Buttons****Interface Headers****Test Points****3V3 RTC Battery Supply****WiFi and App Status LEDs****Ground Test Point****Negative-Voltage / Over-Voltage / Over-Current Protection****Ideal Diode****3V3 Buck Converter****3V3 Power Supply LED**