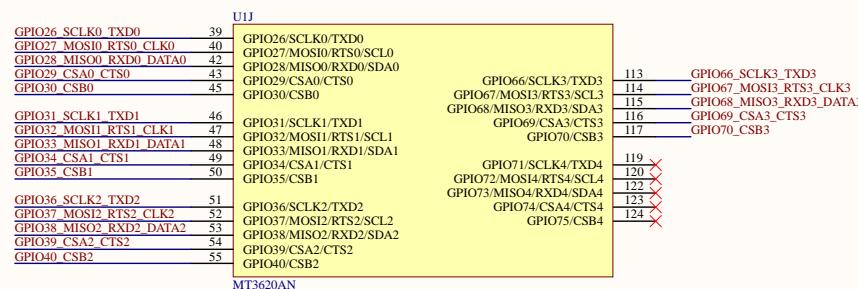
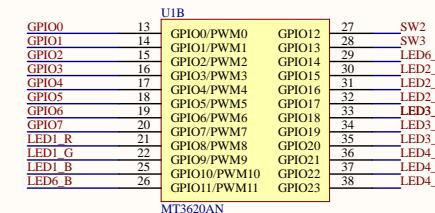


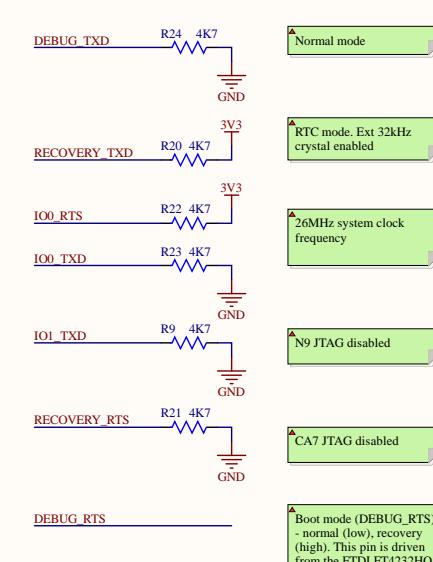
Configurable I/O Blocks



GPIOs



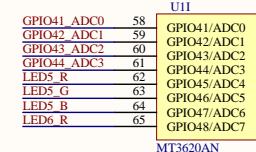
Strapping



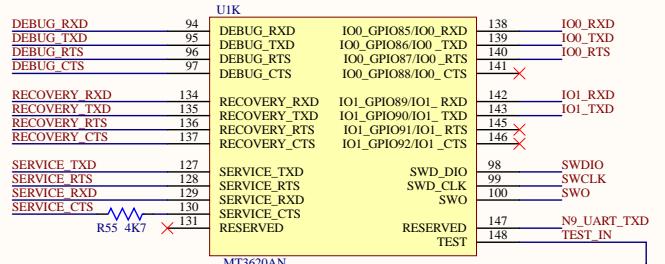
I2S Audio



ADCs and GPIOs

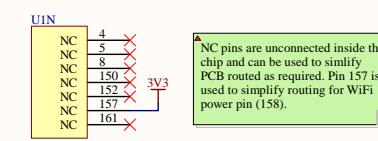


Support Interface



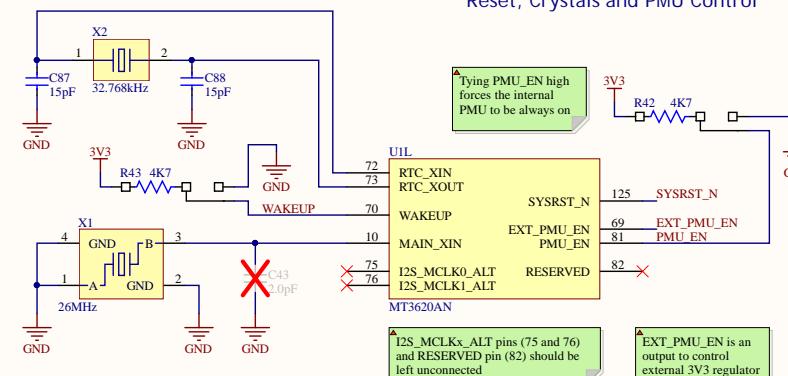
CL=12.5pF
Using the equation given in
<http://foxonline.com/pdfs/xtaldesignnotes.pdf>
(and assuming stray capacitance of 5pF), CL1 and
CL2 = 15pF

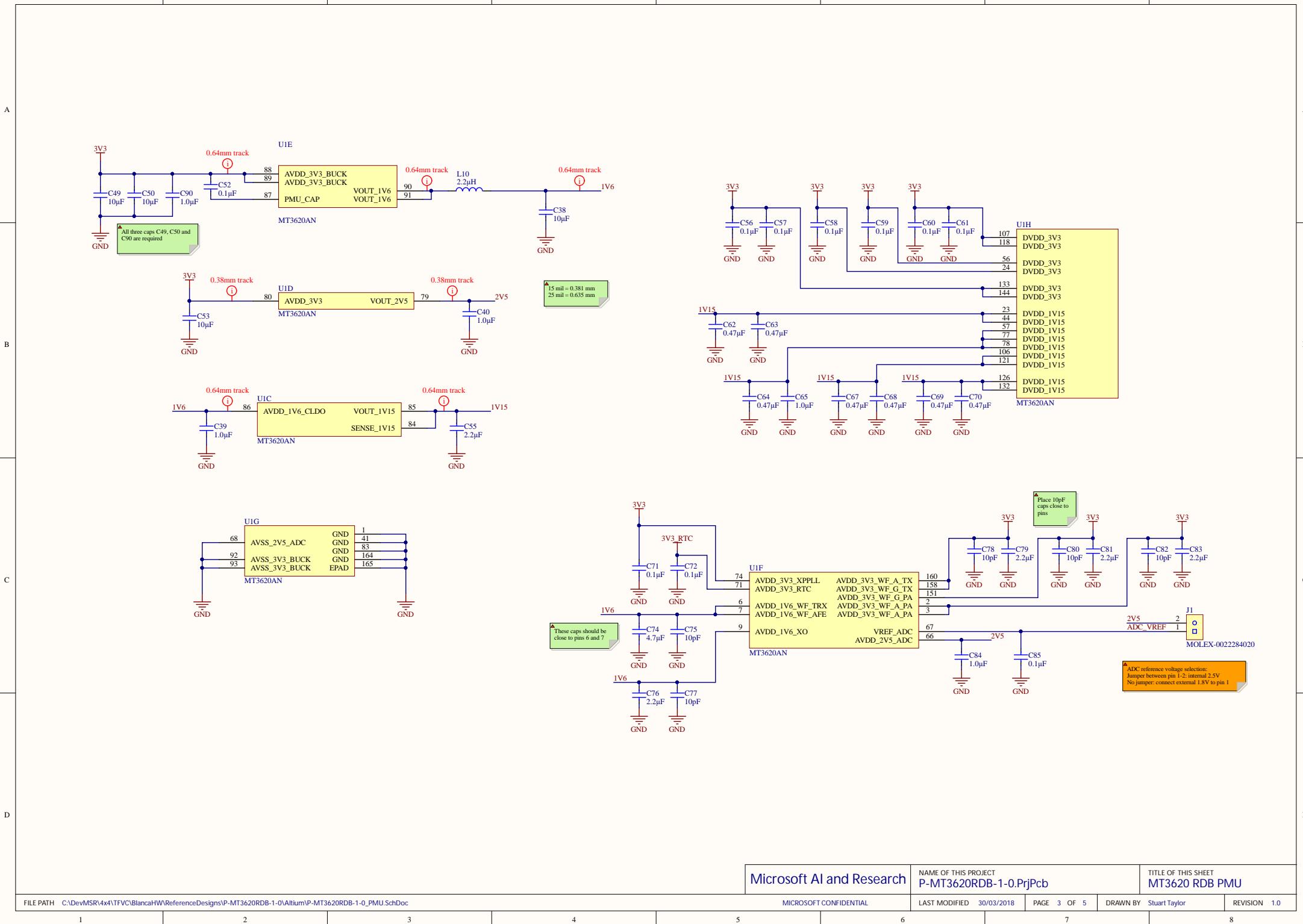
NC Pins



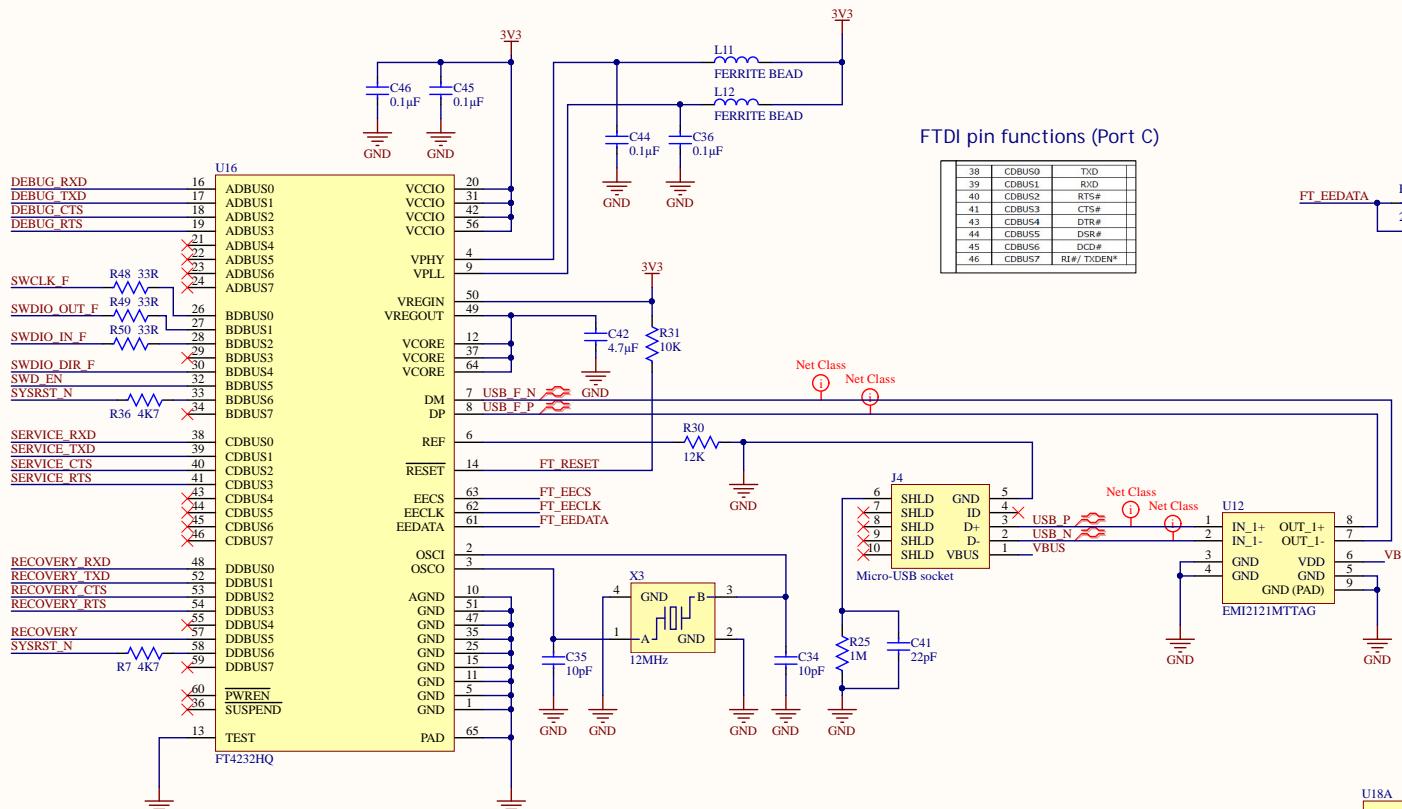
NC pins are unconnected inside the chip and can be used to simplify PCB routed as required. Pin 157 is used to simplify routing for WiFi power pin (158).

Reset, Crystals and PMU Control

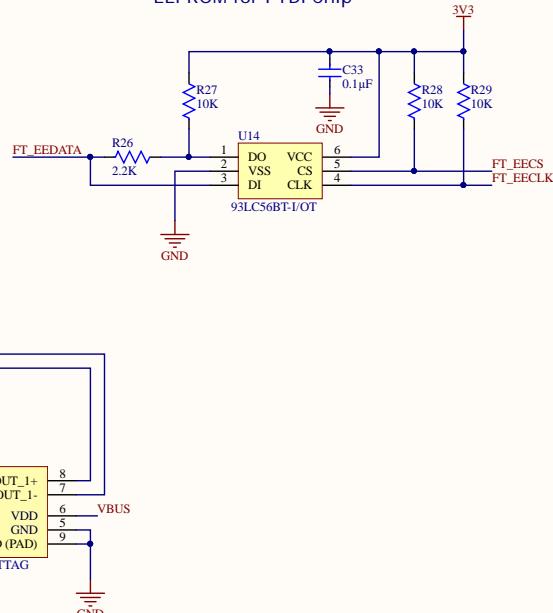




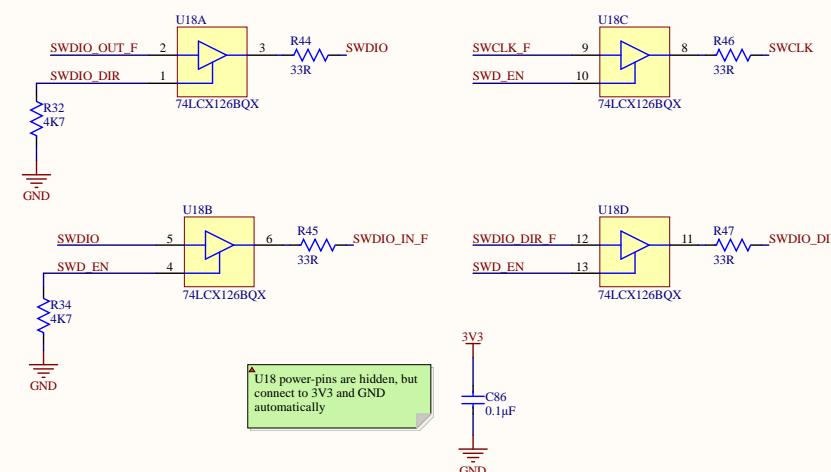
FTDI USB Interface



EEPROM for FTDI Chip



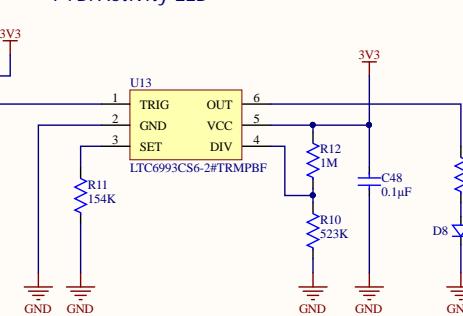
SWD_DIO Tri-state Buffer

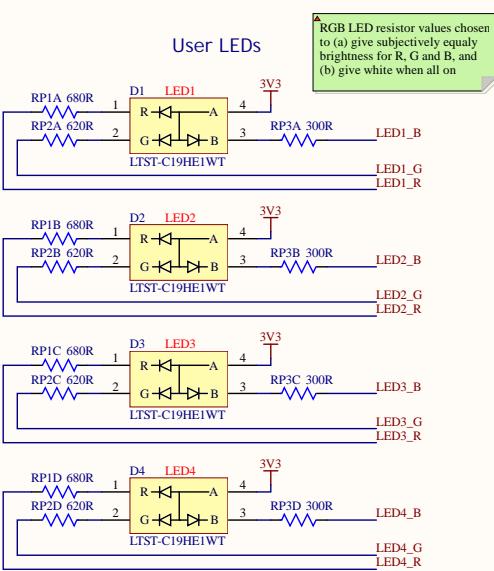
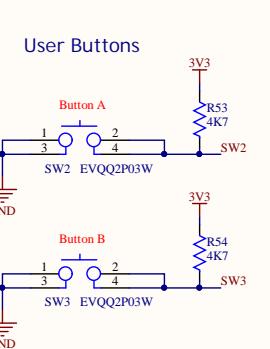
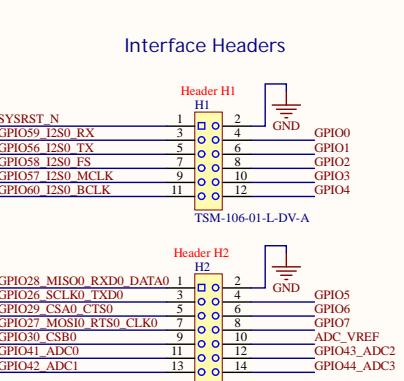
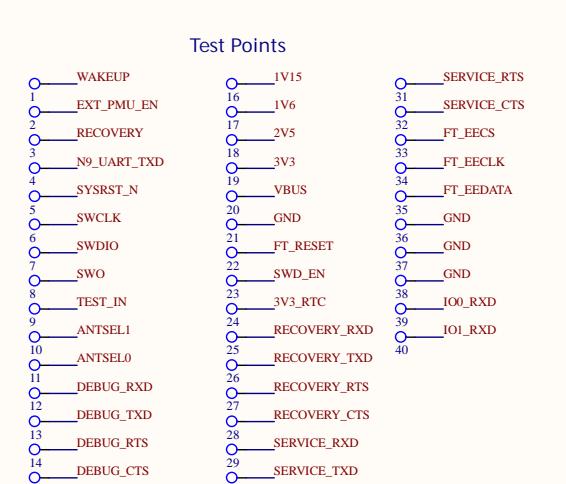
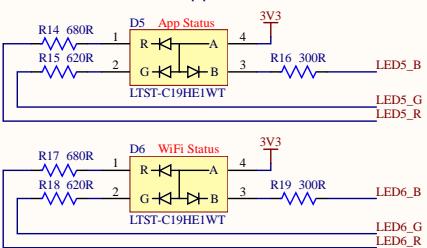
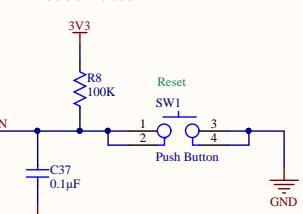
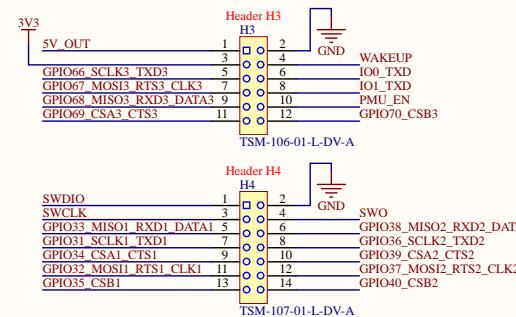
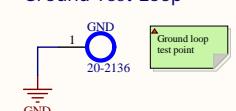
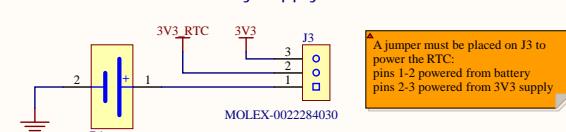
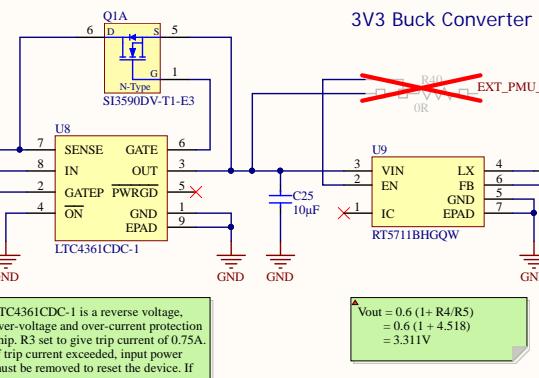
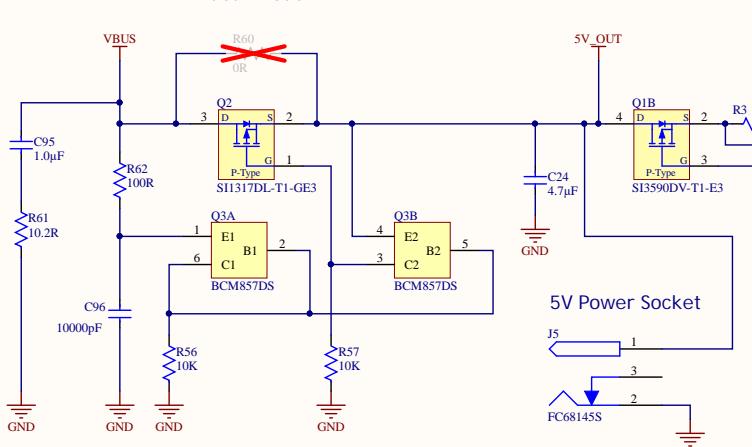
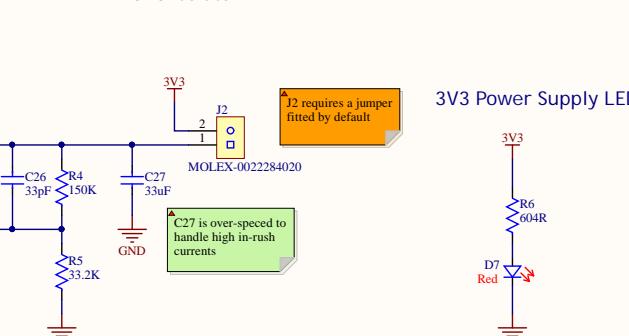


FTDI Recovery



FTDI Activity LED



User LEDs**User Buttons****Interface Headers****Test Points****WiFi and App Status LEDs****Reset Button****Ground Test Loop****3V3 RTC Battery Supply****Ideal Diode****3V3 Isolator****3V3 Power Supply LED**