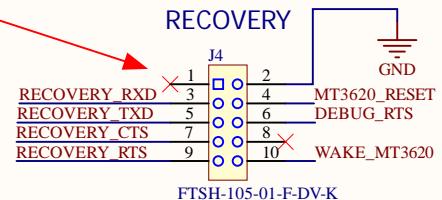


<https://github.com/Azure/azure-sphere-hardware-designs/tree/master/P-FTINT-2-0>
(Uses an FT4232H Mini Module)

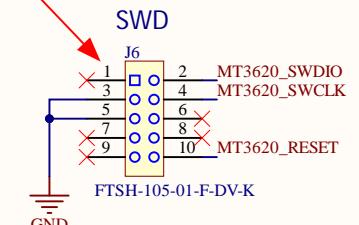
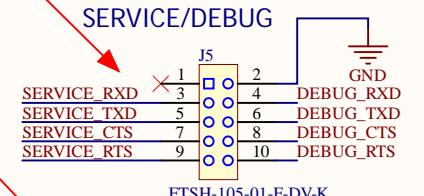
MT3620 programming interface

(Non Board components)

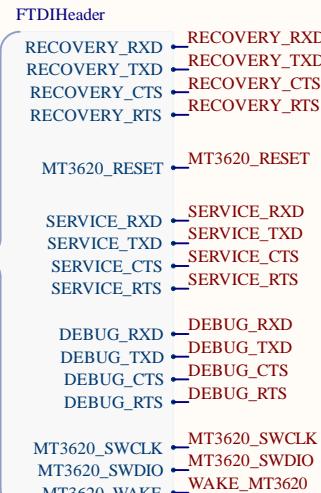
ents) | (On-Board components)



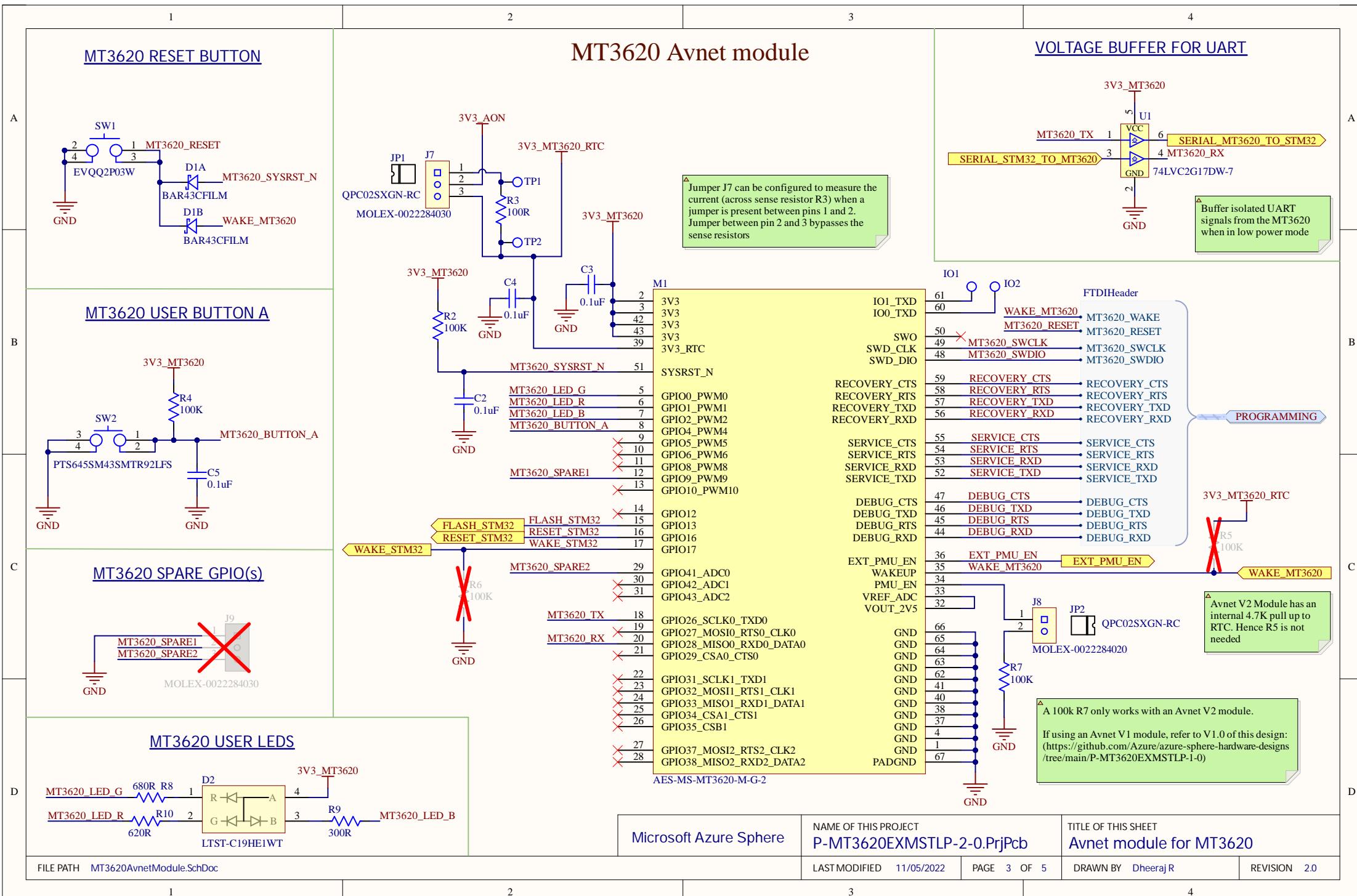
► DEBUG_RTS is used for bootstrapping as RECOVERY

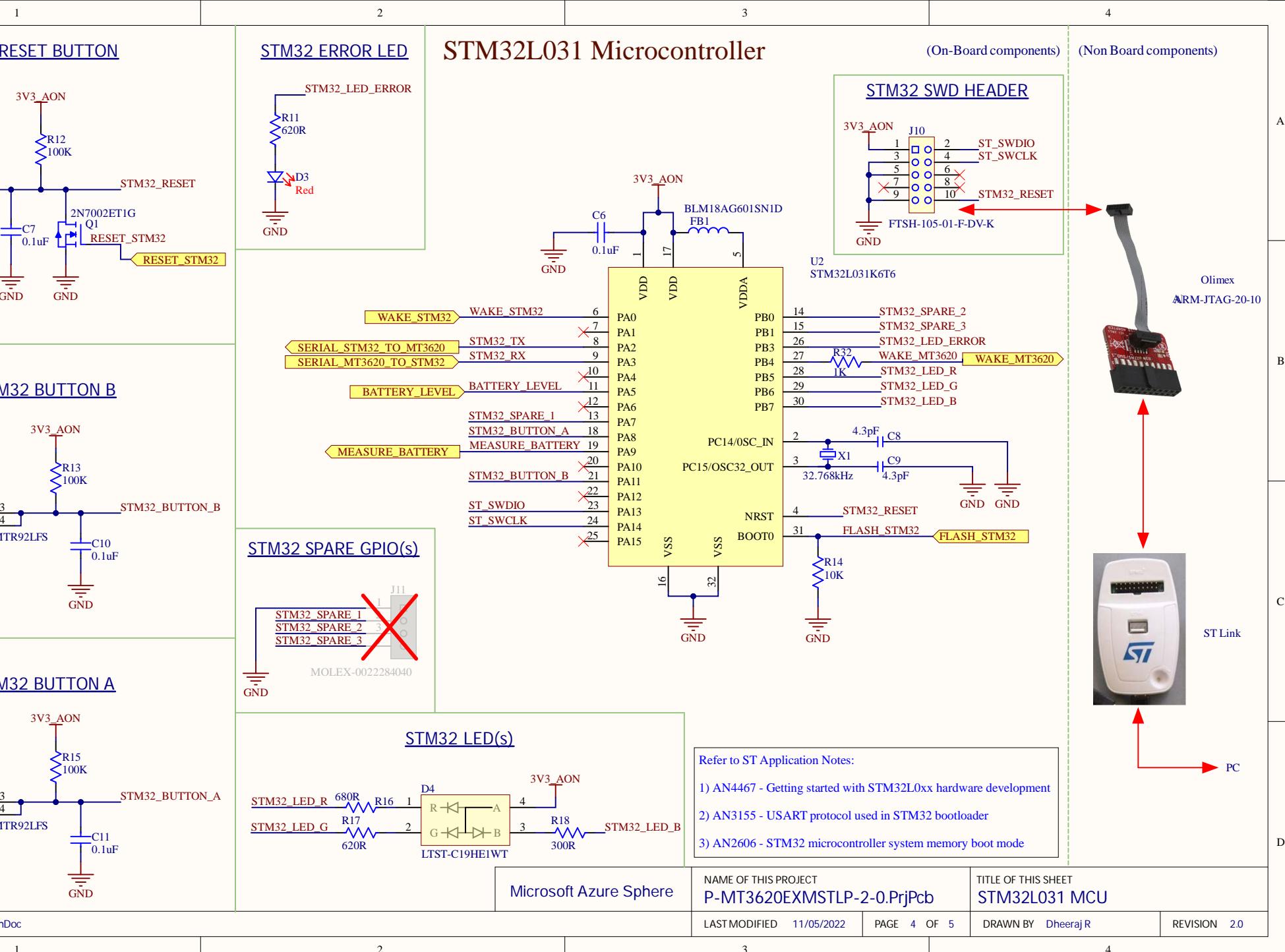


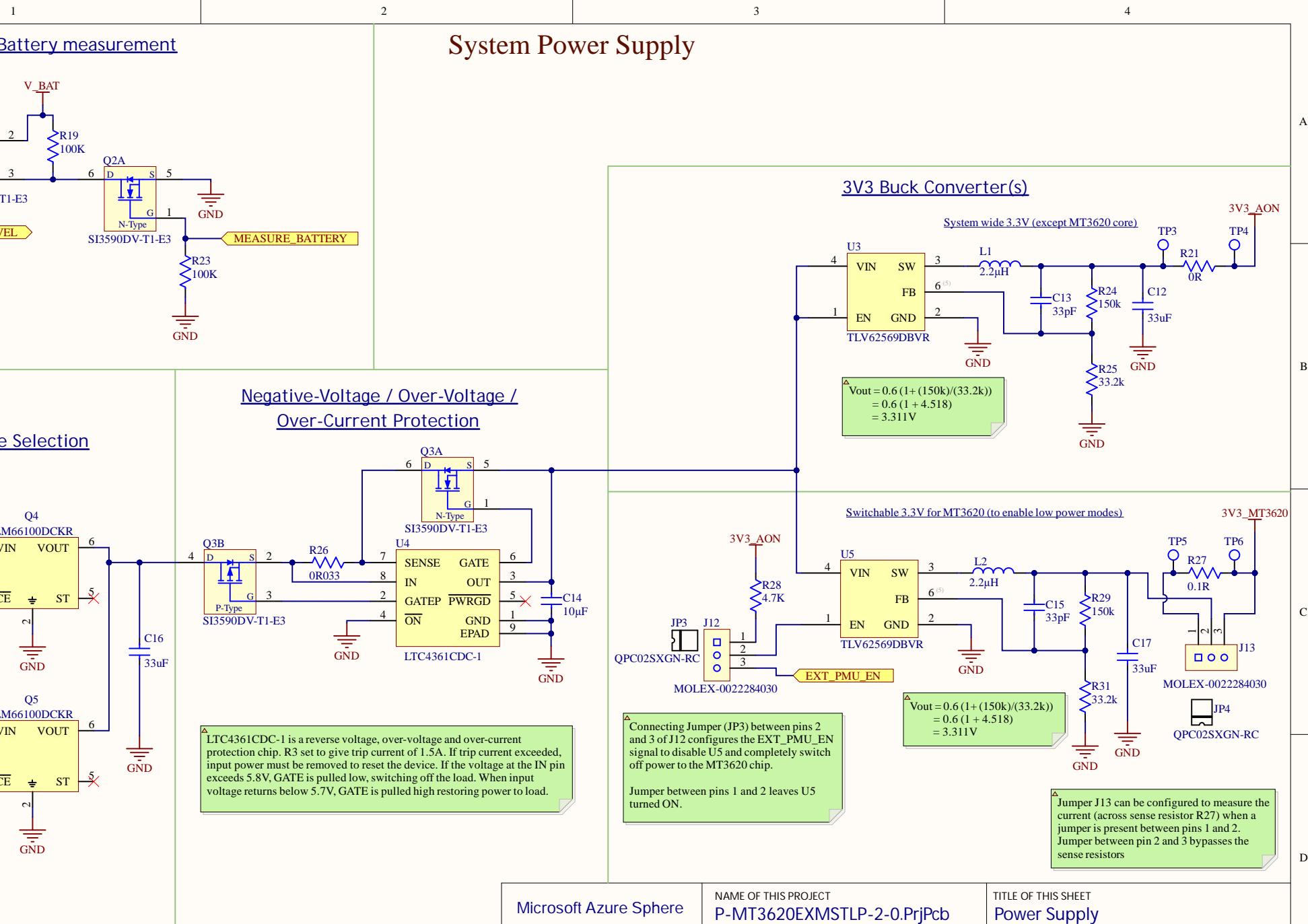
MT3620 programming signals

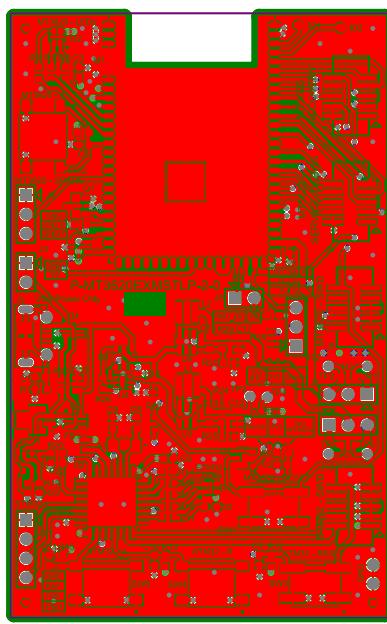


FILE PATH	FTDIProgrammer.SchDoc	LAST MODIFIED	11/05/2022	PAGE	2 OF 5	DRAWN BY	Dheeraj R	REVISION	2.0
MT3620_SWDIO	MT3620_SWDIO	MT3620_WAKE	MT3620_WAKE	Microsoft Azure Sphere	NAME OF THIS PROJECT P-MT3620EXMSTLP-2-0.PrjPcb	TITLE OF THIS SHEET FTDI Programming for MT3620			





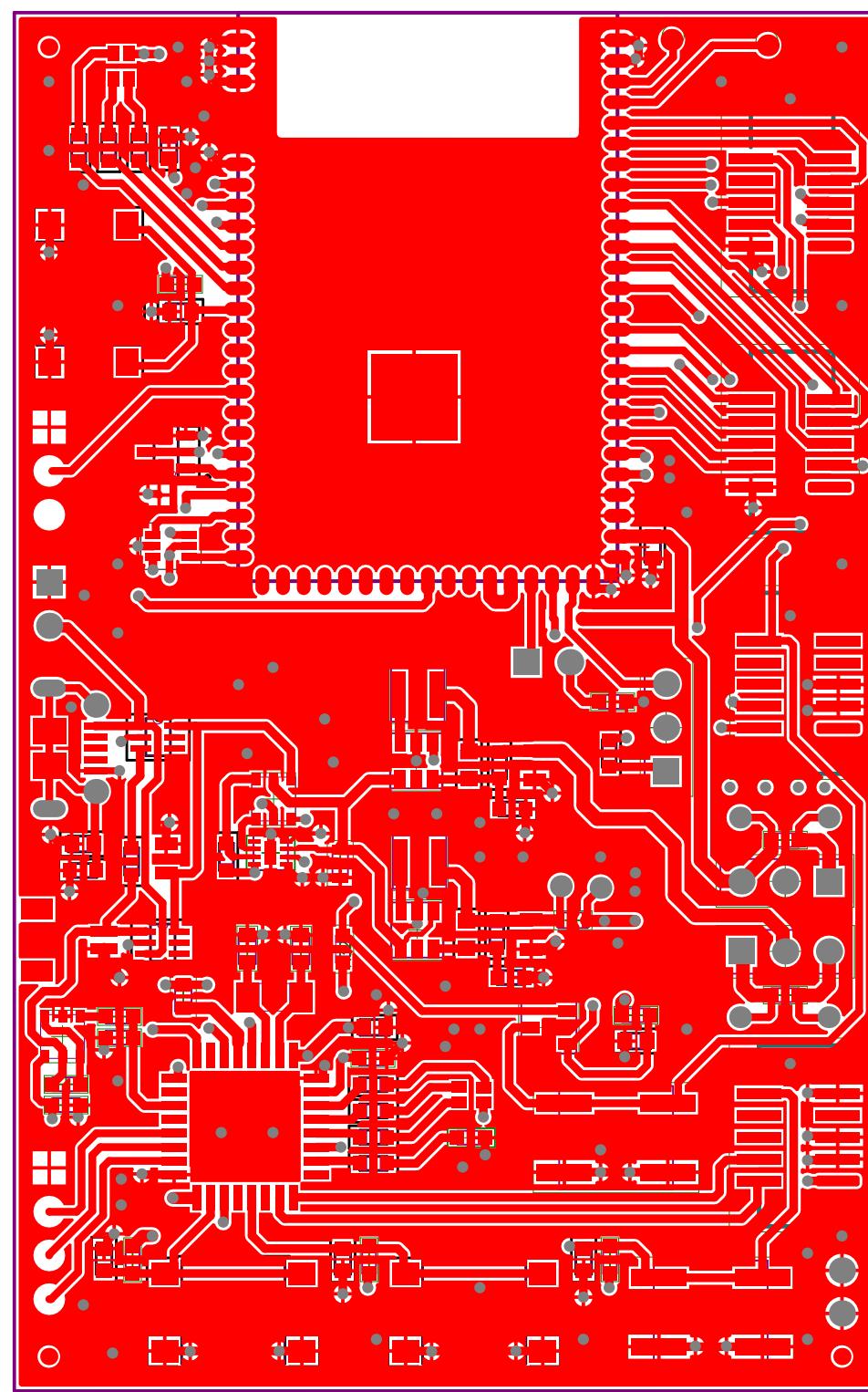




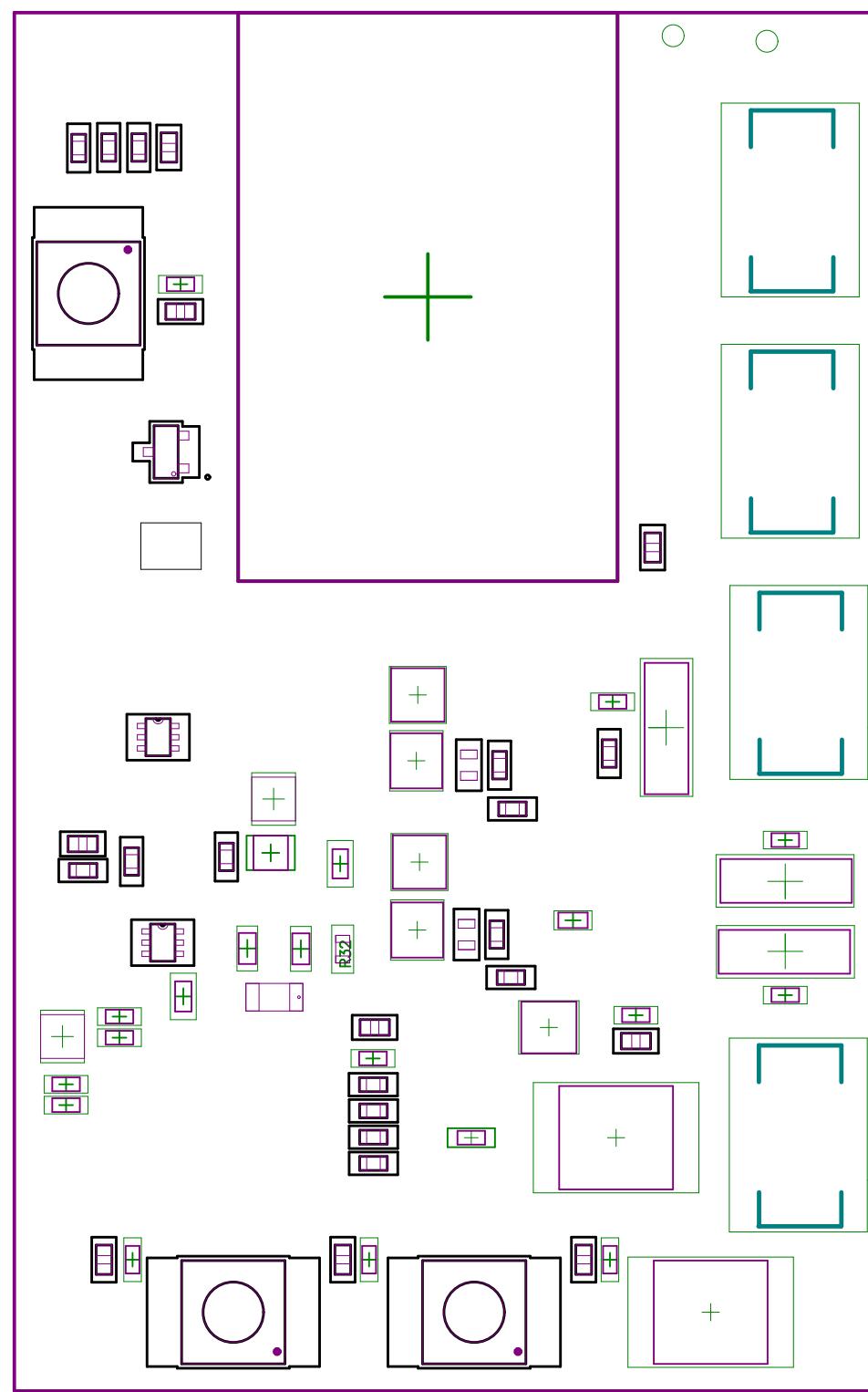
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Board Stack Report

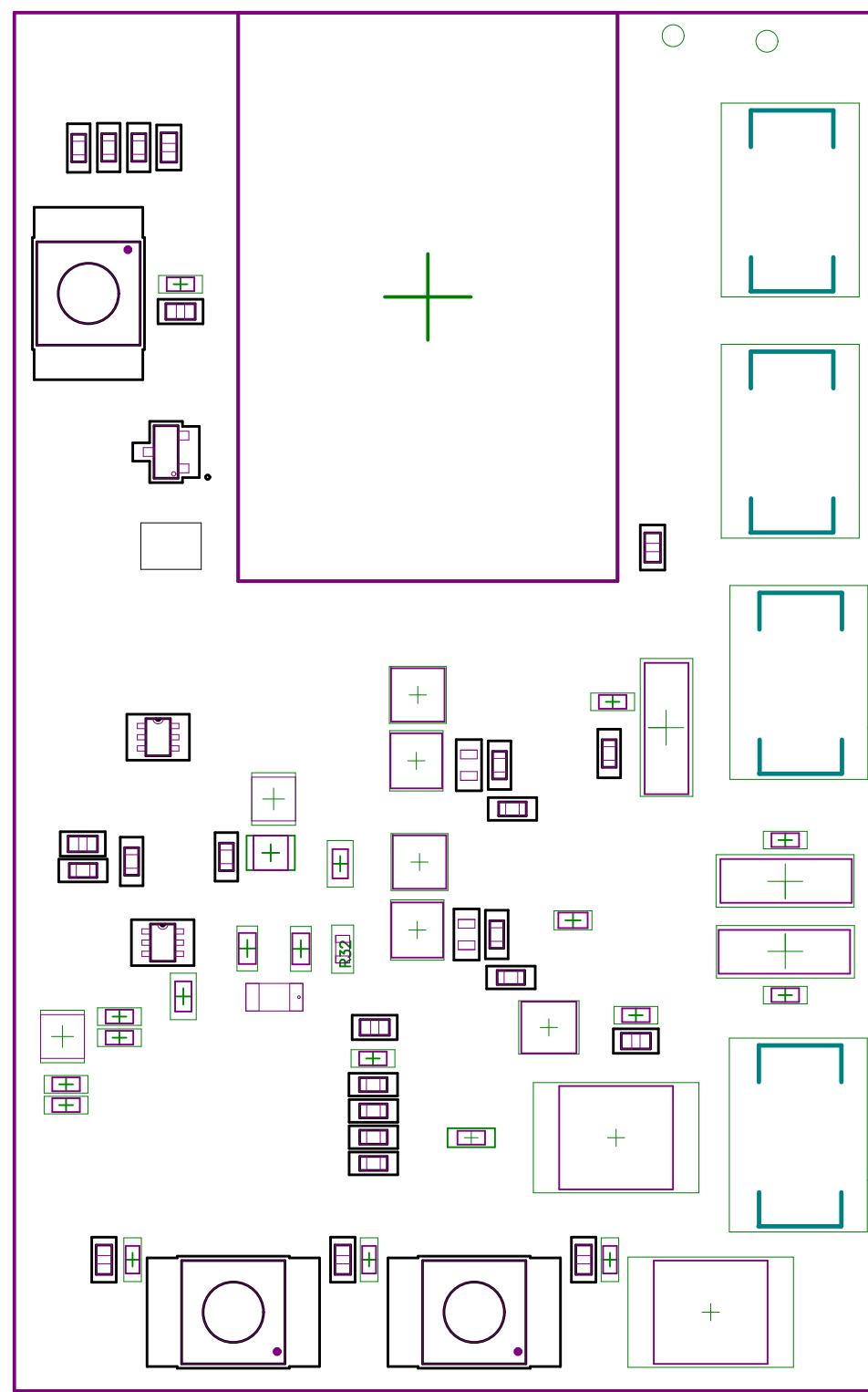
Stack Up		Layer Stack			
Layer	Board Layer Stack	Name	Material	Thickness	Constant
1		Top Paste			
2		Top Overlay			
3		Top Solder	Solder Resist	0.010mm	3.5
4		Top Layer	Copper	0.018mm	
5		Dielectric1	FR-4	0.360mm	4.8
6		Mid-Layer 1	Copper	0.035mm	
7		Dielectric2	FR-4	0.710mm	4.8
8		Mid-Layer 2	Copper	0.035mm	
9		Dielectric3	FR-4	0.360mm	4.8
10		Bottom Layer	Copper	0.018mm	
11		Bottom Solder	Solder Resist	0.010mm	3.5
12		Bottom Overlay			
13		Bottom Paste			
	Height : 1.556mm				



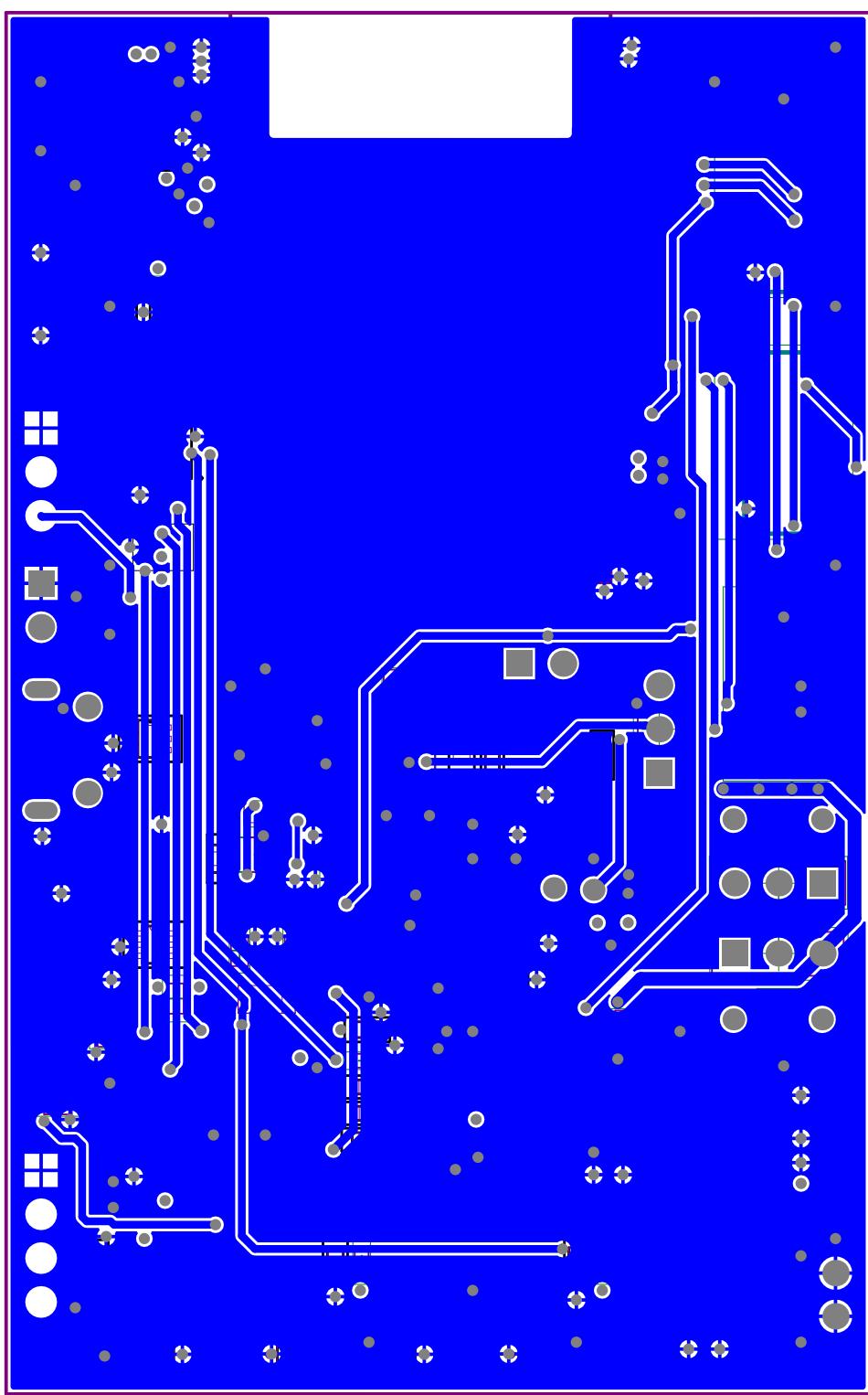
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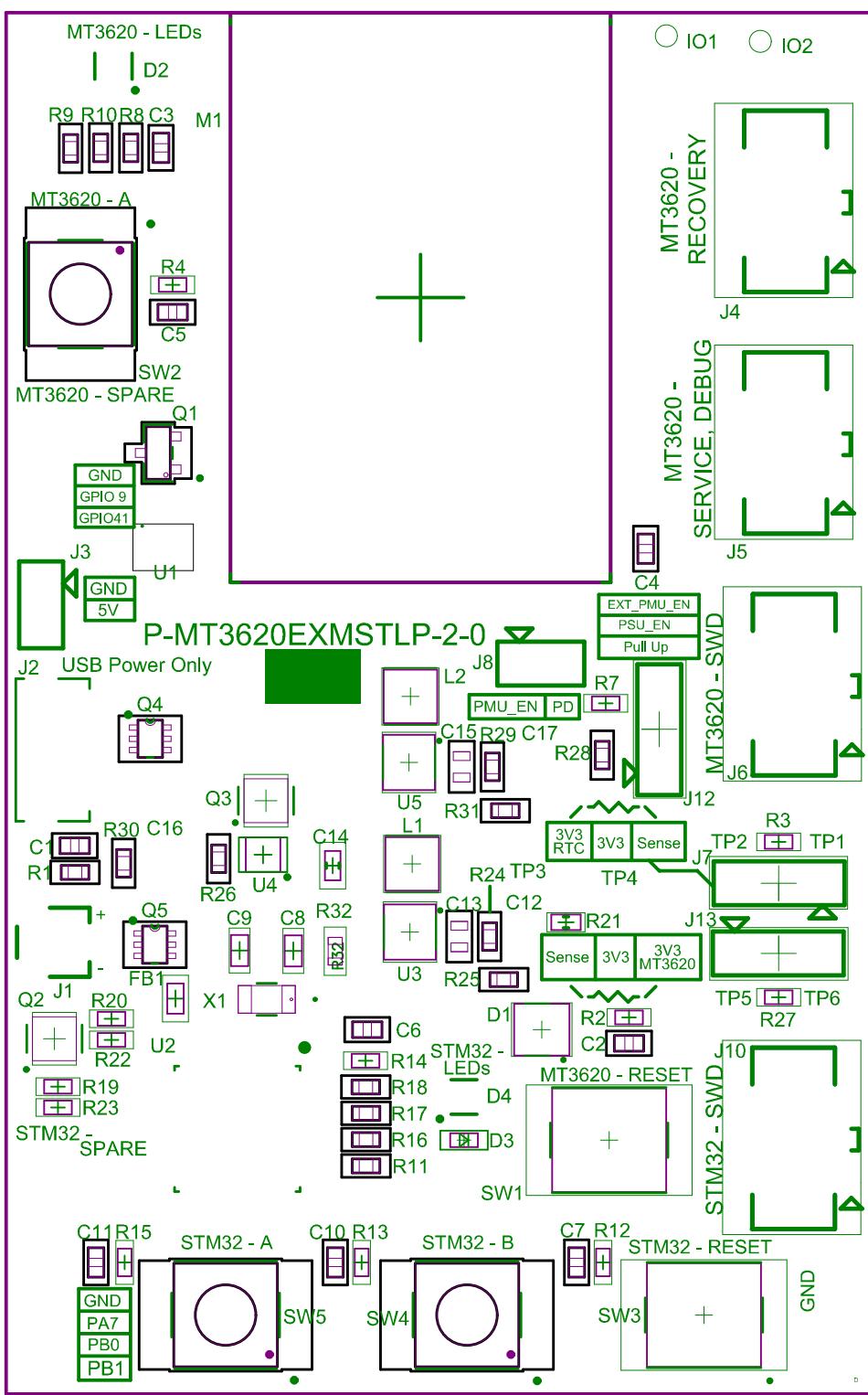
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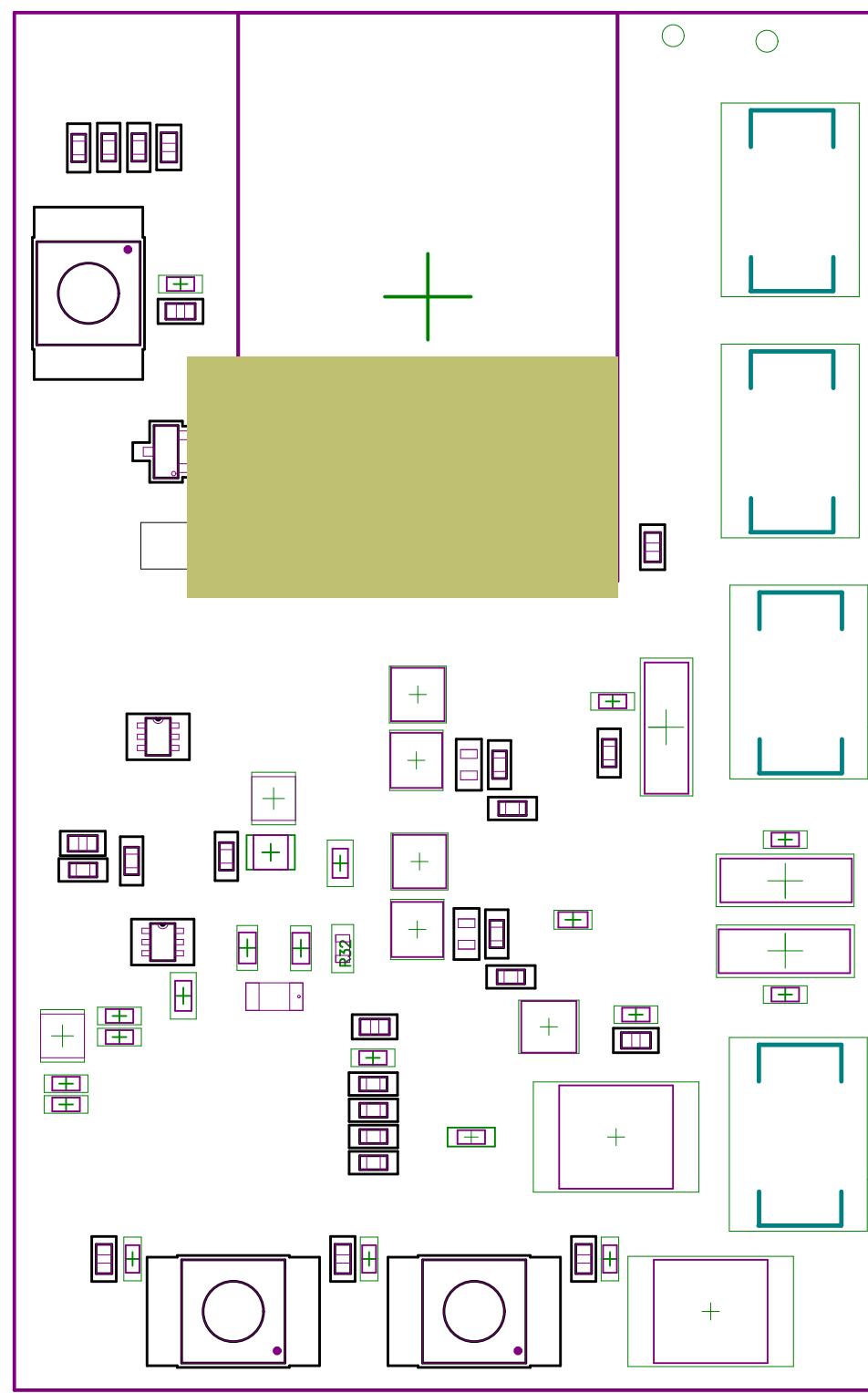


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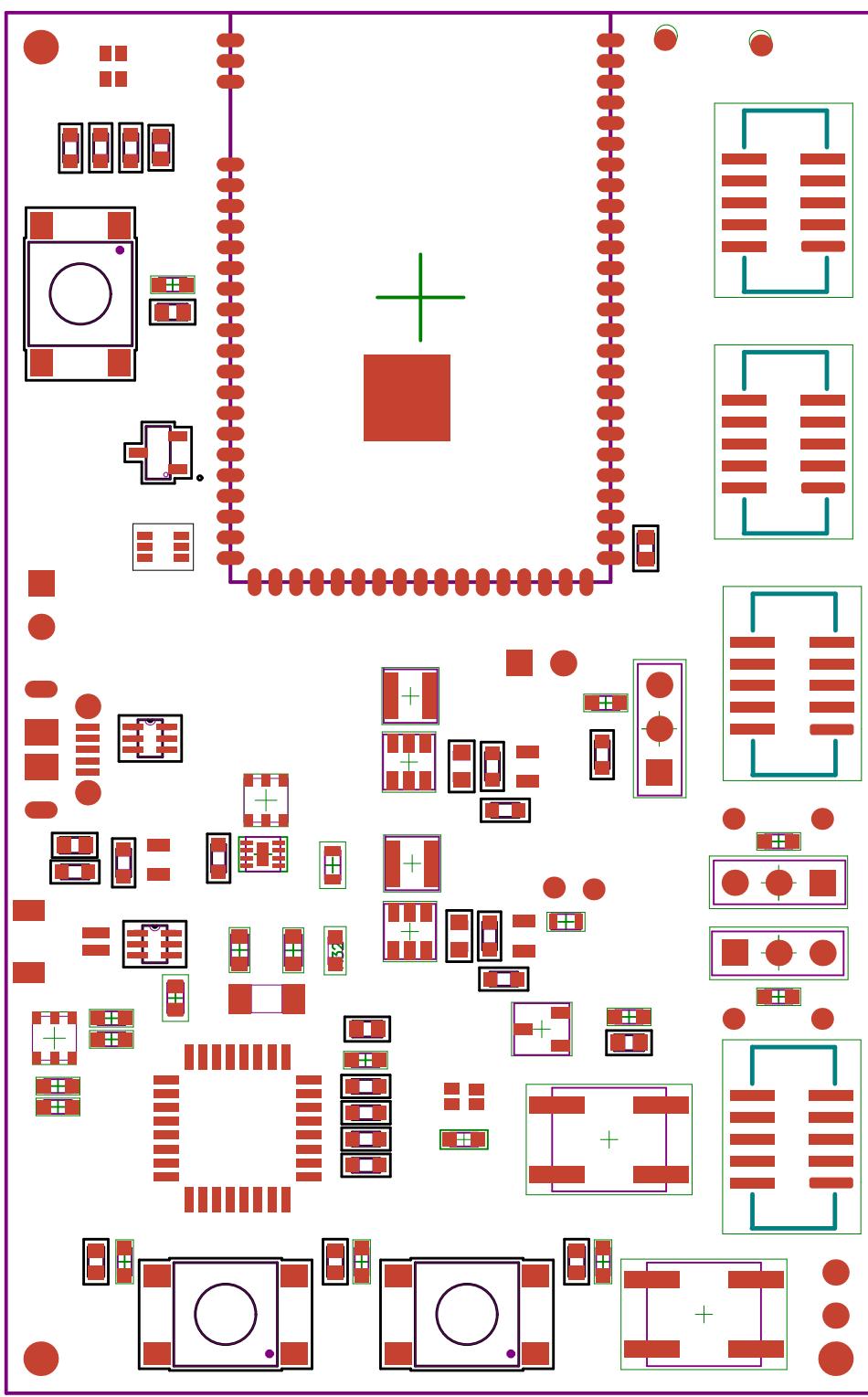


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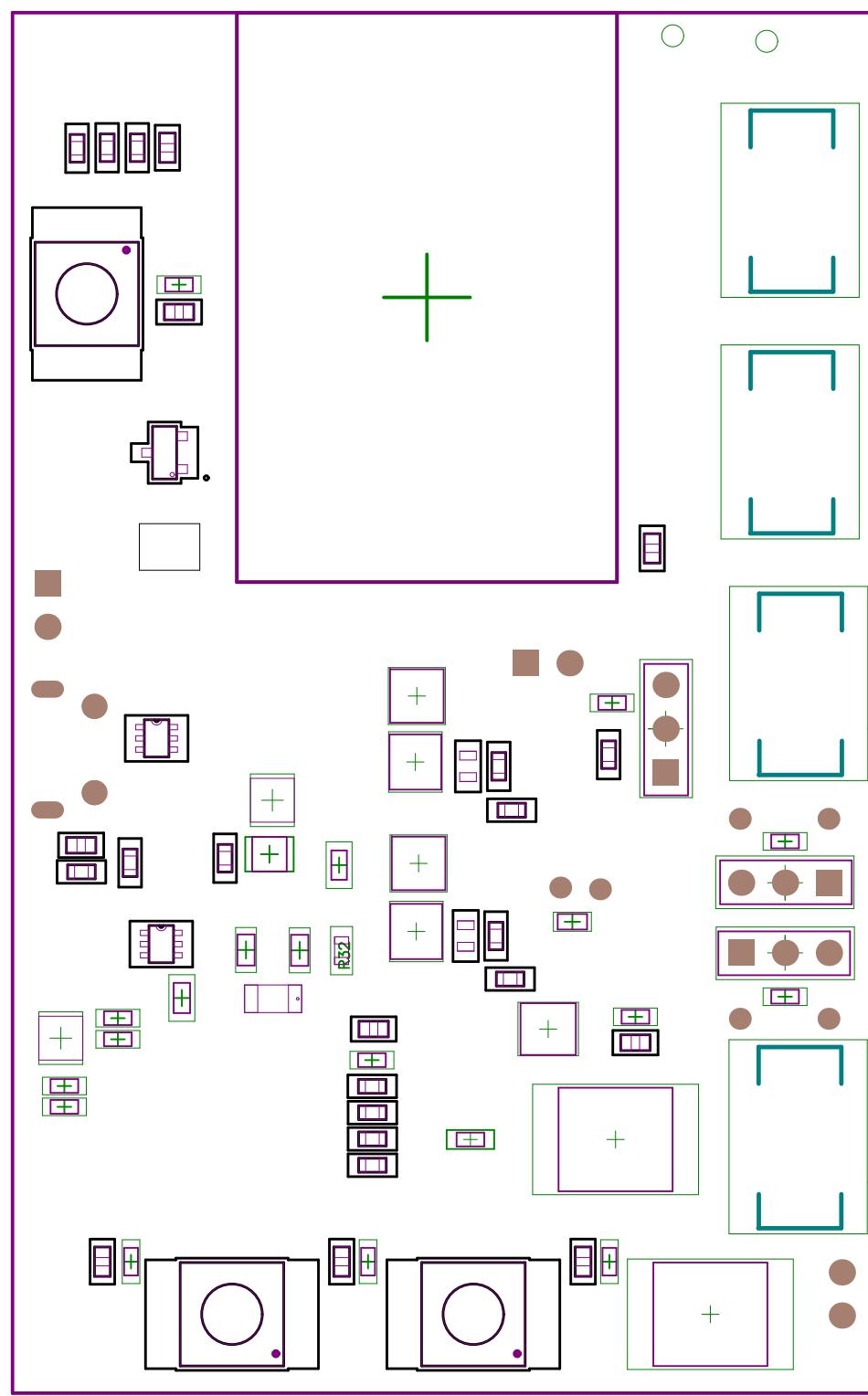




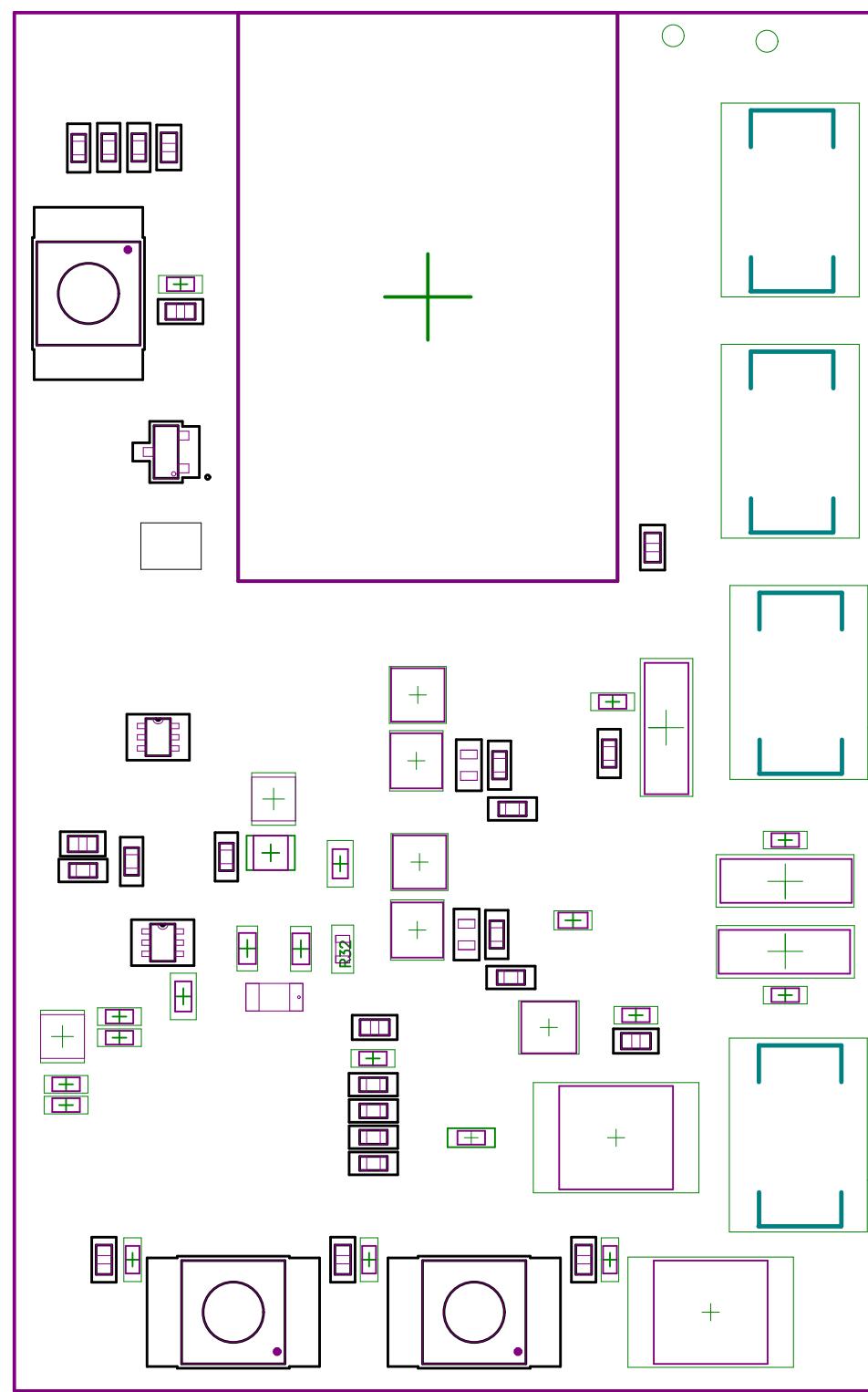
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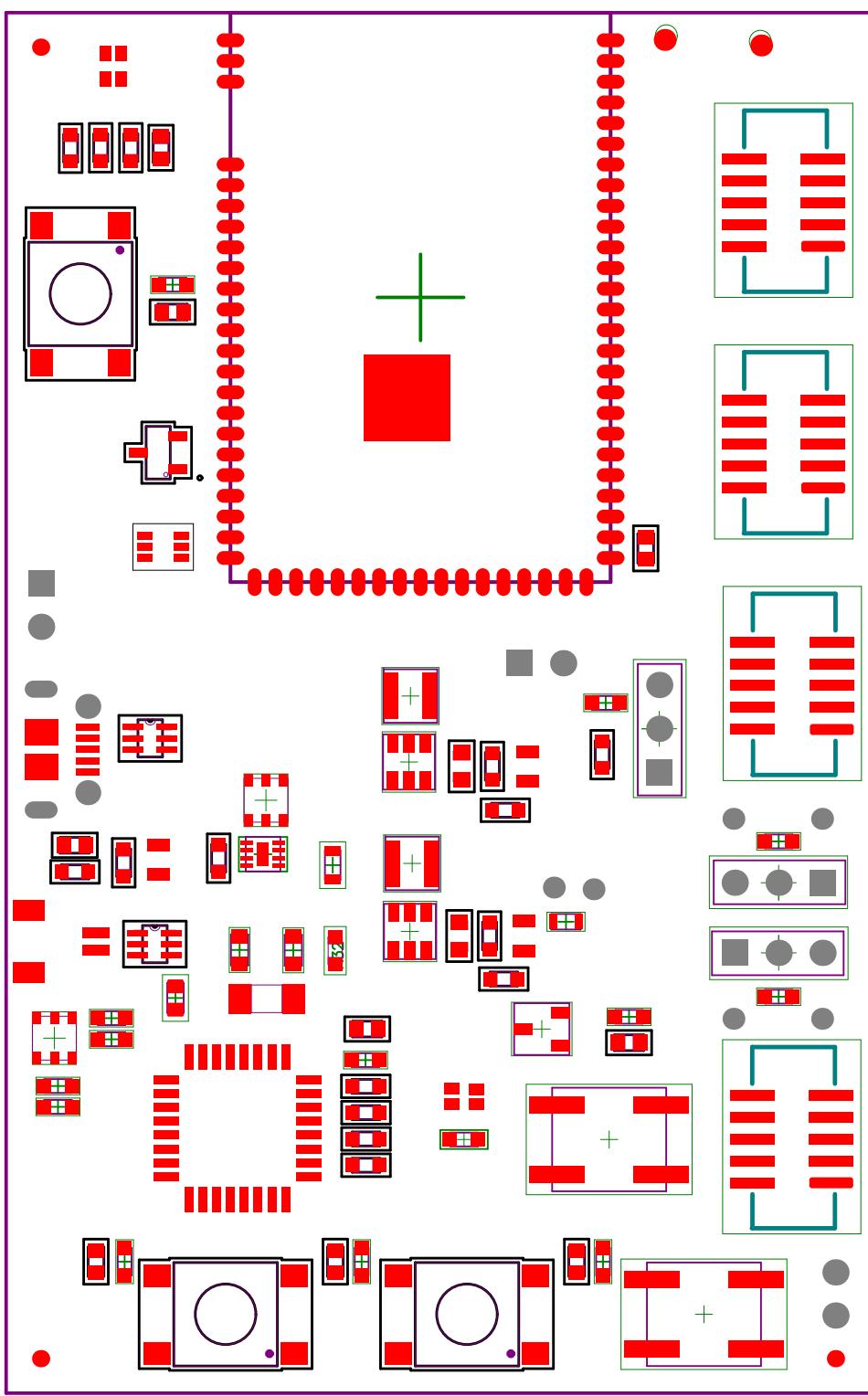


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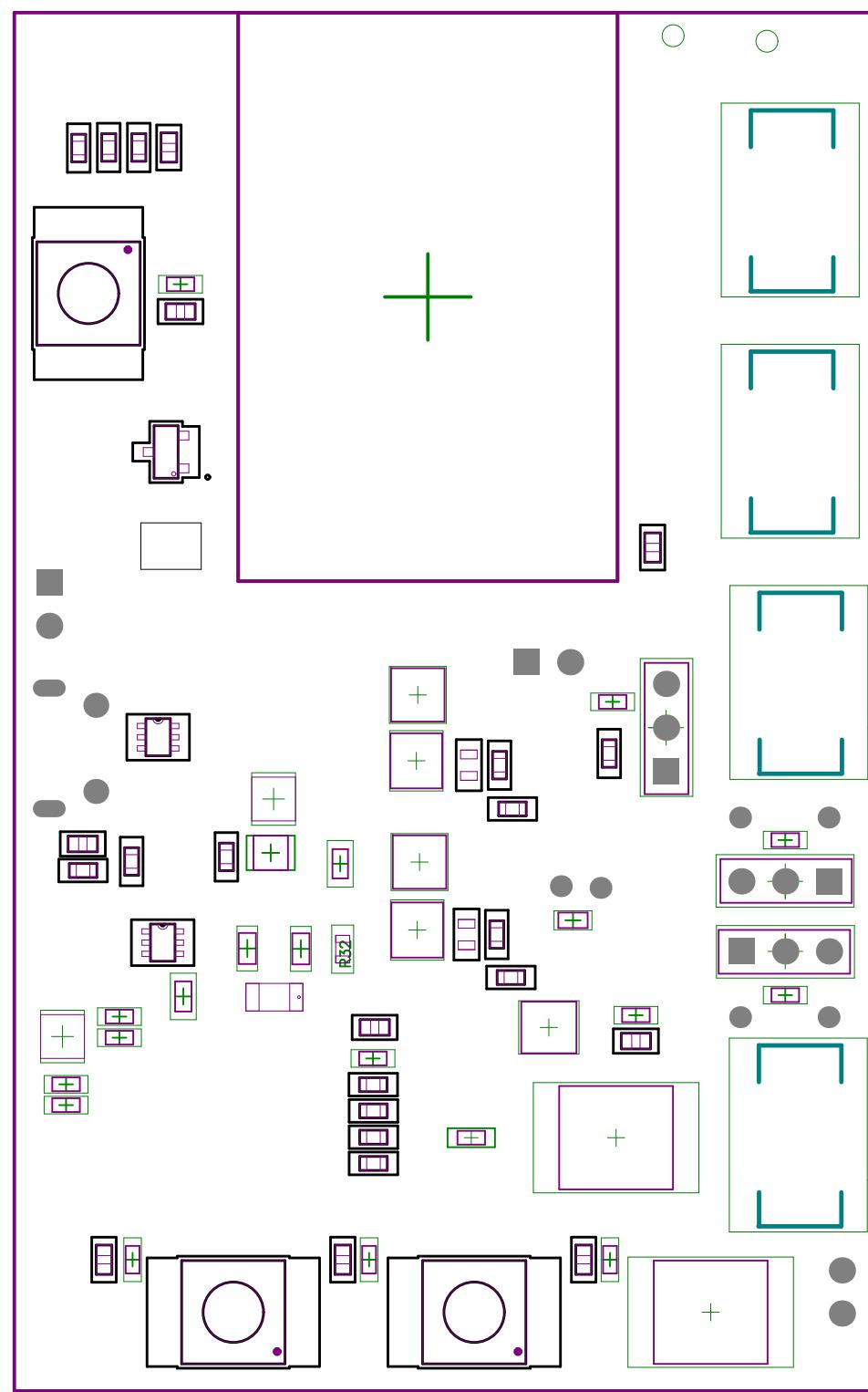


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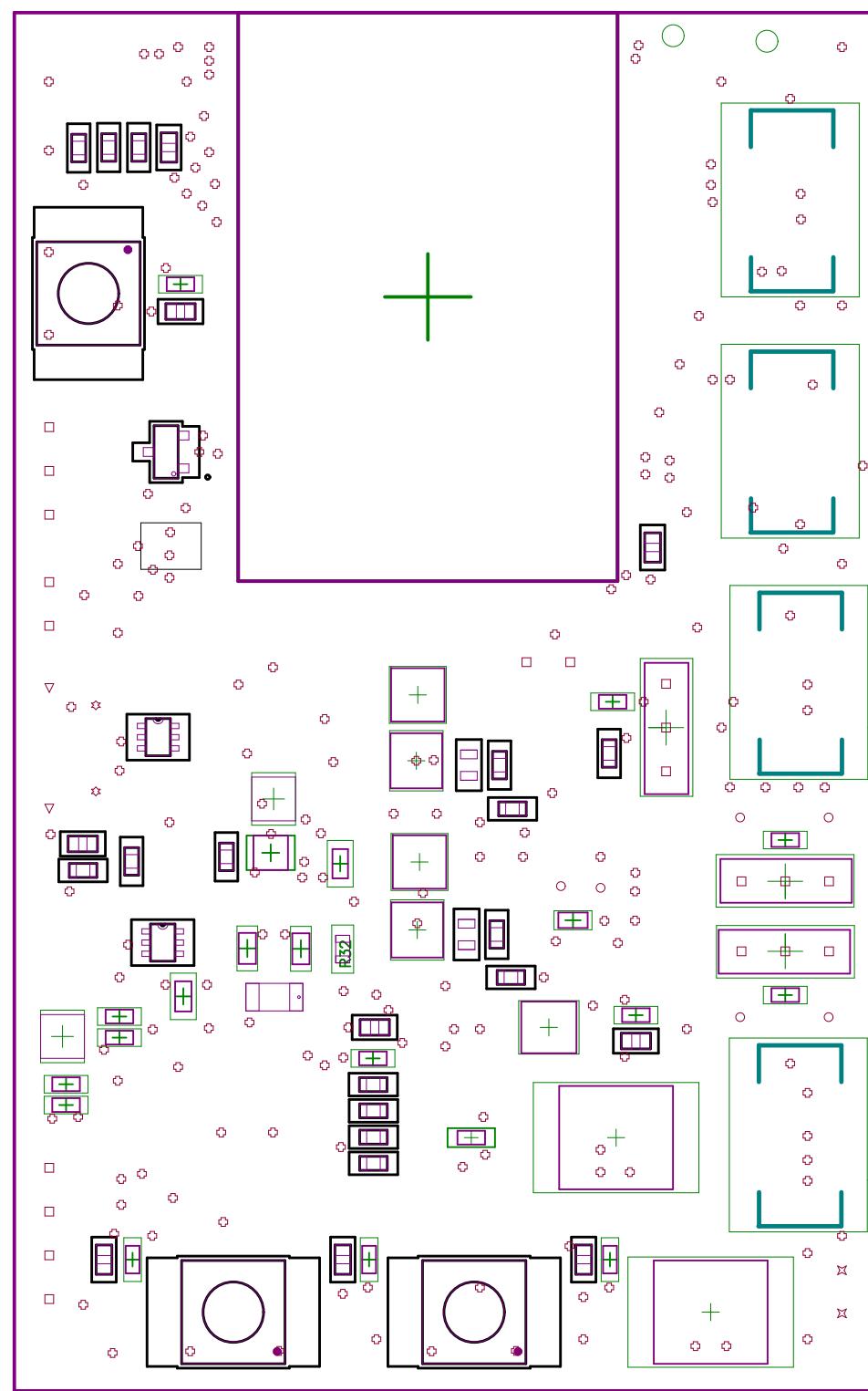




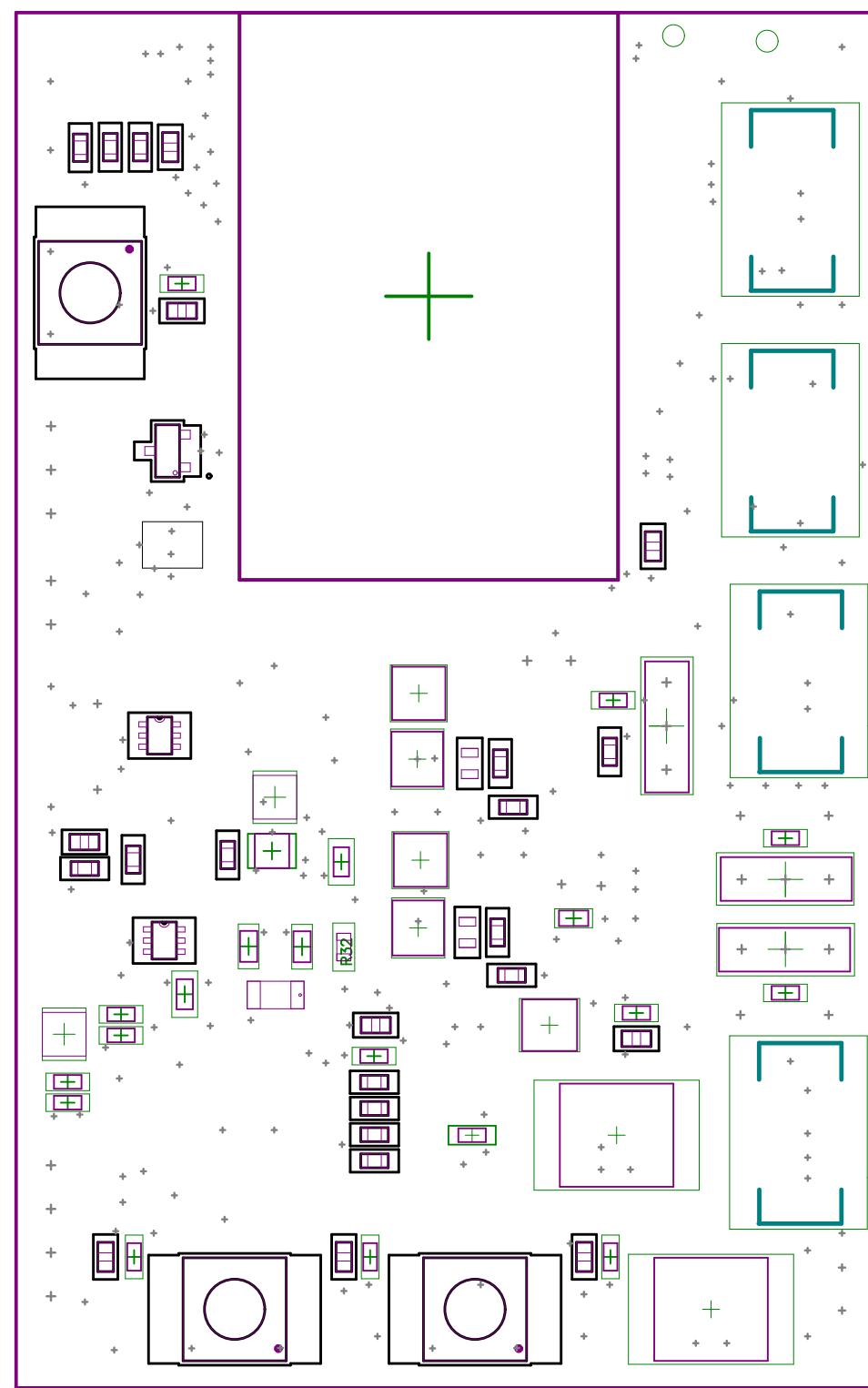
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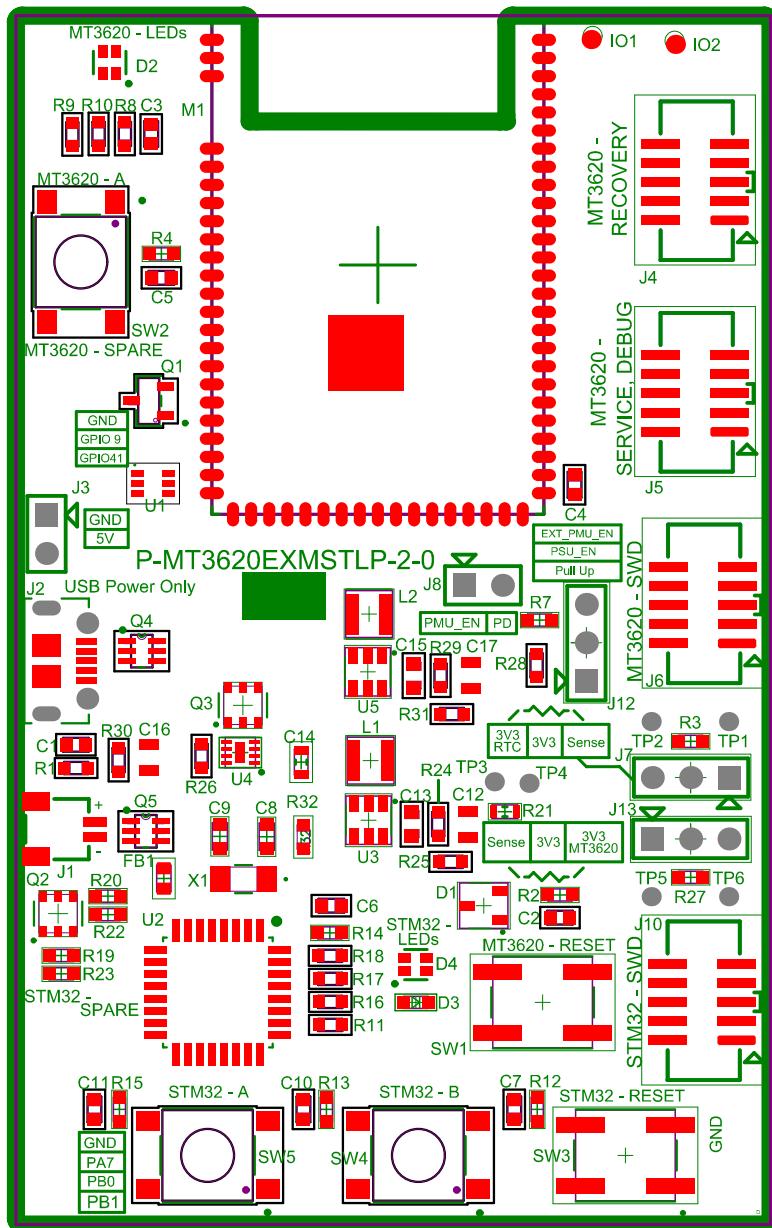
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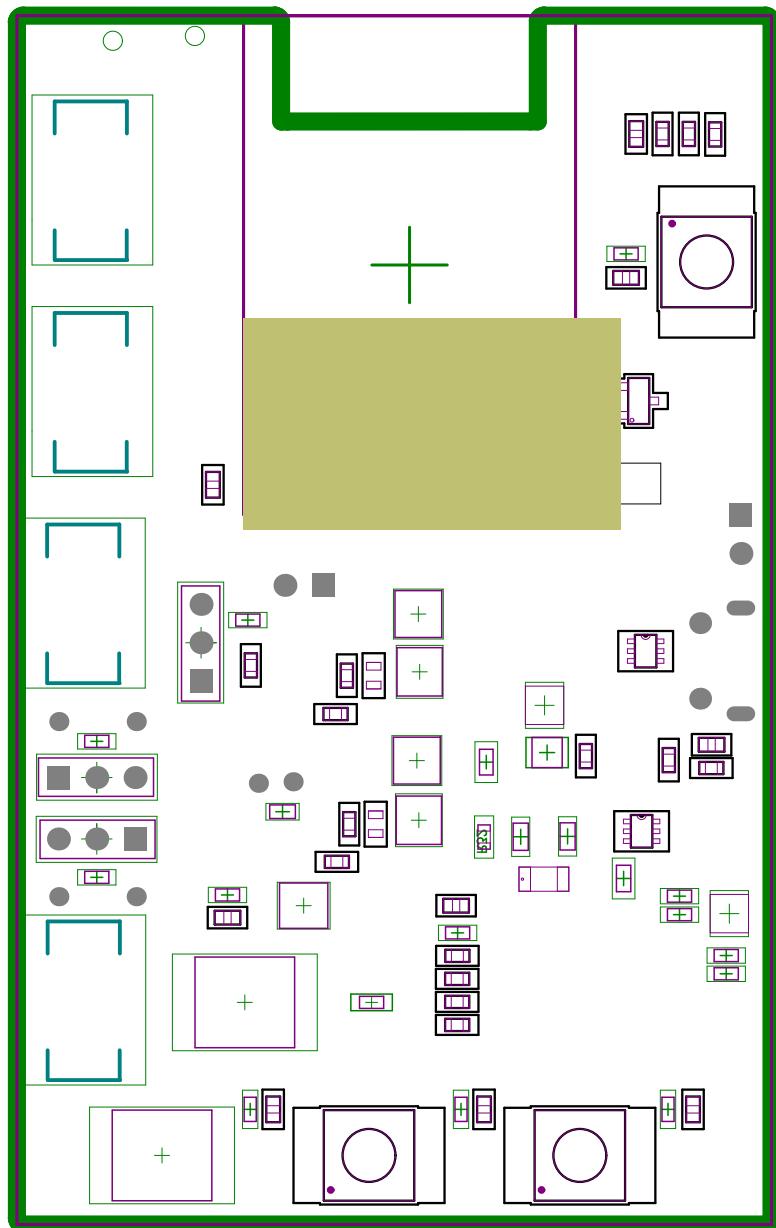
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P-MT3620EXMSLT-P-2-0.PCPLCC