

TOMÁŠ PILAŘ

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EMPLOYMENT HISTORY

Xilinx Inc.

Staff Software Engineer

July 2019 - Present

Cambridge, UK

- UEFI Technical Lead and Linux Driver developer
- Full ownership of the UEFI Project at Solarflare including design, development, test and CI
- Mentored junior member of the UEFI team

Solarflare Communications

Senior Software Engineer

June 2017 - July 2019

Cambridge, UK

- UEFI Driver and Linux Driver Developer
- Developed industry contacts facilitating higher quality of the UEFI products in-house.
- Tutored junior members of other teams to disseminate institutional knowledge
- Collaborated cross-team to design specifications, implement features, develop OEM products, and debug complex issues.

Solarflare Communications

Software Engineer

June 2014 - June 2017

Cambridge, UK

- Test Automation Engineer and Test Developer, Linux Driver Developer
- Designed and implemented test cases for multiple products using asynchronous Python framework based on twisted
- Pioneered in-house CI frameworks streamlining the development process
- Thoroughly benchmarked the linux kernel driver using a custom-built tool
- Created in-house test harness for product web-UI using the Selenium framework

TECHNICAL COMPETENCIES

Systems Programming (Linux & UEFI)

C (5 years)

Test Development and Automation

Python (7 years), Jenkins (2 years)

Version Control

Git and Mercurial (5 years)

Data Analysis

C++ and ROOT (2 years)

Database Design & Administration

T-SQL, mySQL, mariaDB

OTHER SKILLS

Languages

Slovak & Czech (Native), English (Bilingual), French (Intermediate)

Management Software

JIRA, Bugzilla, Reviewboard, Slack

Methodology

Agile development and Continuous Integration

Public Speaking

Experience chairing meetings (up to ~ 40 people)

POSITIONS OF RESPONSIBILITY

GAUSS Monte Carlo Simulation Group

Decay Files repository manager

October 2011 - June 2014

LHCb Collaboration, CERN

EvtGen Development Group

Development and technical support

October 2010 - June 2014

University of Warwick, UK

LHCb UK Student Group

Convener

June 2012 - December 2012

LHCb Collaboration, CERN

University of Warwick Computing Society

Co-President

February 2007 - January 2009

University of Warwick, UK

Warwick Conferences

Lead Conference Assistant

June 2008 - September 2009

Warwick Conferences, Coventry

EDUCATION

University of Warwick, UK

October 2010 - June 2014

PhD in Particle Physics

Thesis on analysing data from LHCb to extract Standard Model parameters

Transferrable Doctoral Skills Course

Long Term Attachment in CERN, Geneva (14 months)

University of Warwick, UK

October 2006 - June 2010

MMathPhys (2.1), Thesis in Particle Physics

BASG Milana Hodžu, Sučany, Slovak Republic

October 2001 - June 2006

A-level equivalent (AAAABB)

INTERESTS AND HOBBIES

Professional

Low-level programming (system software or firmware), solving complex problems

Designing robust systems that last, leaving code in better shape, bringing hardware to life

Imparting institutional knowledge

Personal

Board games and computer games, field improv

Literature, theatre, musicals, travelling, swing dancing

COMMUNICATIONS

UEFI Plugfest

Autumn 2016 - *Current*

Regular attendee on behalf of Solarflare Communications

Charm mixing in $D^0 \rightarrow K_S \pi \pi$ at LHCb

June 2014

<https://cds.cern.ch/record/2008671?ln=en>

University of Warwick

Model-independent measurement of mixing parameters in $D^0 \rightarrow K_S \pi \pi$ decays

April 2016

<http://link.springer.com/article/10.1007%2FJHEP04%282016%29033>

Journal of High Energy Physics

Charm mixing and CP violation at LHCb

May 2013

Large Hadron Collider Physics Conference

Barcelona, Spain

Measuring charm mixing using a model-independent technique in $D^0 \rightarrow K_S hh$

April 2013

Institute of Physics HEP Conference

Liverpool, UK

REFERENCES

Dinan Gunawardena

2017 - *Current*

Director, Xilinx Inc.

+44 7808 926449, dinan@ieee.com

Robert Stonehouse

June 2014 - June 2019

Linux Driver Team Lead, Solarflare Communications

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