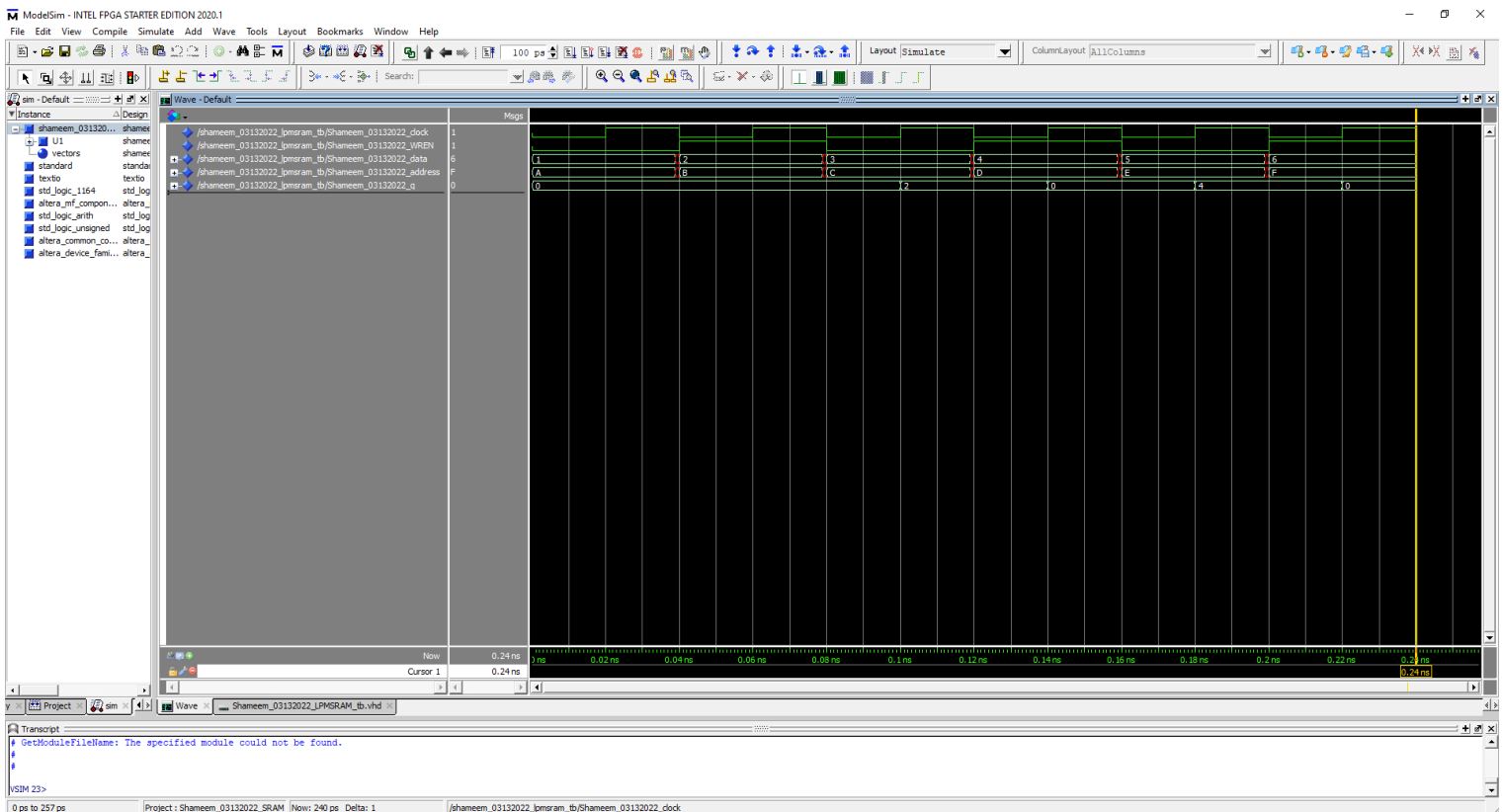# CS 342000 | CS343000
Instructor: Professor Izidor Gertner
Spring 2022
Azwad Shameem, 3/14/2022

SRAM Simulated in ModelSim. The data, address and q inputs are shown in hexadecimal. The clock starts at 0 then rises every 20 ns and goes down every other 20 ns. The wren input starts at 0 and goes up every 40 ns and down every other 40 ns