

Lesson 5

Standard Peripheral Library

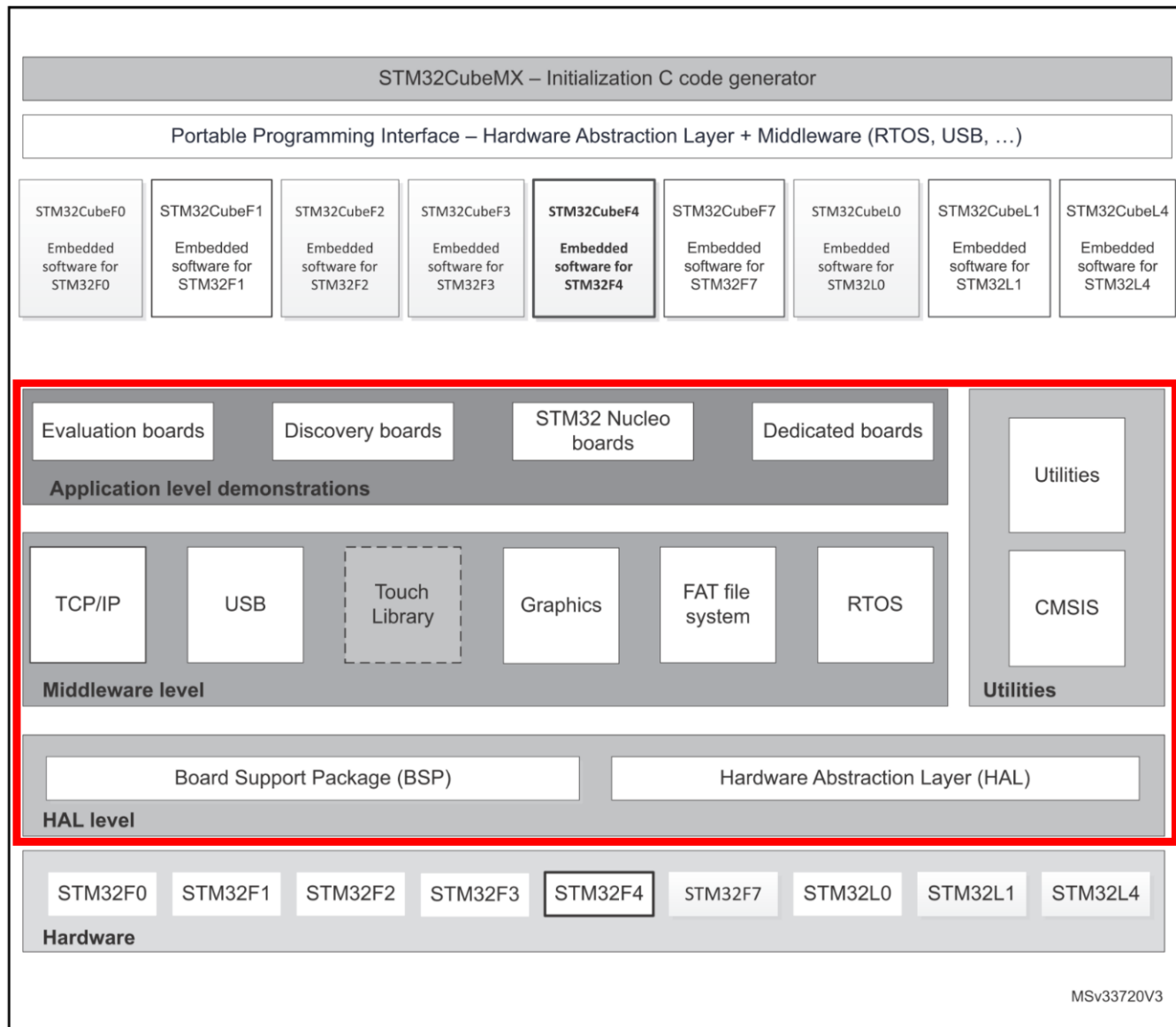
Lecturer: Harvard Tseng

TIM2->CR1 |= 0x1<<4;

What does this mean?

- R/W register coding style is hard to read for human.
- We prefer using functions rather than R/W register.

Separate hardware & software.



What does StdPeriph Library contain?

- Hundreds of examples
- Middleware level driver
- Peripheral driver
- CMSIS

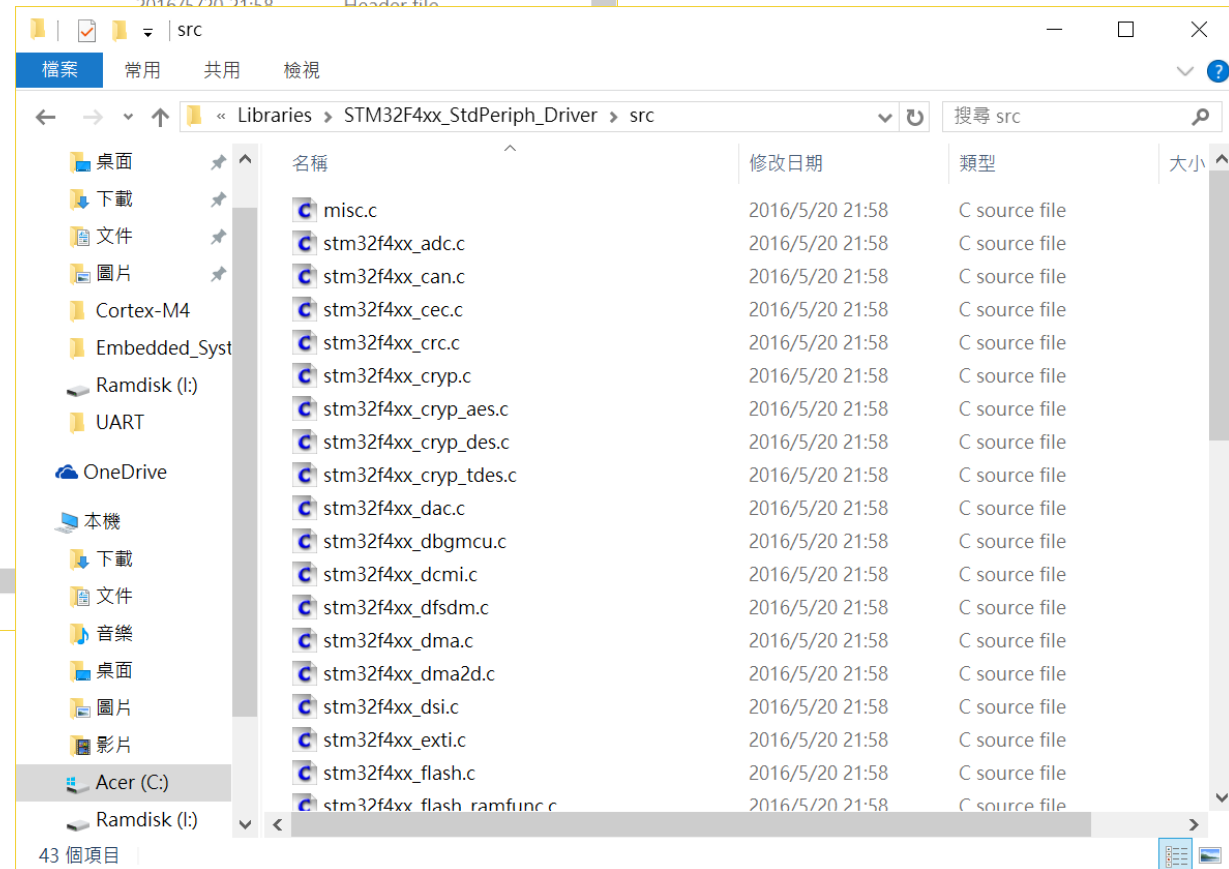
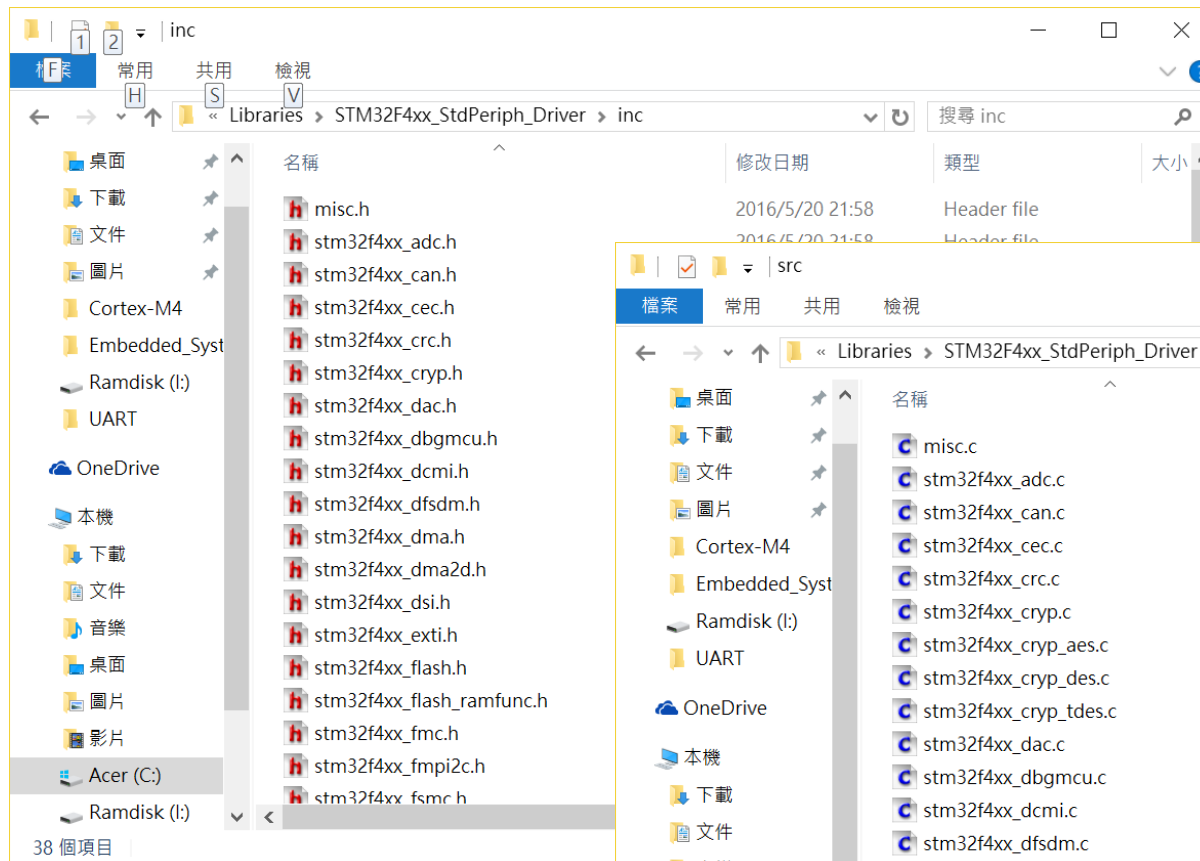
Middleware level

- TCP/IP
- USB
- Graphics
- ...

HAL level

- HAL = Hardware Abstraction Layer
- Contains lots of peripheral driver.

Files
















StdPeriph Library Setup

Step1. Download StdPeriph Library

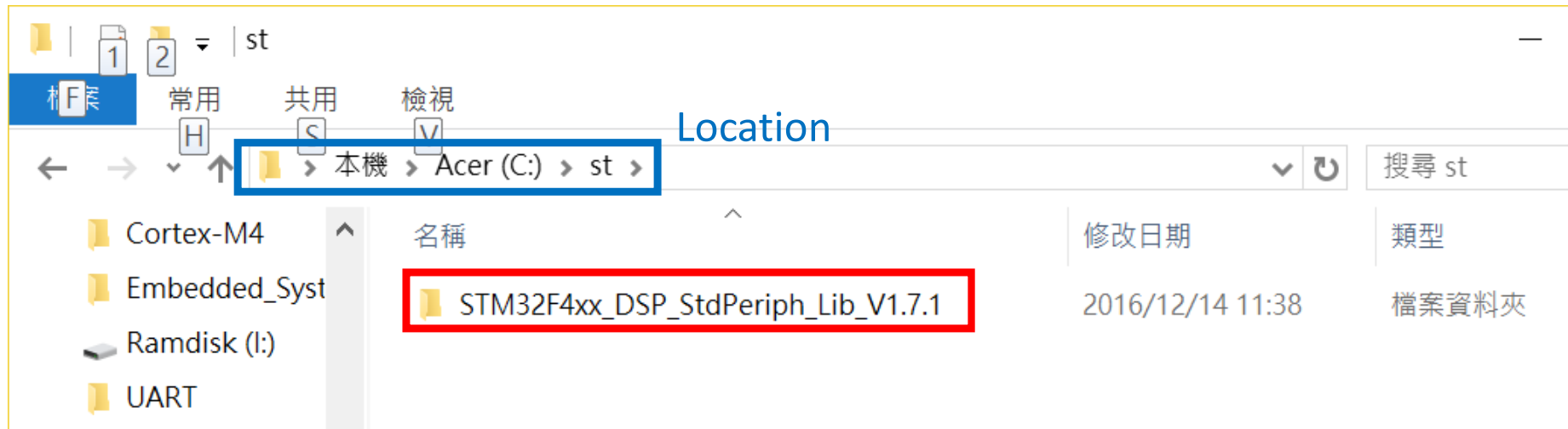
- Download from [ST](#).

STM32 Standard Peripheral Libraries

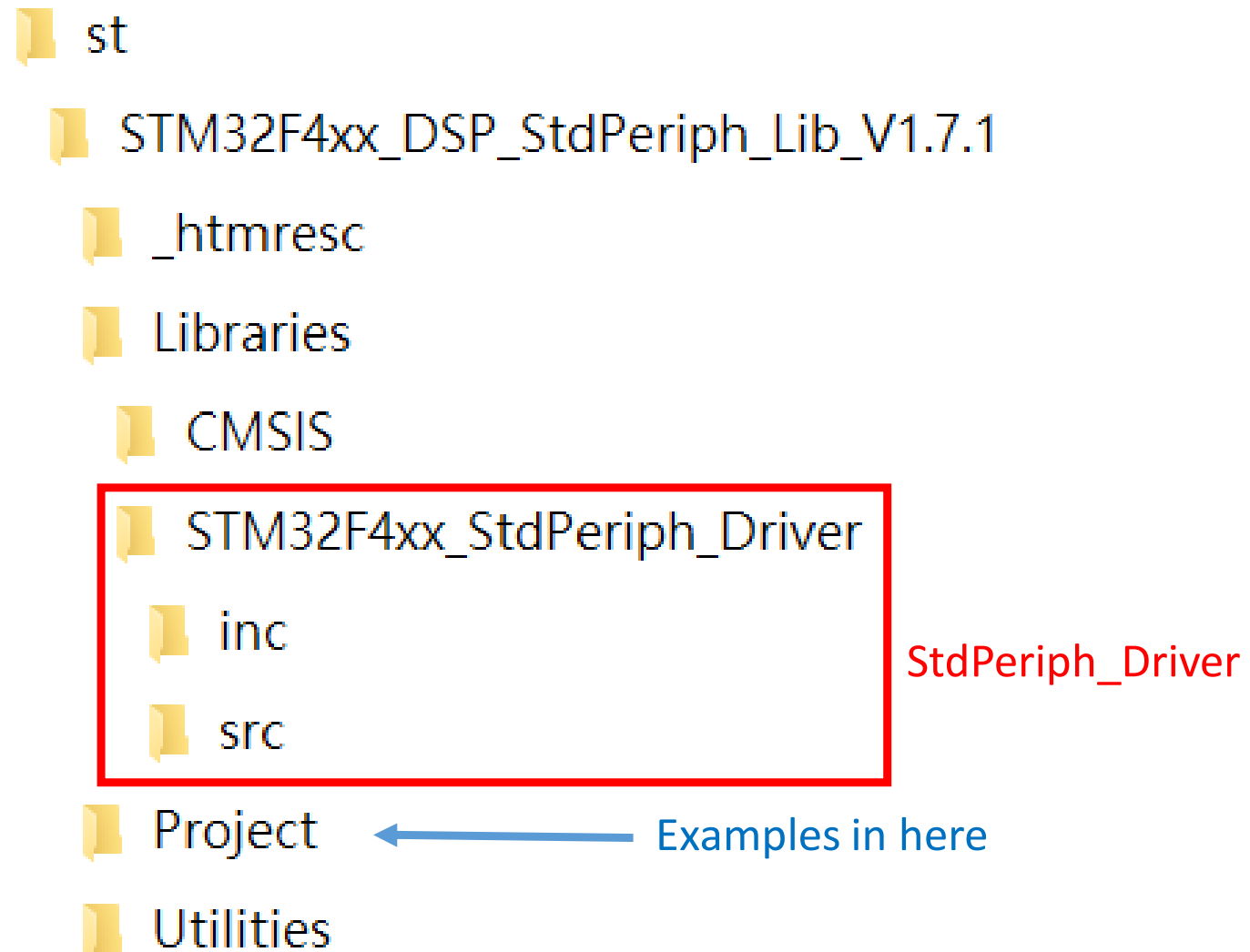
 Last Viewed  Share  Bookmark  Download  Full screen						
STM32 Standard Peripheral Libraries						
Total Parts: (9) for STM32 Standard Peripheral Libraries Matching Parts : (9)  Customize Table  Reset All						
Part Number 	Marketing Status 	Supplier 	Supported Devices 	Software Type 	Software Version 	
STSW-STM32054 STM32F10x standard peripheral library	Active	ST	STM32, STM32F1	Firmware	3.5.0	
STSW-STM32062 STM32F2xx standard peripherals library (UM1061)	Active	ST	STM32, STM32F2	Firmware	1.1.0	
STSW-STM32065 STM32F4 DSP and standard peripherals library	Active	ST	STM32, STM32F4	Firmware	1.8.0	
STSW-STM32115 STM32F37x/38x DSP and standard peripherals library, including 73 ex...	Active	ST	STM32F, STM32F3	Firmware	1.0.0	

Step2. Extract

- Extract to folder C:\st\

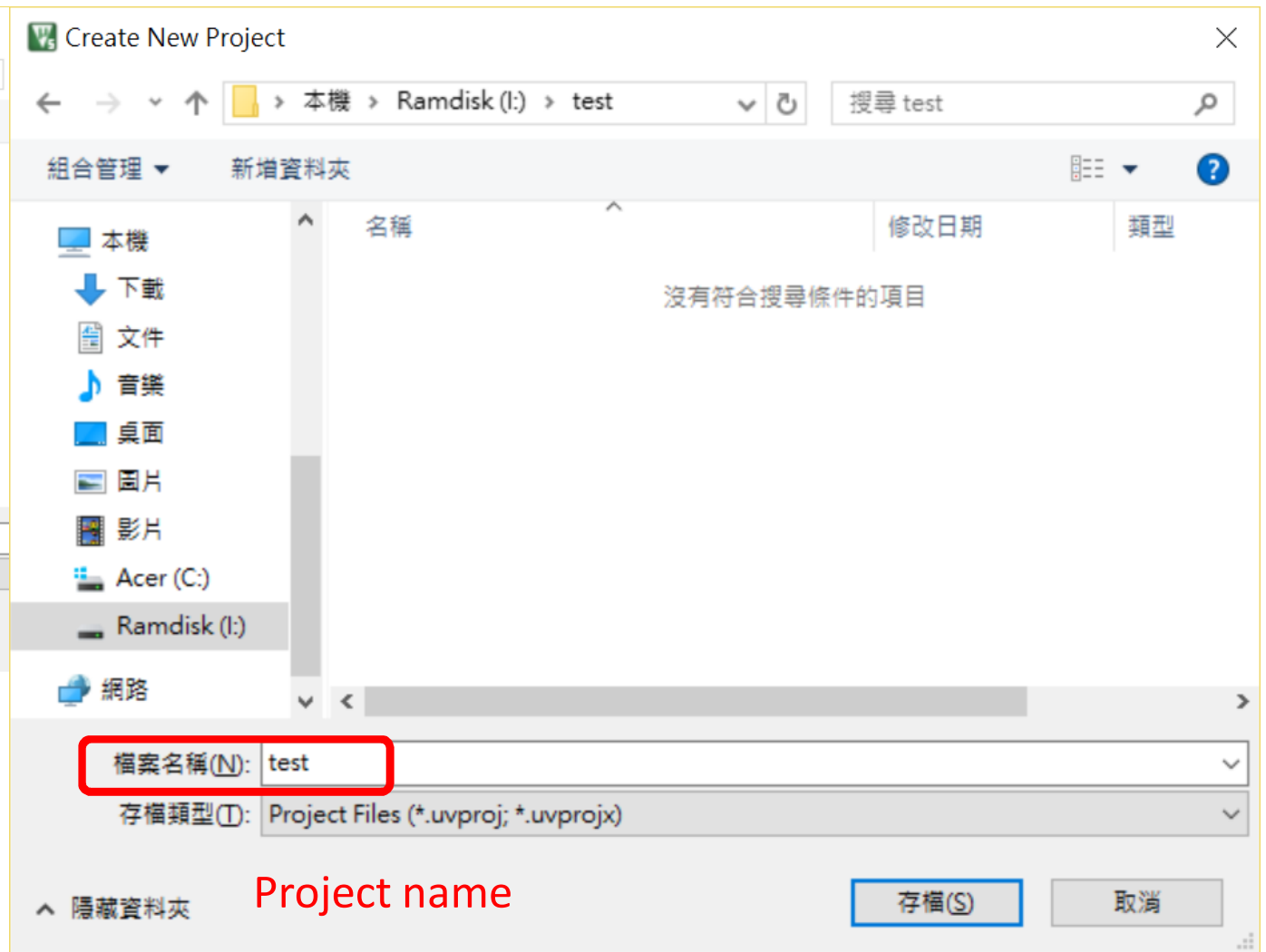
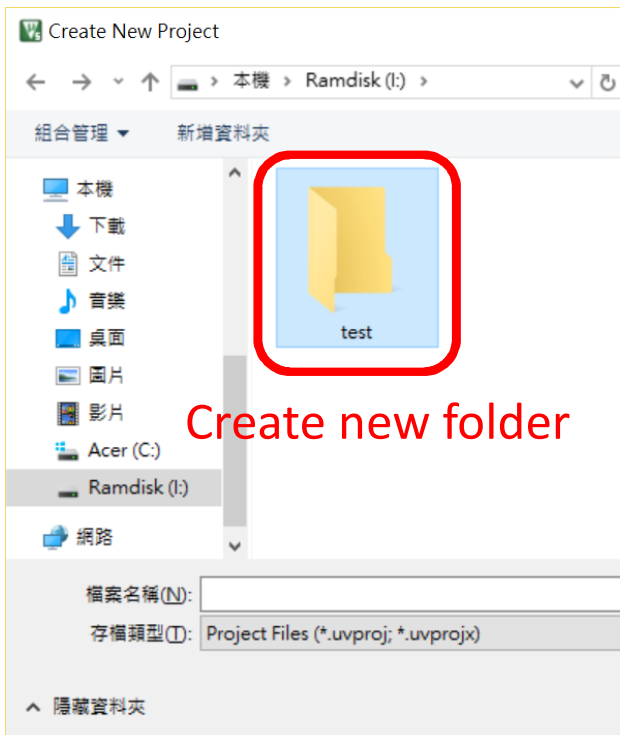


File tree



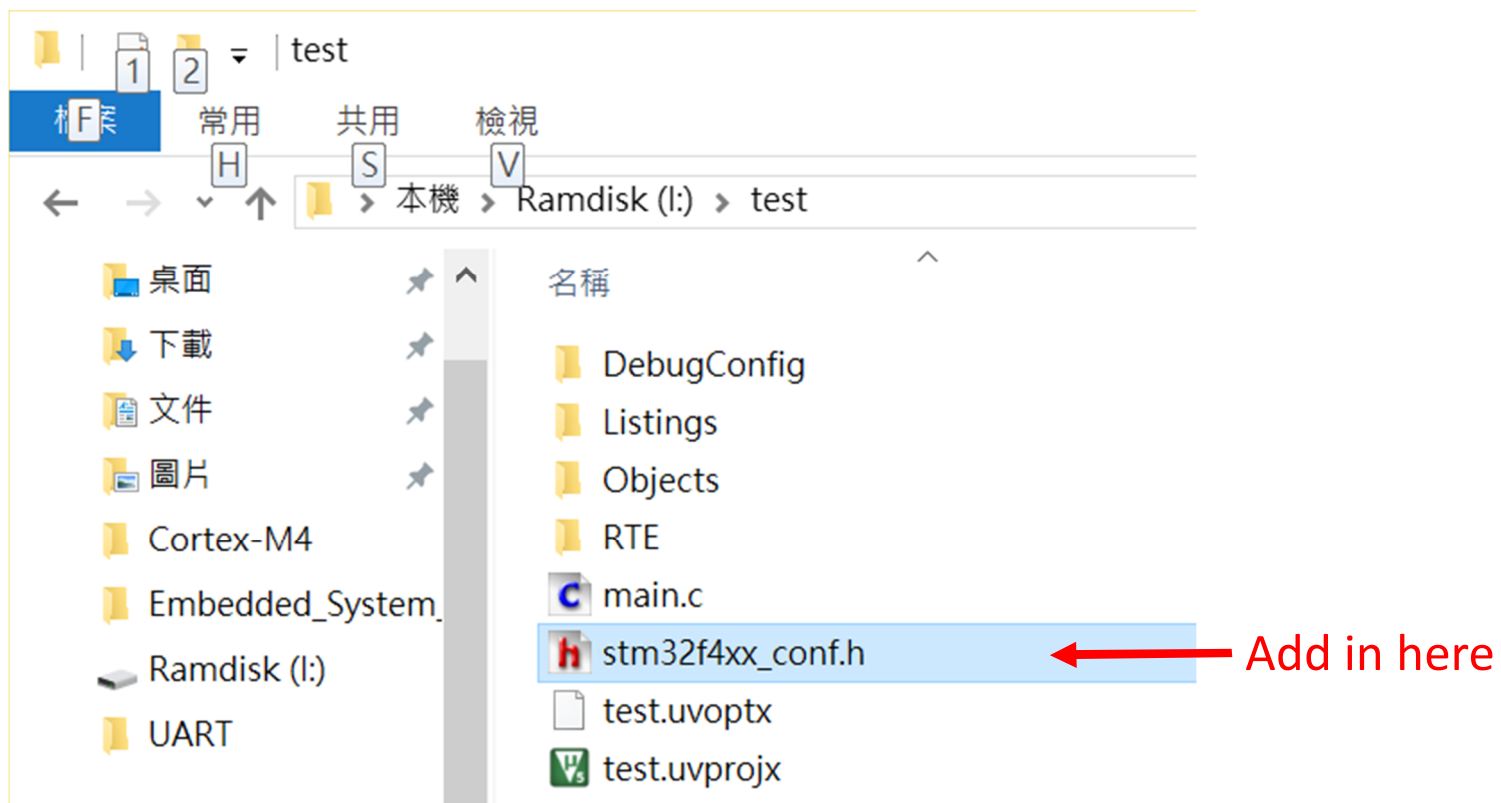
Step3. Create new project

- Setup as usual.

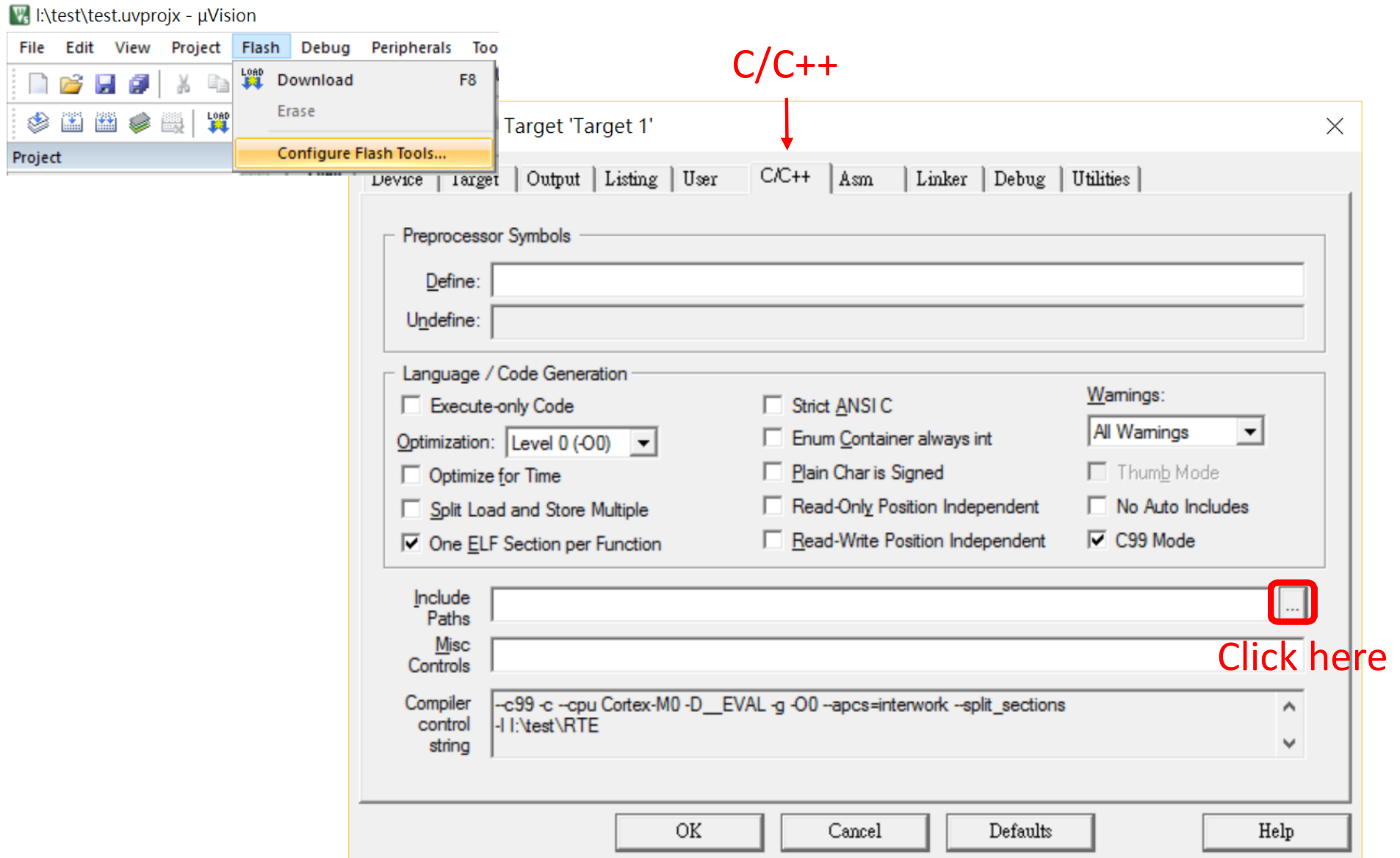


Step4. Add stm32f4xx_conf.h

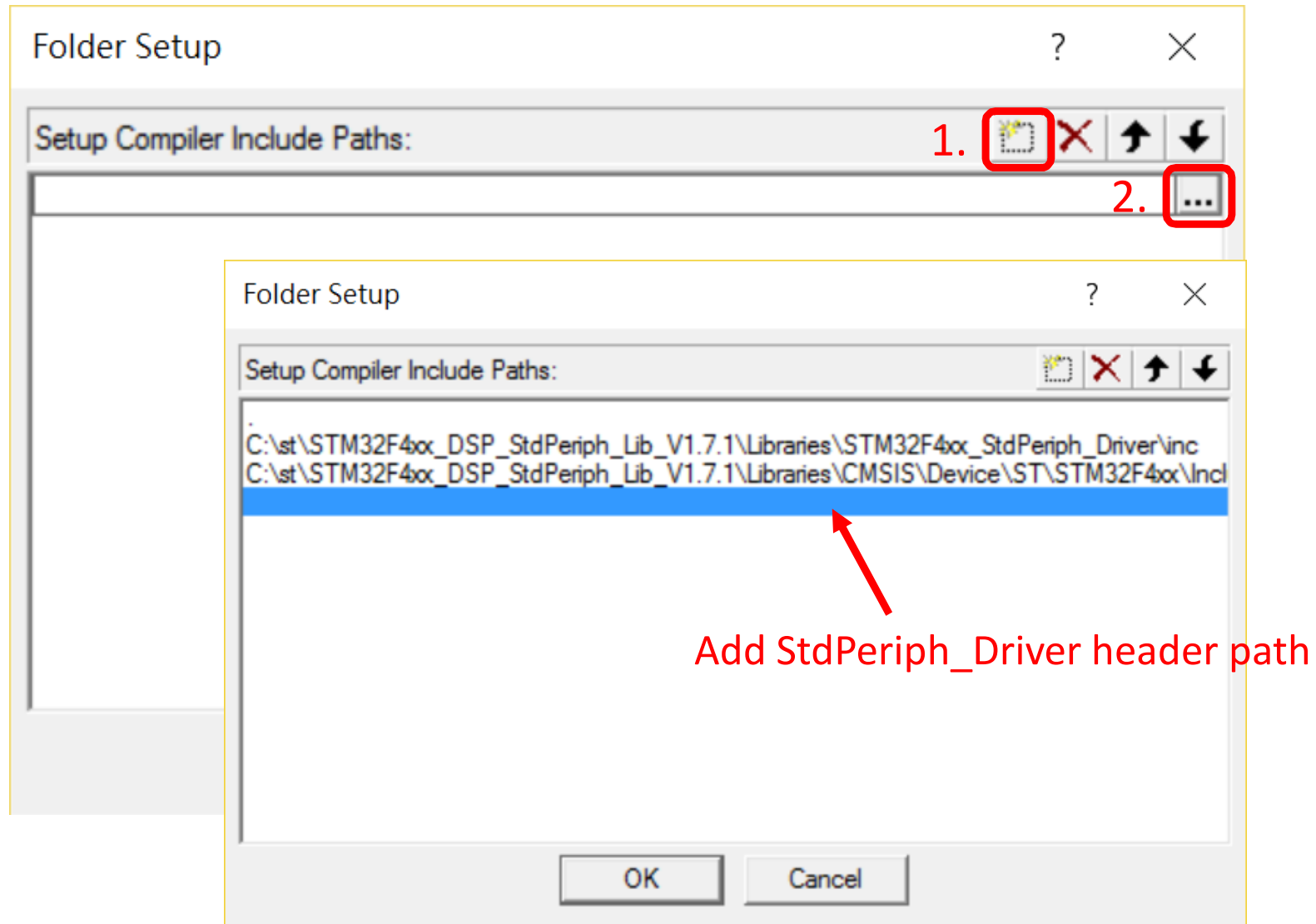
- This header can be found in any example.



Step5. Add include path

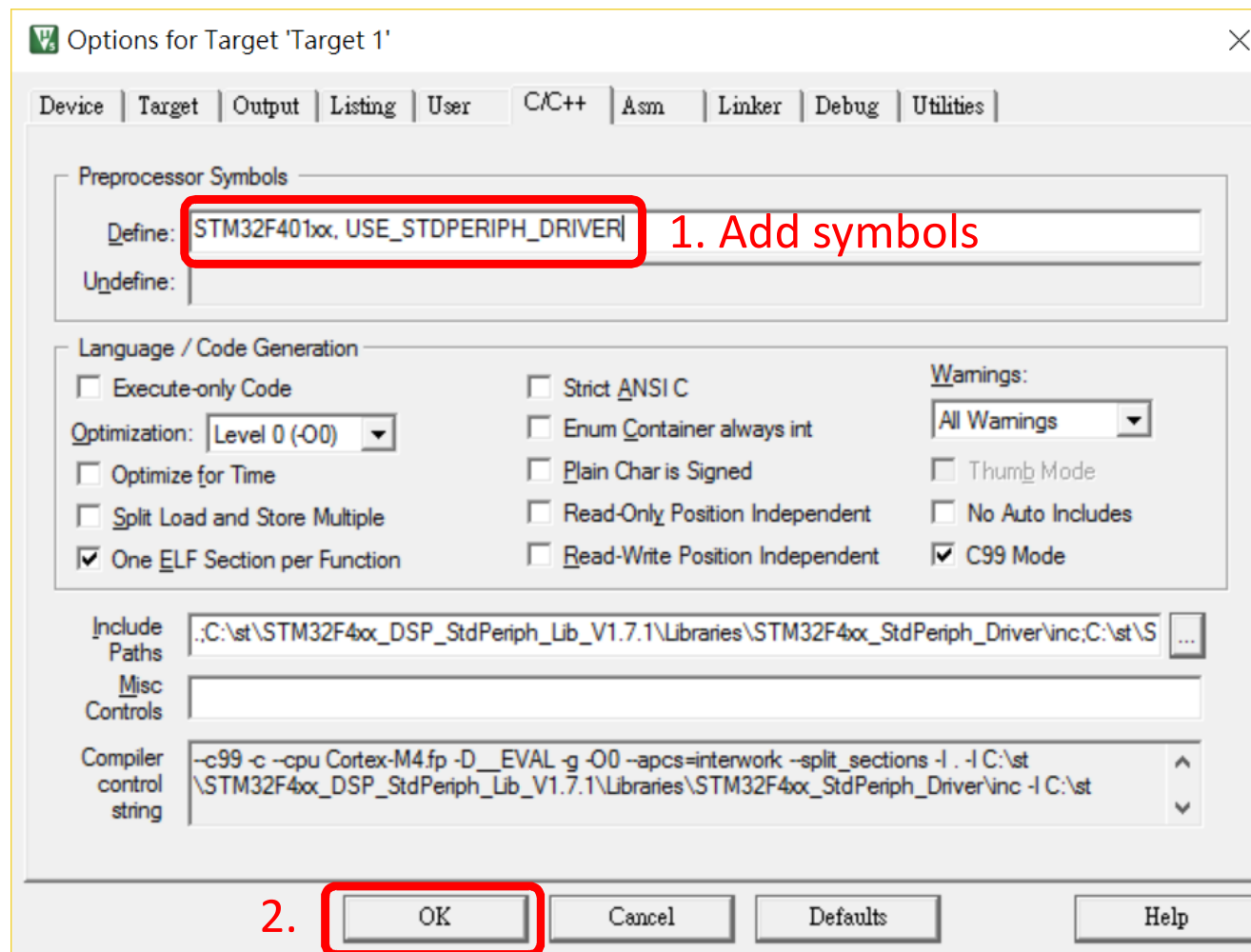


Step5. Add include path



Step6. Add Preprocessor Symbol

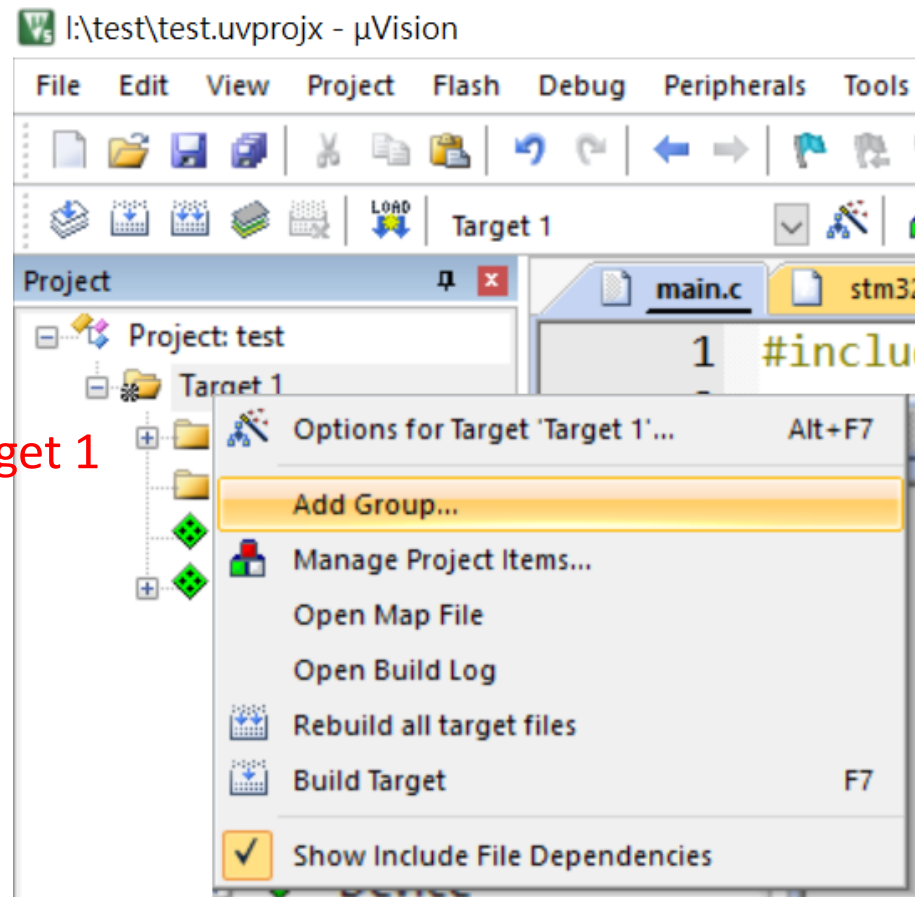
- Type STM32F401xx, USE_STDPERIPH_DRIVER.



Step8. Add source file

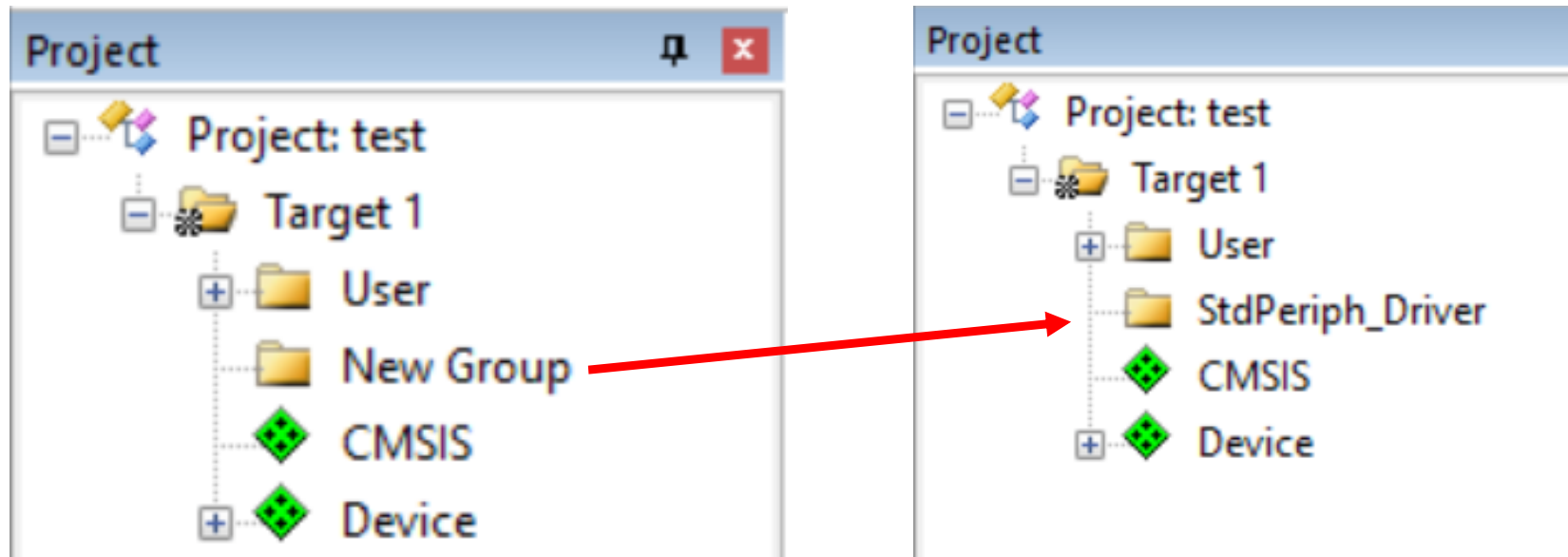
- Add group.

Right click on Target 1

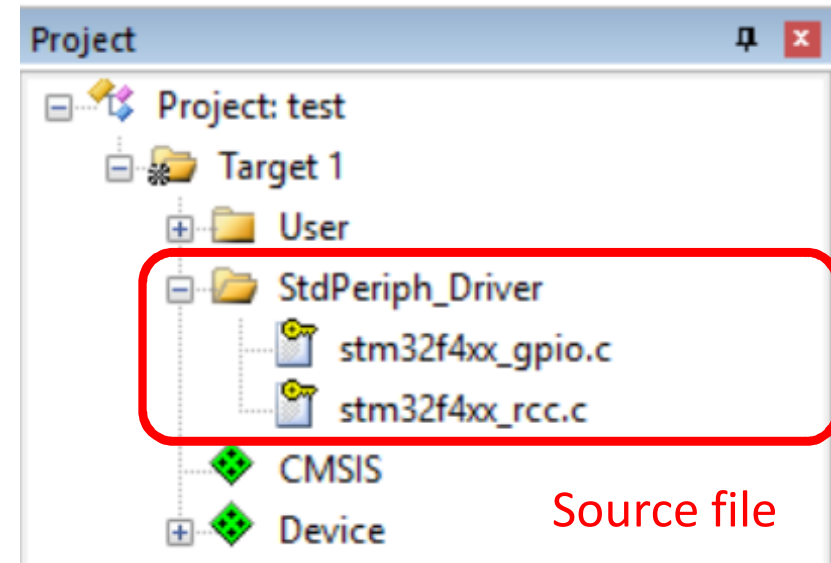
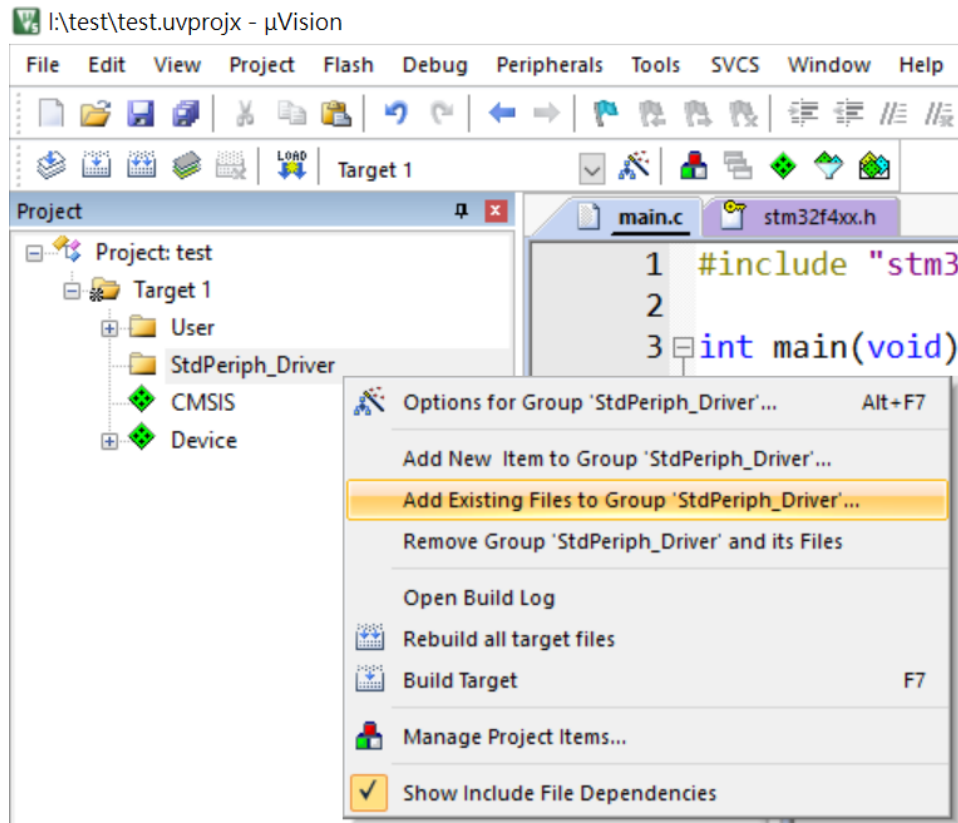


Step8. Add source file

- Rename New Group to StdPeriph_Driver.

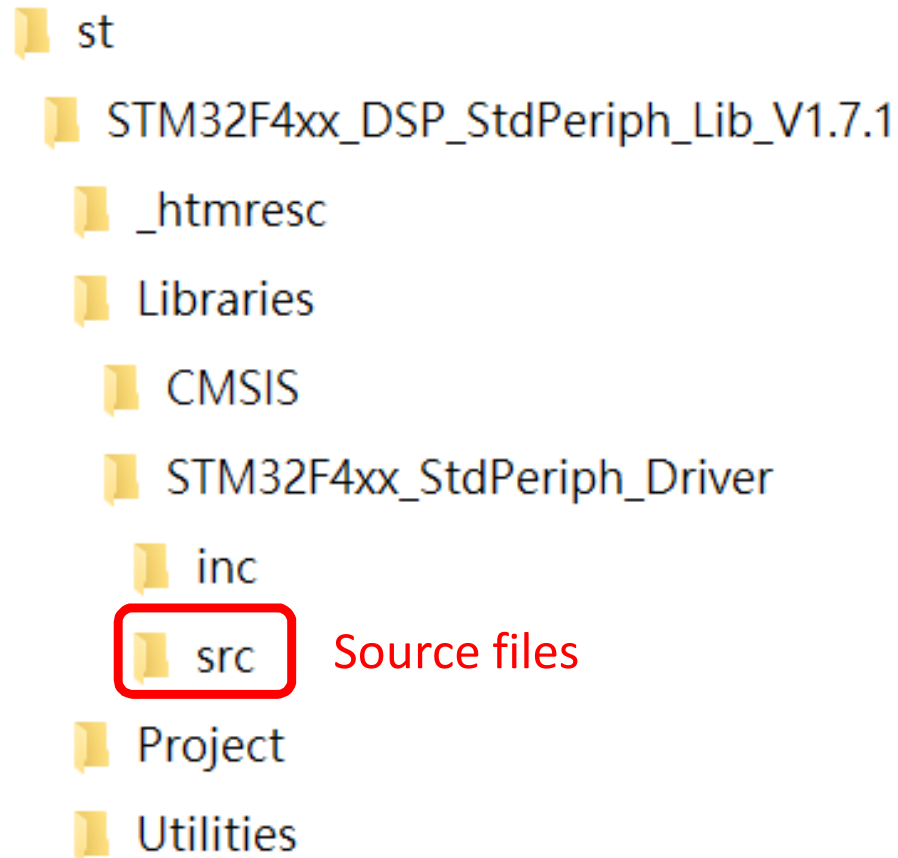


Step8. Add source file



Step8. Add source file

- Source files will be in here.



Step9. Test

- Blinky LED.

```
main.c
1  #include "stm32f4xx.h"           // Device header
2
3  int main(void){
4      RCC_AHB1PeriphClockCmd(RCC_AHB1Periph_GPIOA, ENABLE);
5
6      GPIO_InitTypeDef GPIO_InitStructure;
7      GPIO_InitStructure.GPIO_Pin = GPIO_Pin_5;
8      GPIO_InitStructure.GPIO_Mode = GPIO_Mode_OUT;
9      GPIO_InitStructure.GPIO_OType = GPIO_OType_PP;
10     GPIO_Init(GPIOA, &GPIO_InitStructure);
11
12     while(1){
13         GPIO_ToggleBits(GPIOA, GPIO_Pin_5);
14         for(int i=0; i<1600000; i++);
15     }
16     return 0;
17 }
18
```

Exercise

Transfer previous code using StdPeriph Library.

```
main.c  stm32f4xx.h
1  #include "stm32f4xx.h"           // Device header
2
3  int main(void){
4      RCC->AHB1ENR |= RCC_AHB1ENR_GPIOCEN;
5
6      GPIOC->MODER |= (0x1 << 2*5);
7
8      while(1){
9          GPIOC->BSRRH = (0x1 << 5);
10         for(int i=0; i<1600000; i++);
11         GPIOC->BSRRL = (0x1 << 5);
12         for(int i=0; i<1600000; i++);
13     }
14     return 0;
15 }
16
```

```
main.c
1  #include "stm32f4xx.h"           // Device header
2
3  int main(void){
4      RCC_AHB1PeriphClockCmd(RCC_AHB1Periph_GPIOA, ENABLE);
5
6      GPIO_InitTypeDef GPIO_InitStructure;
7      GPIO_InitStructure.GPIO_Pin = GPIO_Pin_5;
8      GPIO_InitStructure.GPIO_Mode = GPIO_Mode_OUT;
9      GPIO_InitStructure.GPIO_OType = GPIO_OType_PP;
10     GPIO_Init(GPIOA, &GPIO_InitStructure);
11
12     while(1){
13         GPIO_ToggleBits(GPIOA, GPIO_Pin_5);
14         for(int i=0; i<1600000; i++);
15     }
16     return 0;
17 }
18
```

Performance comparison

	Code Size(Byte)	Diff.	Execute Cycle	Diff.
Low Level	4453		1337	
HAL	5427	21.9%	1912	43.0%
StdPeriph Library	6028	35.4%	2100	57.1%