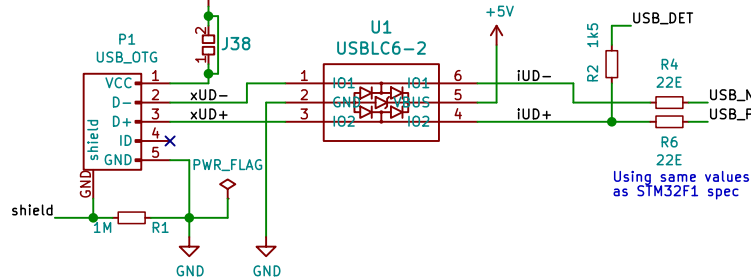
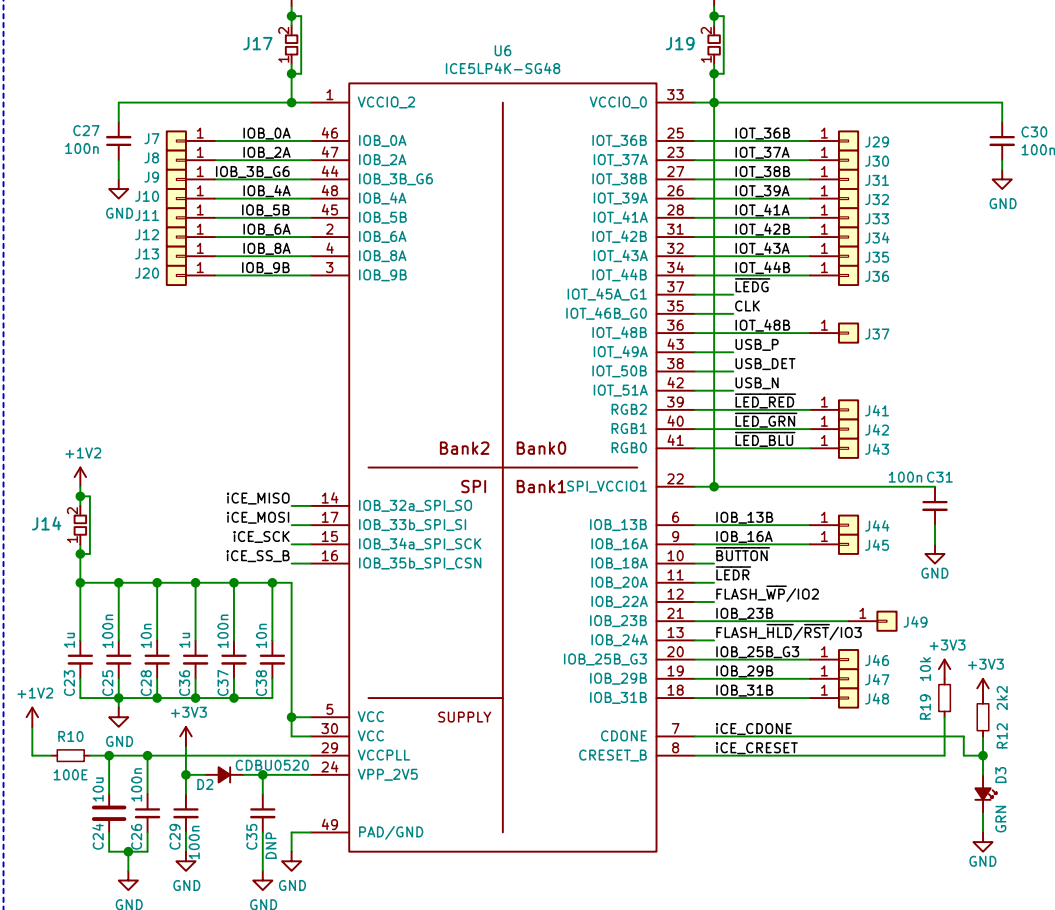


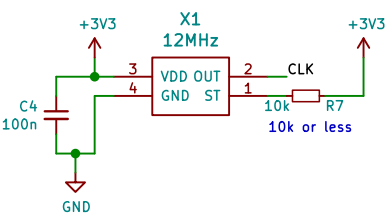
USB



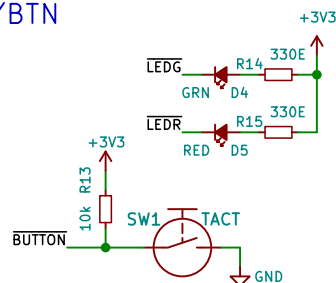
FPGA



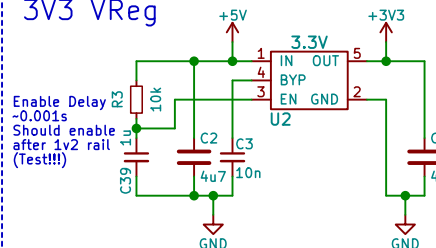
Ext Clock



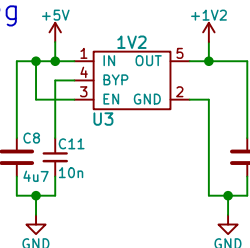
LED/BTN



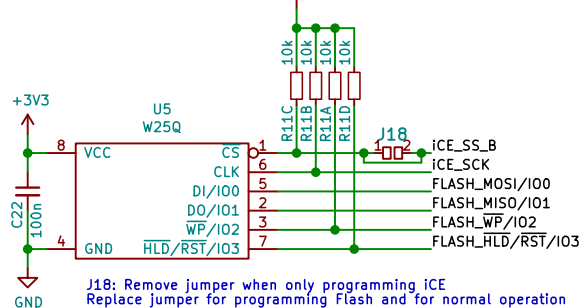
3V3 VReg



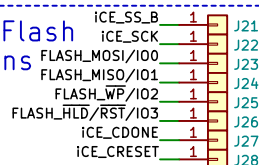
1V2 VReg



ICE FPGA Flash



ICE FPGA Flash & Prog Pins



For programming ICE FPGA:
* cut traces connecting J15 and J16 pads.
* Short the pads 1 and 2 together of the J15 and J16
For programming Flash reverse the operations.

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