

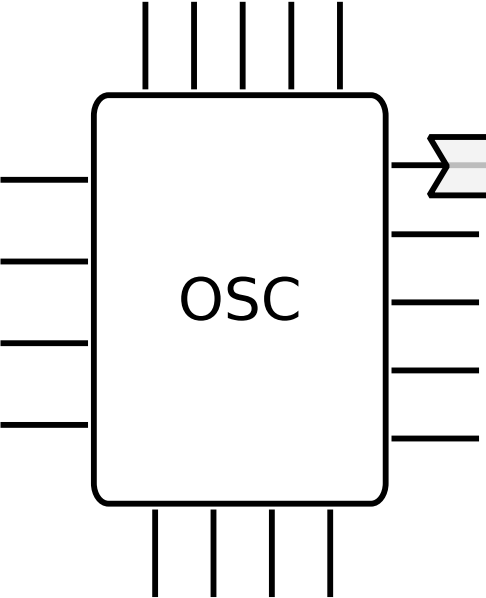
VDD

OUT (VDD)

PIN 3

OE (VDD)

IN



OUT0

