Response to: Can you please recheck for FTEMP

Apologies for any confusion earlier. Let's clarify the configuration settings for the BQ40Z50-R2 when using TS2 for cell

temperature and TSInt for FET temperature.

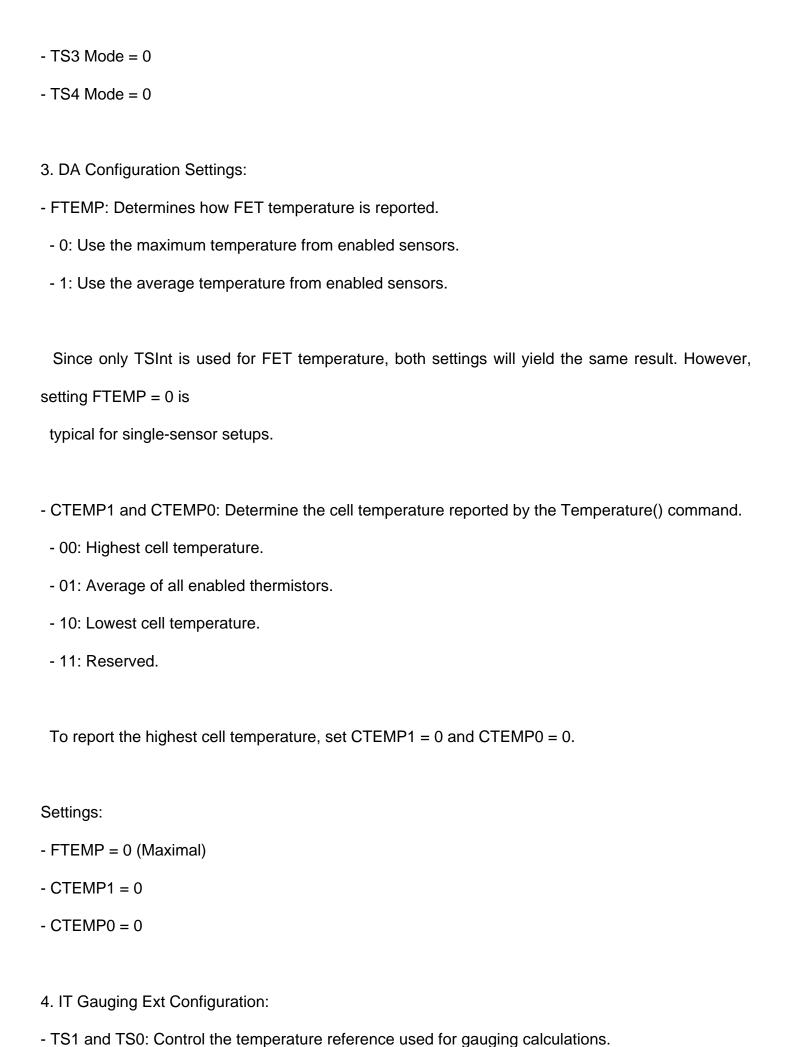
- 1. Temperature Enable Configuration:
- TS2: Enable for cell temperature measurement.
- TSInt: Enable for FET temperature measurement.
- TS1 and TS3: Disable, as they are not used.

Settings:

- -TS4 = 0
- TS3 = 0
- -TS2 = 1
- -TS1 = 0
- TSInt = 1
- 2. Temperature Mode Configuration:
- TS2: Set to report cell temperature.
- TSInt: Set to report FET temperature.

Settings:

- TS2 Mode = 0 (cell temperature)
- TSInt Mode = 1 (FET temperature)
- TS1 Mode = 0



- 00: Use the internal temperature sensor (TSInt).
- 01: Use the average of enabled external thermistors.
- 10: Use the minimum of enabled external thermistors.
- 11: Use the maximum of enabled external thermistors.

Since TS2 is used for cell temperature, set TS1 = 1 and TS0 = 1 to use the maximum temperature from enabled sensors.

Settings:

- -TS1 = 1
- TS0 = 1

Summary of Configuration Settings:

By configuring these settings, the BQ40Z50-R2 will use TS2 for cell temperature reporting and TSInt for FET temperature reporting, ensuring accurate measurements and appropriate protections.