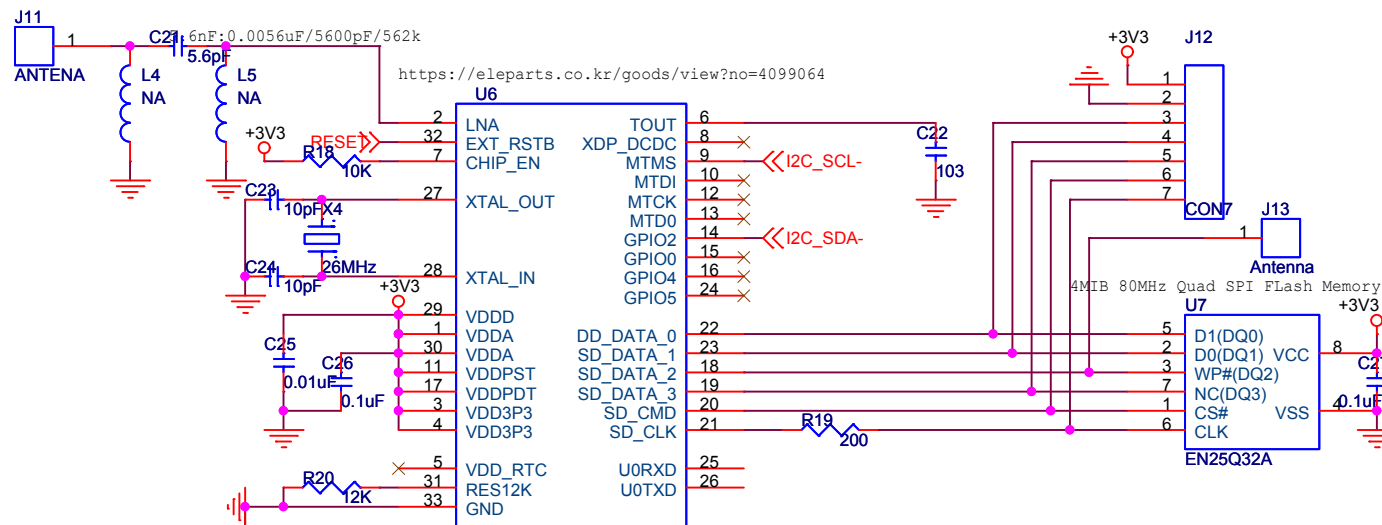
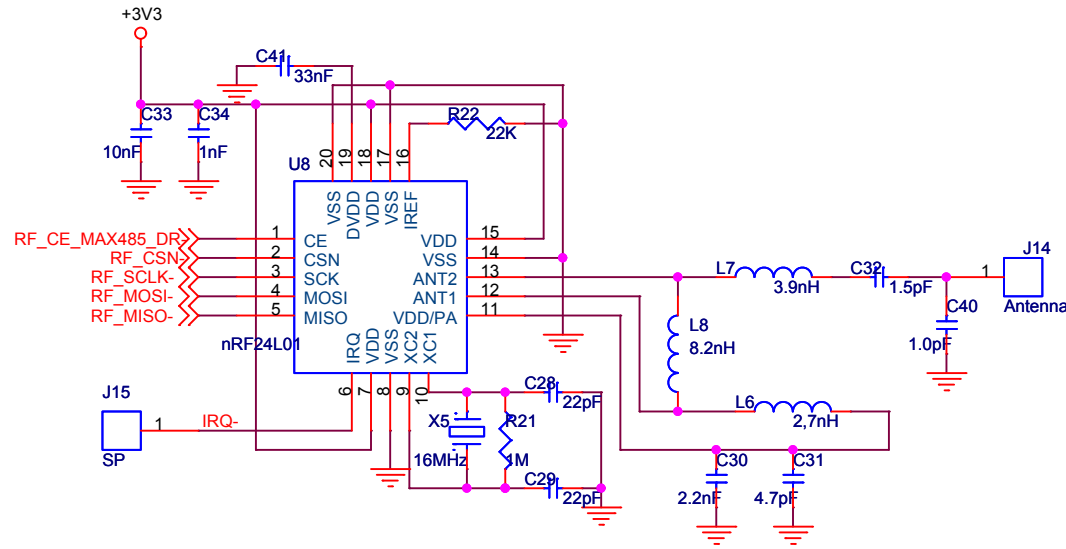
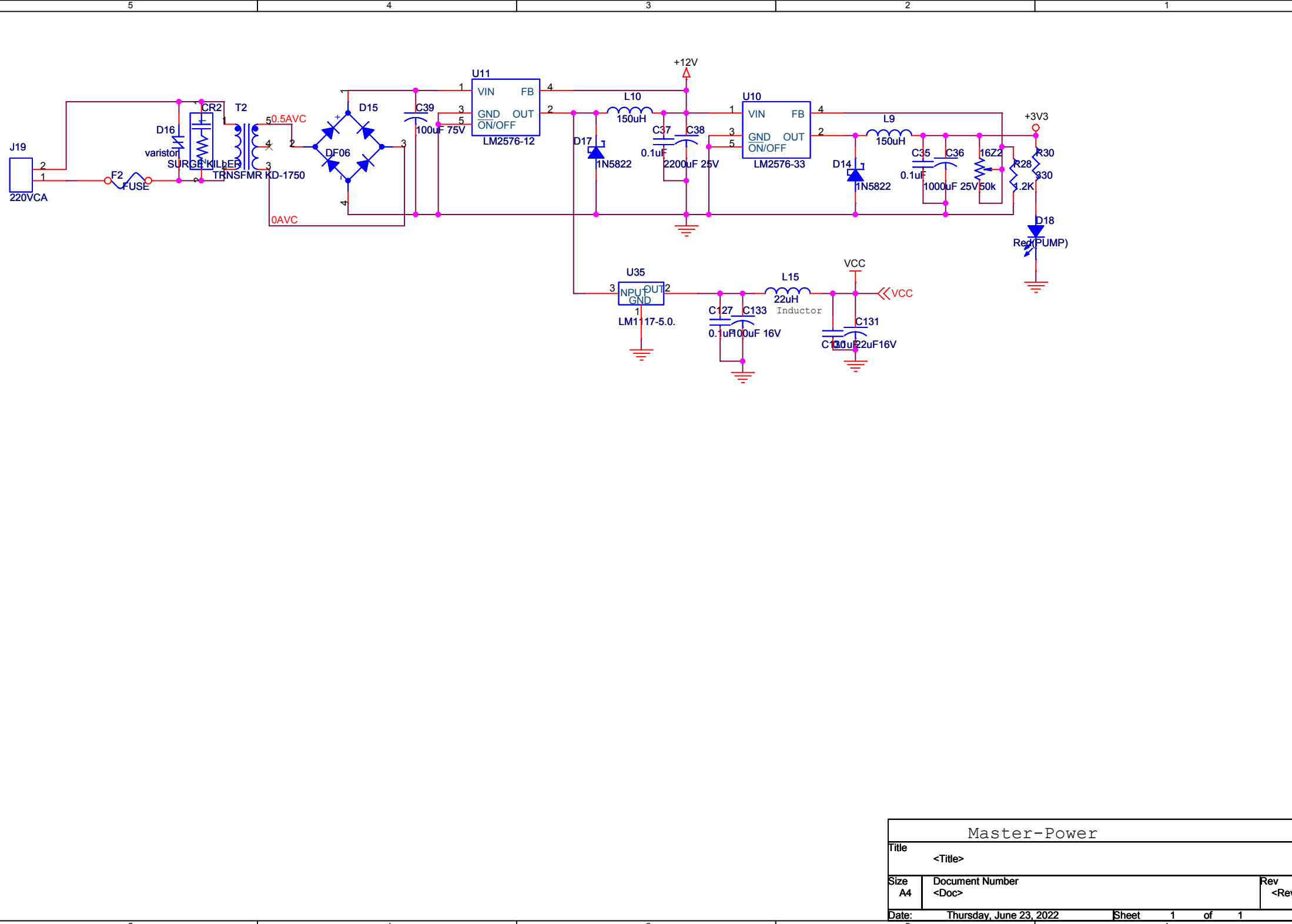


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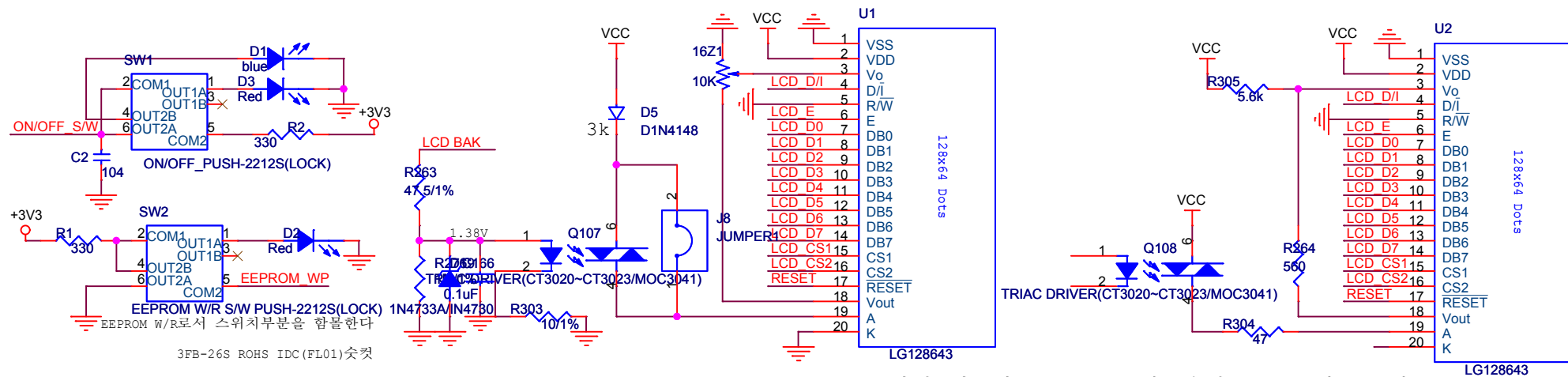


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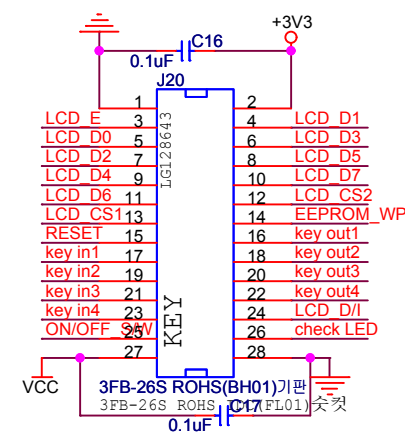
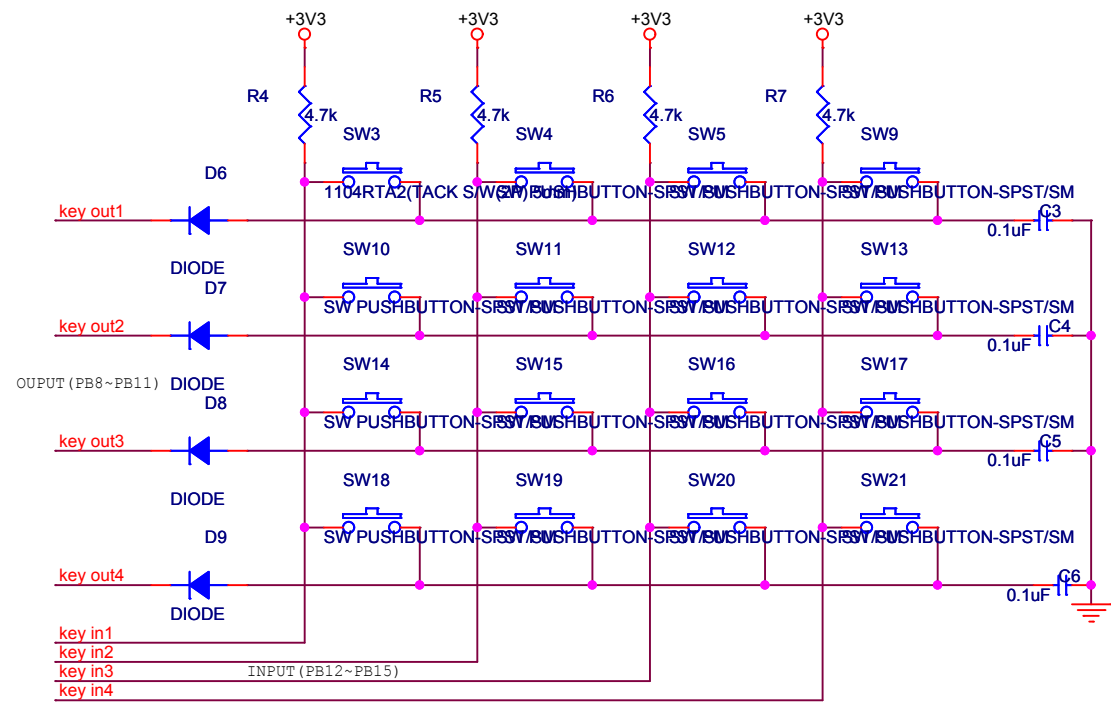




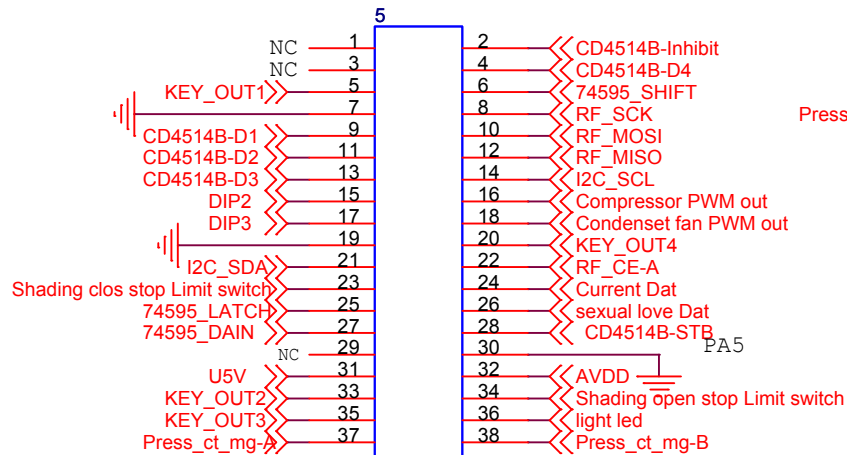
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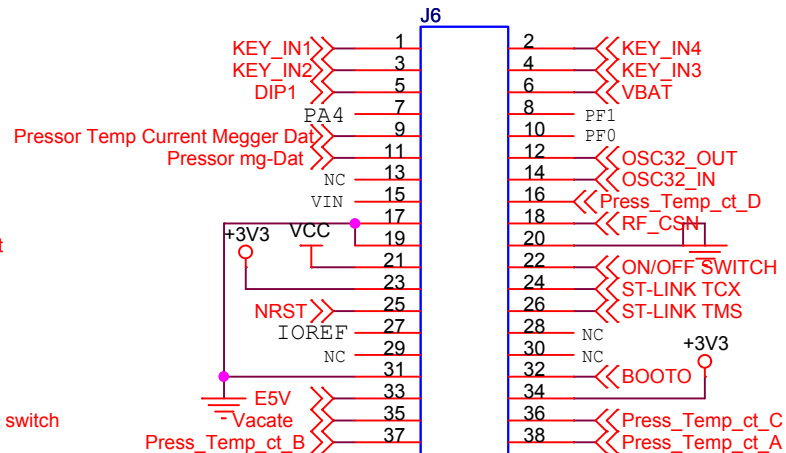
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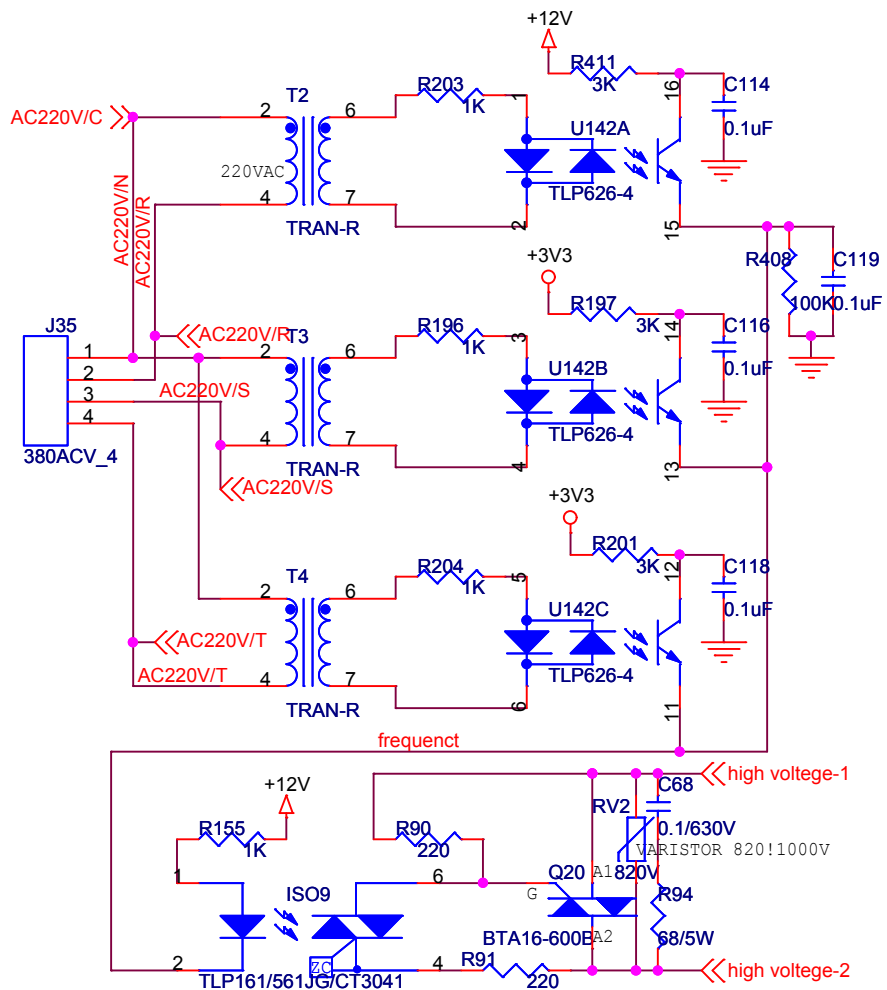


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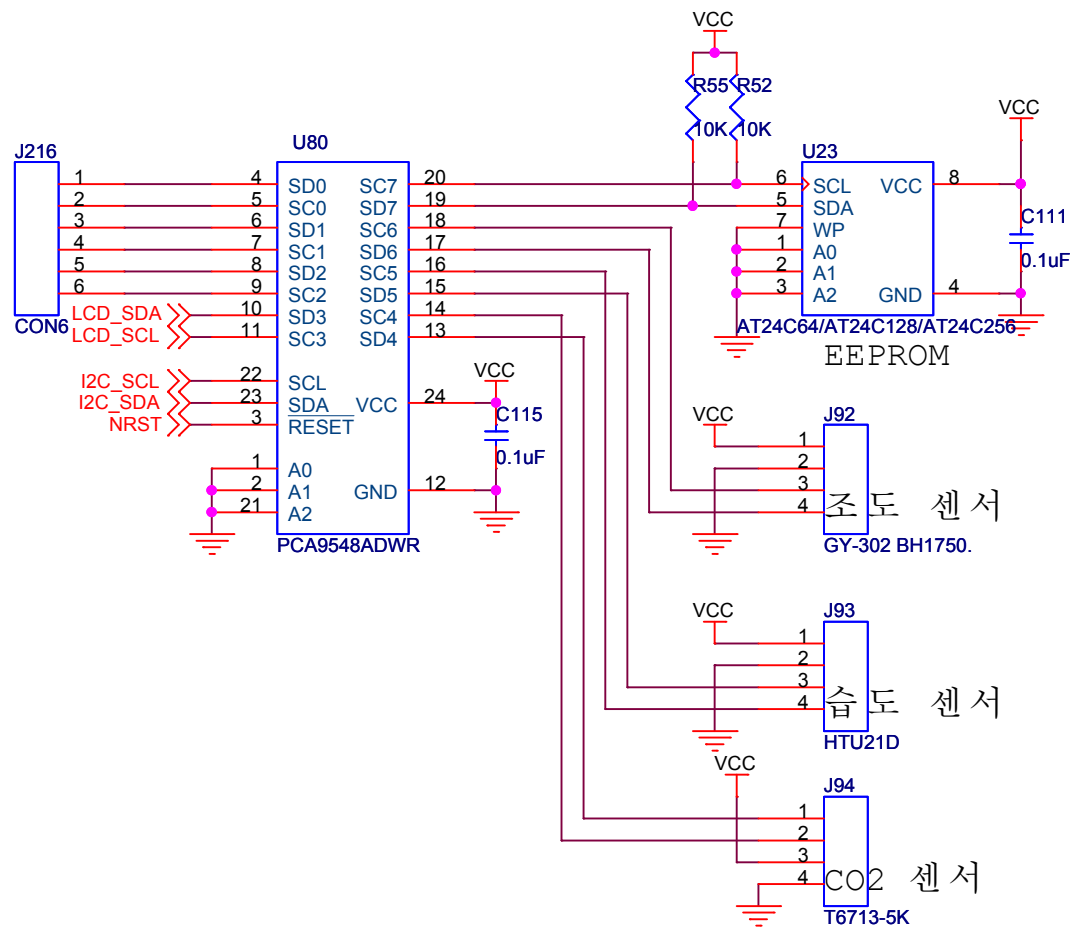
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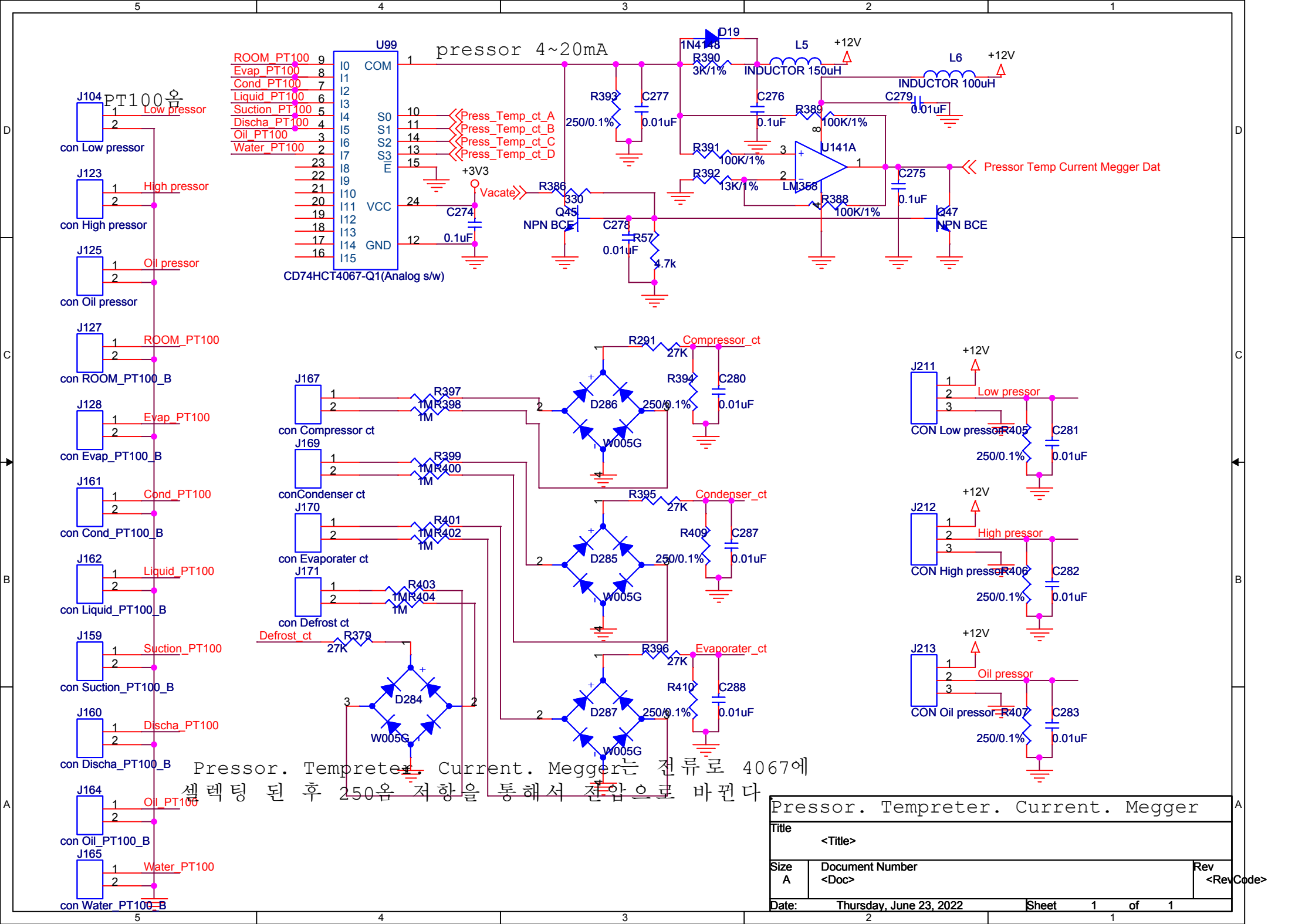
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V1 PCB에는 없음

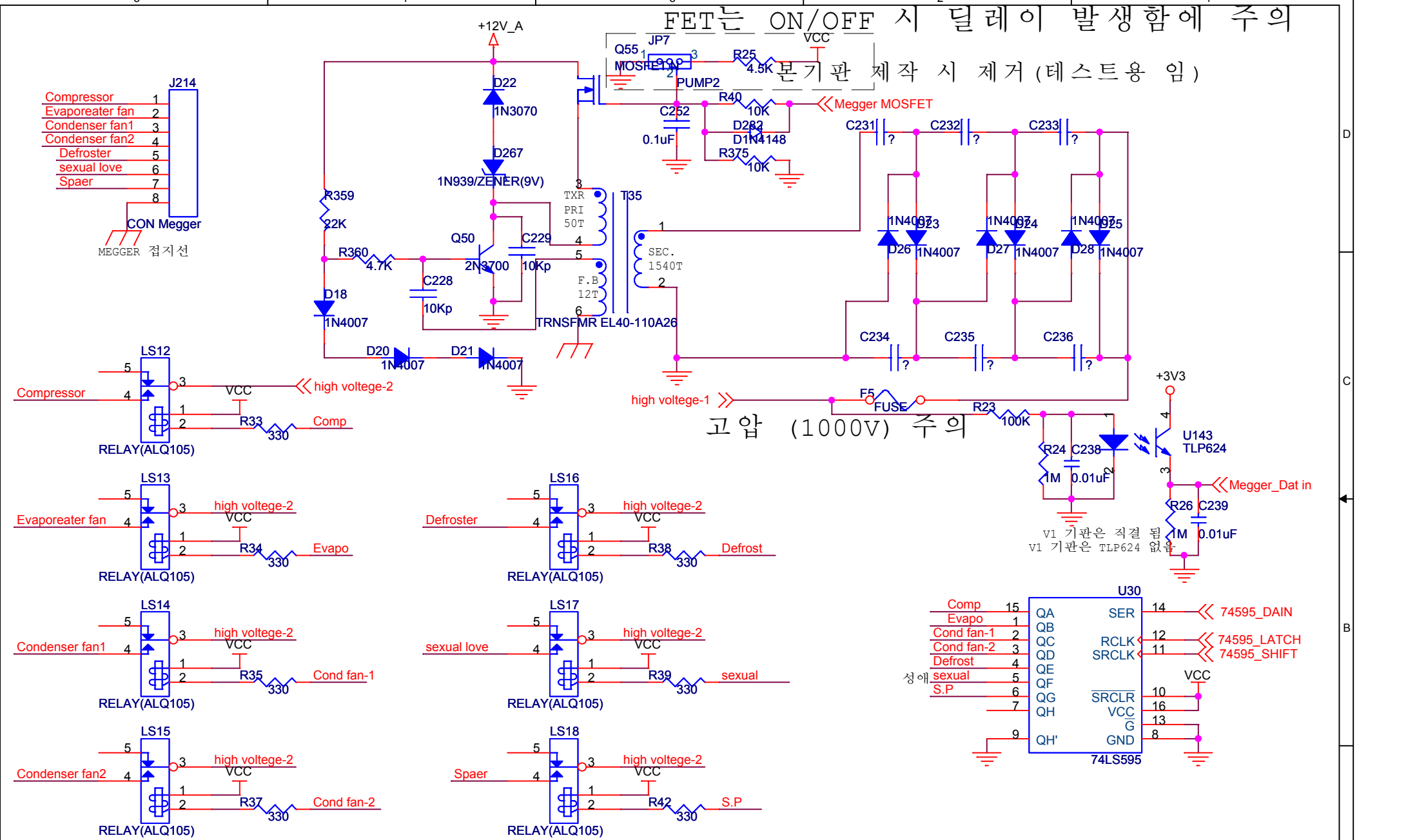


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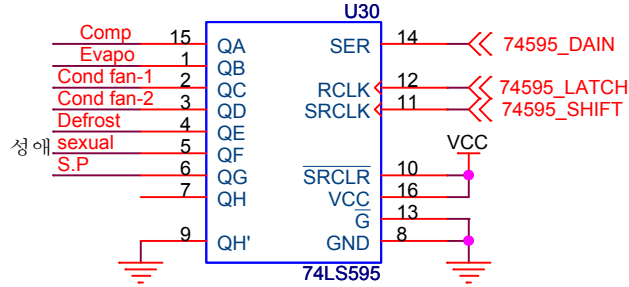
FET는 ON/OFF 시 딜레이 발생함에 주의

본기판 제작 시 제거 (테스트용 임)

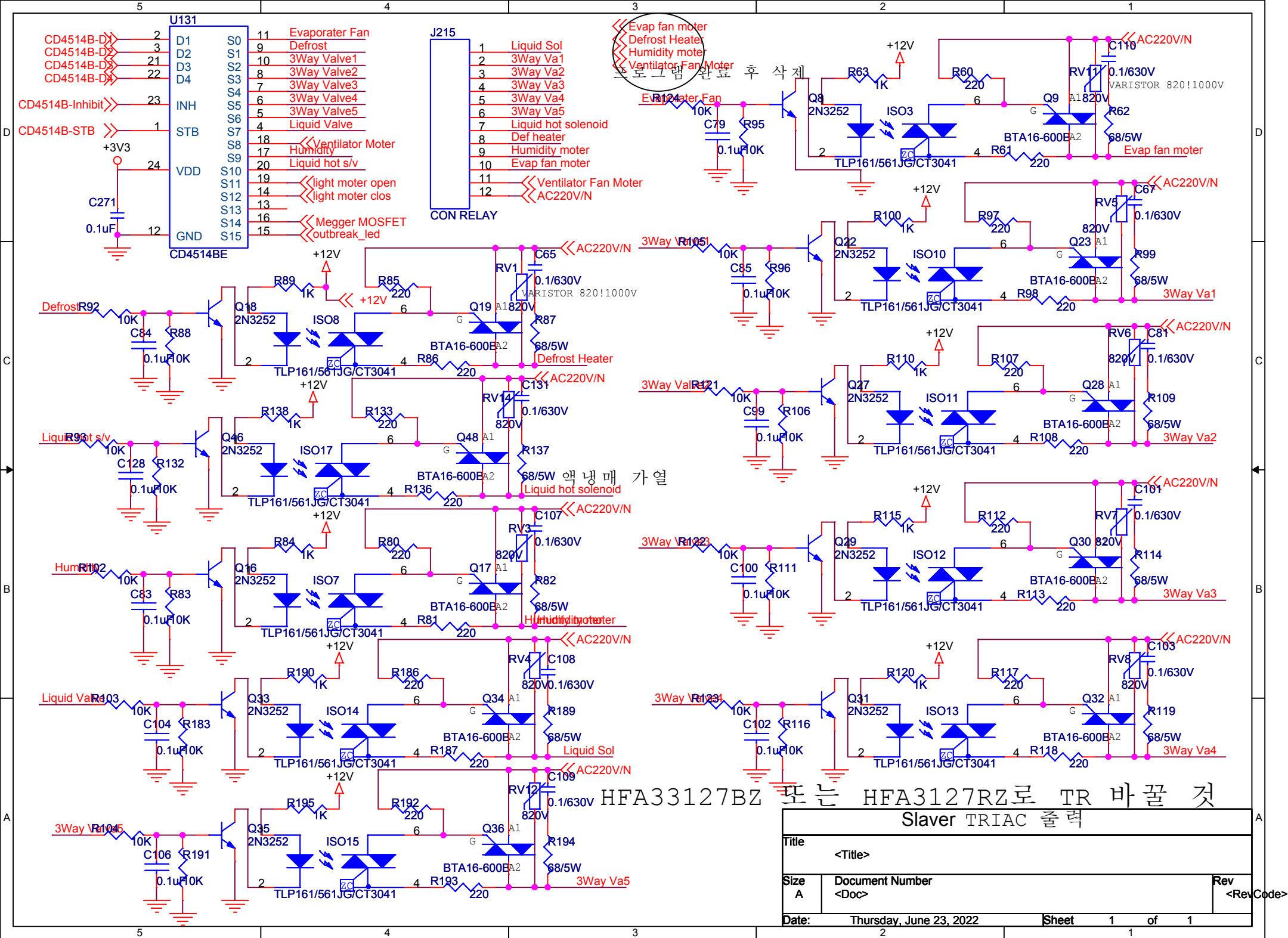


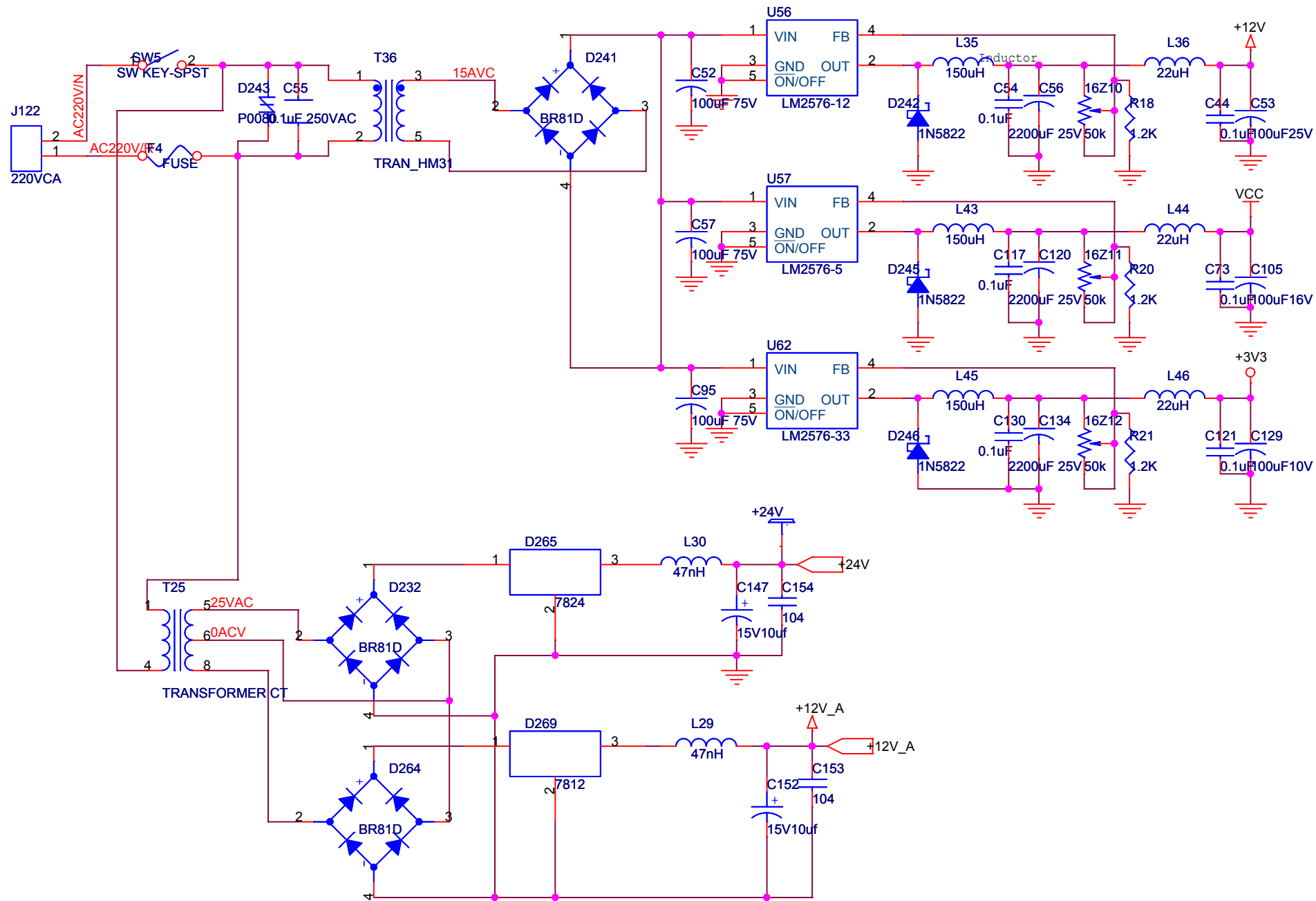
고압 (1000V) 주의

V1 기판은 직결 됨
V1 기판은 TLP624 없음

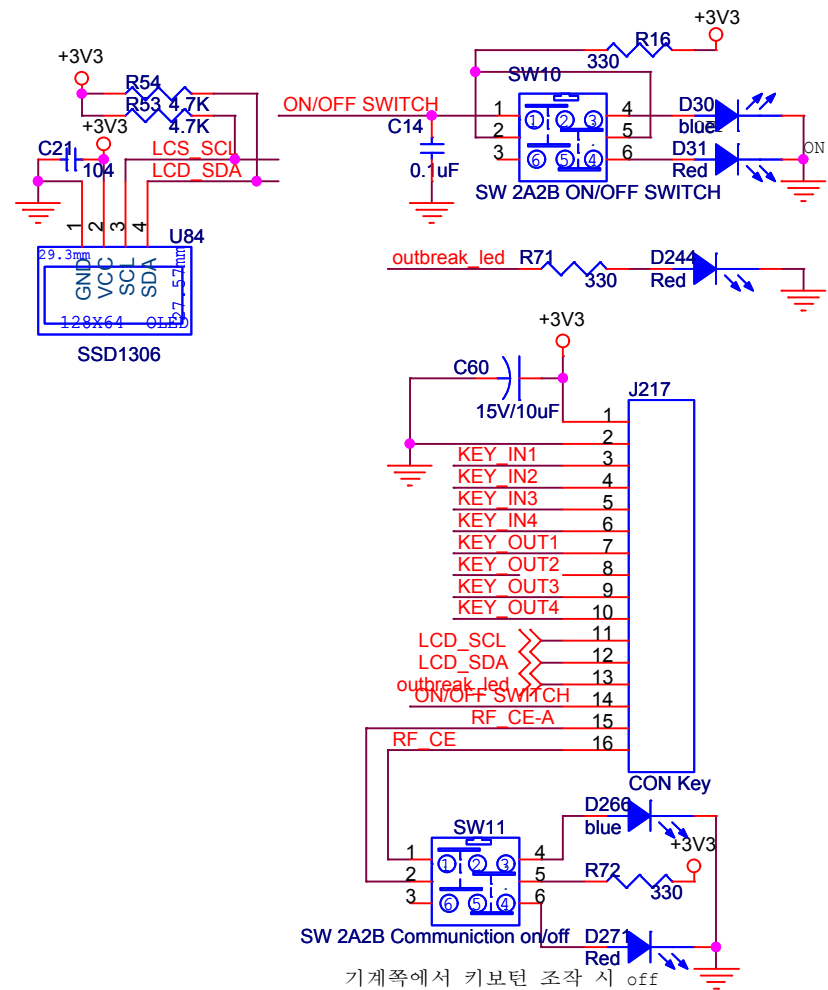


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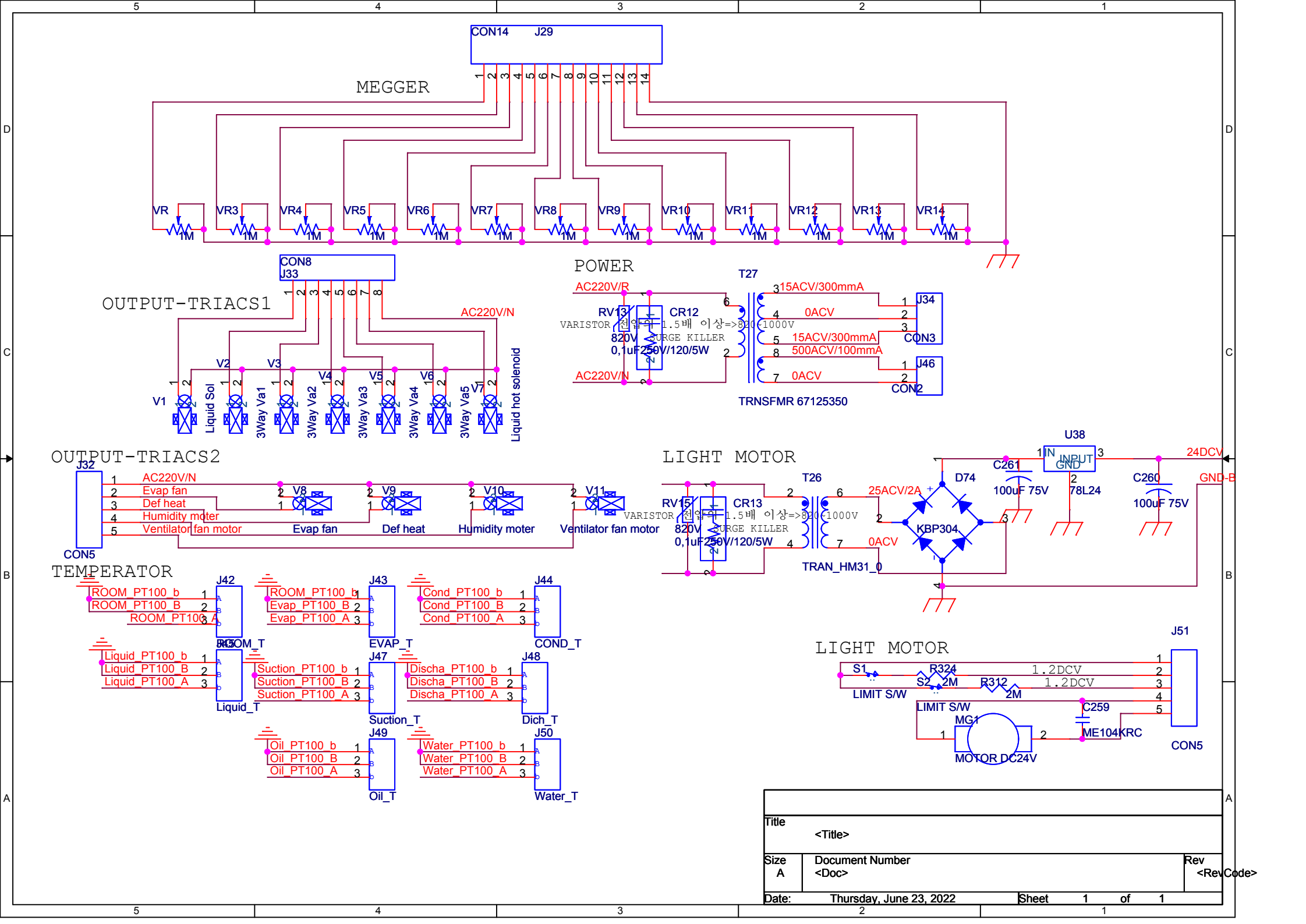




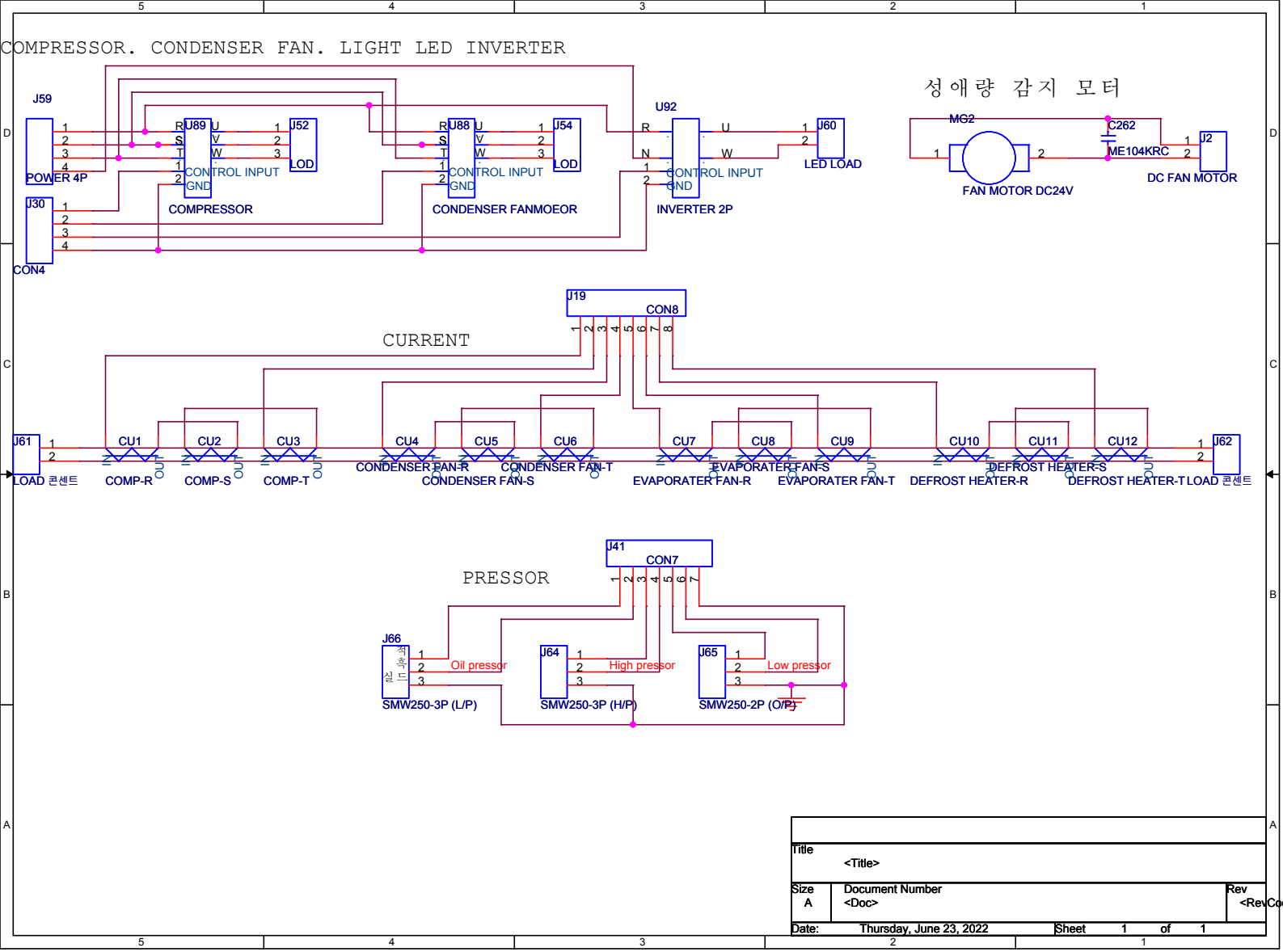
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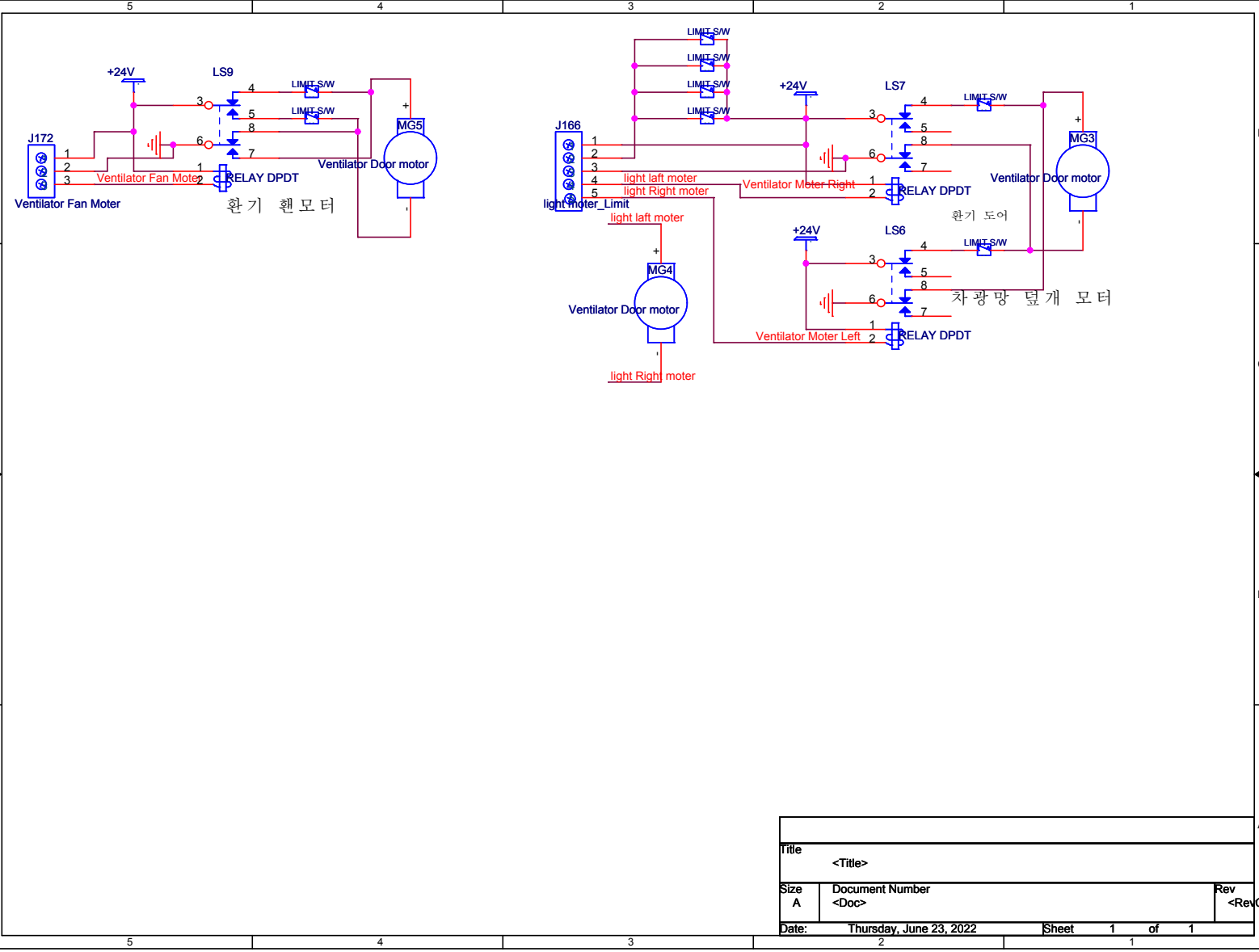


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