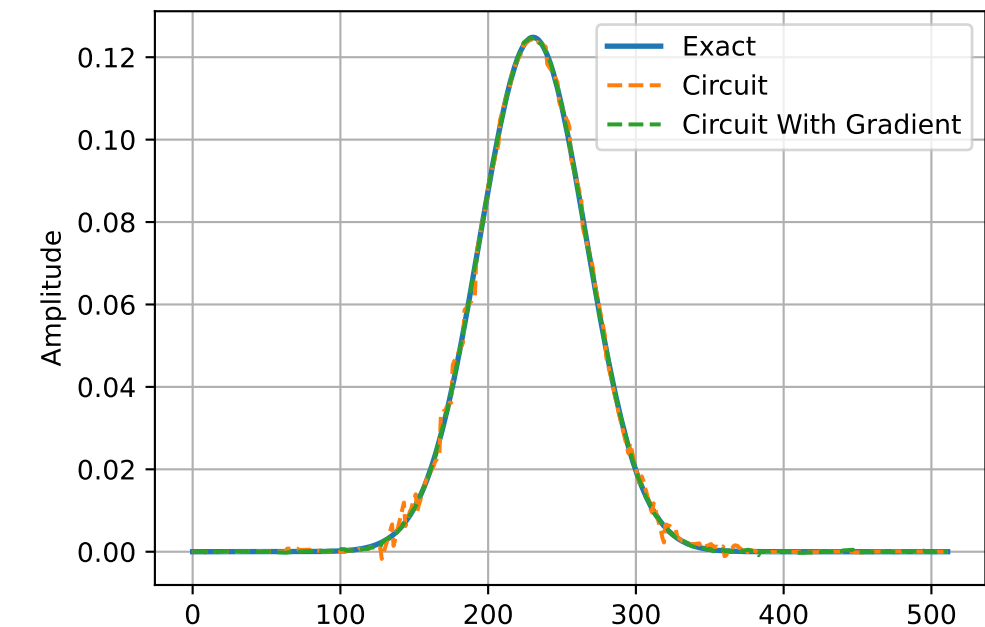
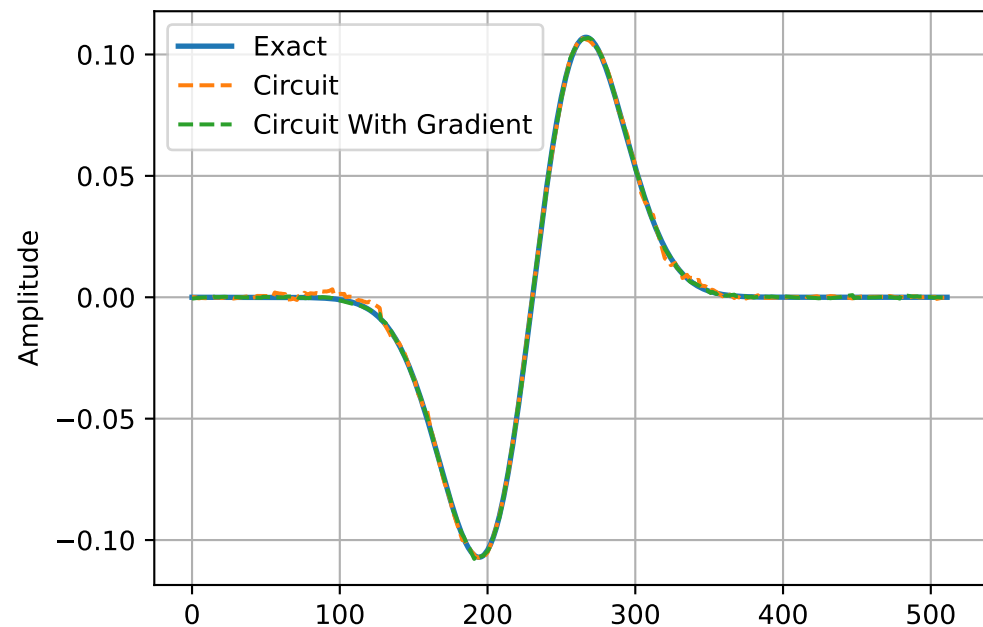


Circuit vs Exact States and Errors

Function 1



Function 2



Function 3

