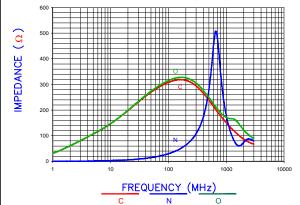
#### PHYSICAL DIMENSIONS:

- ± 0.18 [.007] A 11.94 [.470]
- ± 0.13 [.005] 8.13 [.320]
- B<sub>1</sub> 10.92 [.430] MAX
- ± 0.25 [.010] 14.48 [.570]
- MAX 15.11 [.595] + 0.05 [.002] 4.06 [.160]
- 1.27 [.050] + 0.13 [.005]
- 2.03 [.080] ± 0.13 [.005]

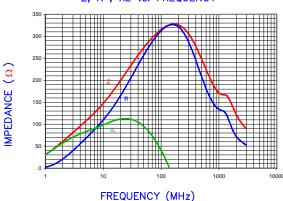
## WIRE DIMENSIONS:

- 3.30 [.130] ± 0.38 [.015]
- 0.64 [.025] TYP
- 0.38 [.015] **TYP**

### Z vs. FREQUENCY (C,O,N)



#### Z, R , XL vs. FREQUENCY

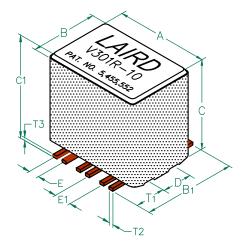


AGILENT E4991A RF Impedance/Material Analyzer

HP 16194A Test Fixture. TEST REF. 3007

# CM4732V301R-10





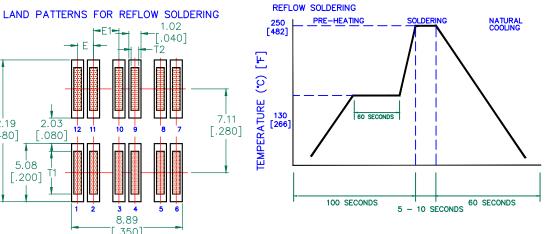
# **UNCONTROLLED DOCUMENT**

ELECTRICAL CHARACTERISTICS:				
Z @ 100MHz ( Ω )		DCR (Ω)	Rated Current	Rated Voltage (VDC)
Nominal	300			
Minimum	225			
Maximum	375	0.01	8,000 mA	30

NOTES: UNLESS OTHERWISE SPECIFIED

- 1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 13" REELS, 150 PCS/REEL.
- 2. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
- 3. REF. CARRIER TAPE SPECIFICATION #CART4732-33.
- 4. TERMINATION FINISH IS 100% TIN.
- 5. THIS PART HAS NO PIN POLARITY.
- 6. OPERATION TEMPERATURE (INCLUDING SELF-HEATING): -40 ~ +125℃.

#### RECOMMENDED SOLDERING CONDITIONS



**FQUIVALENT CIRCUIT** 

12.19

[.480]

DIMENSIONS ARE IN mm (INCHES) This print is the property of Laird Tech, and is loaned in confidence subject to return upon request and Laird with the understanding that no copies shall be made without the written consent of Laird Tech. All rights to design or invention are PROJECT/PART NUMBER: PART TYPE: UPDATE COMPANY LOGO AND KAPTON С 11/11/08 JRK ASSEMBLY CM4732V301R-10 LABEL ADD EQUIV. CIRCUIT UPDATE COMPANY LOGO В 12/12/07 JRK DATE: 06/03/04 NTS 06/03/04 JRK CAD # ORIGINAL DRAFT Α 1 of 2 REV DESCRIPTION DATE INT CM4732V301R-10-D-1