

A

A

B

B

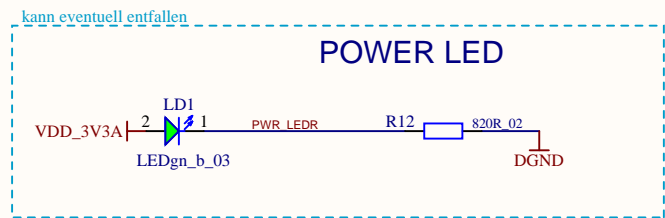
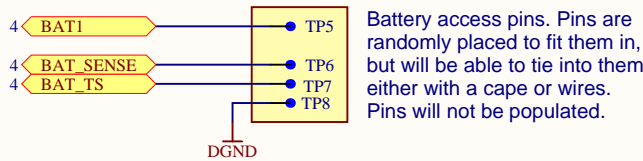
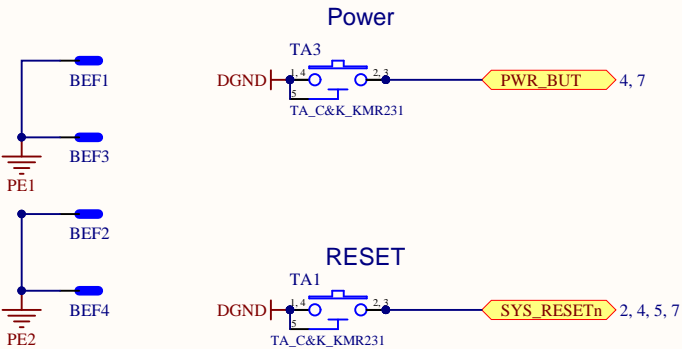
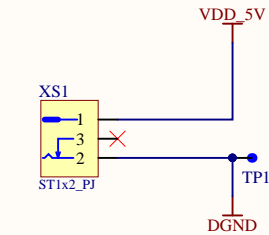
C

C

D

D

5V DC POWER



uSD BOOT

BOOT ORDER....MMC1....MMC0...UART0....USB0.....24MHZ

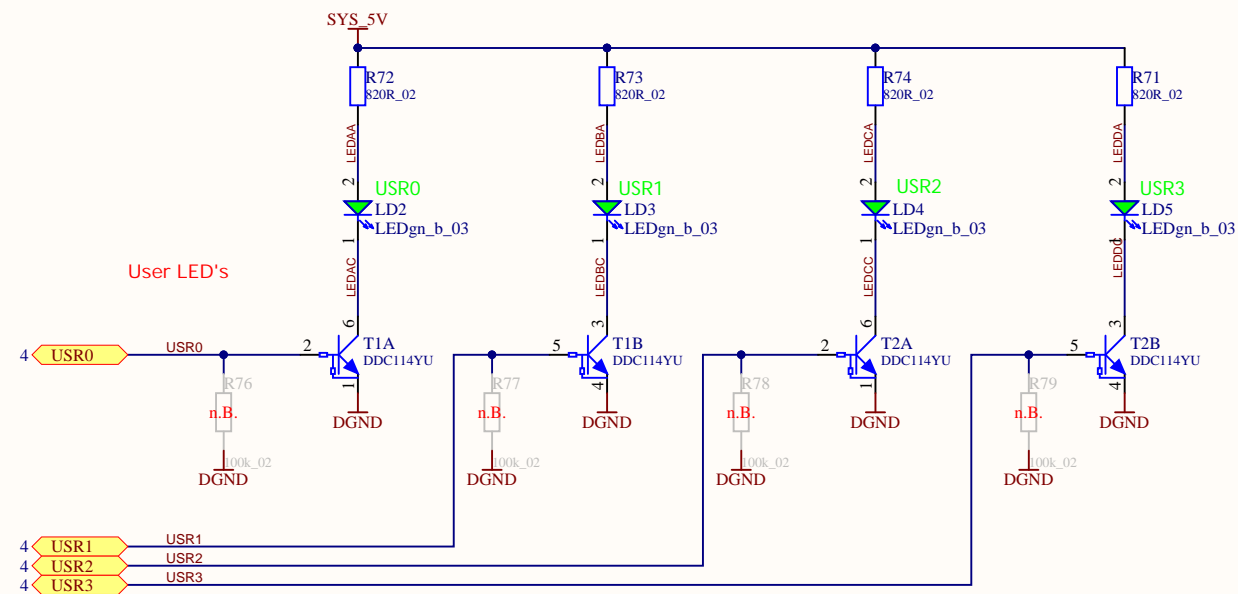
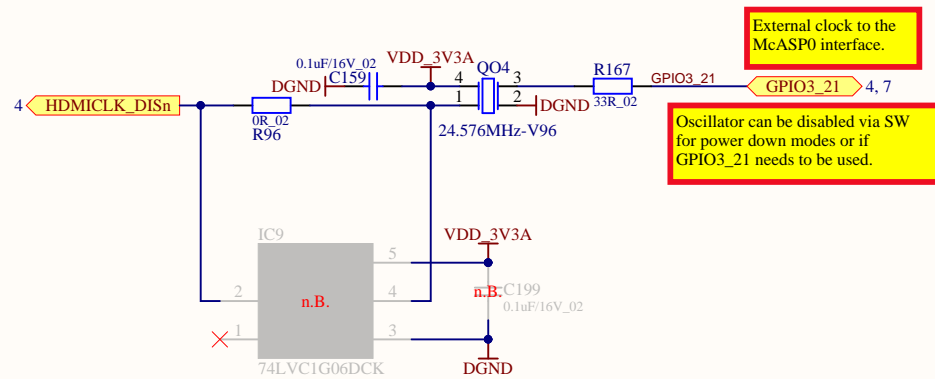
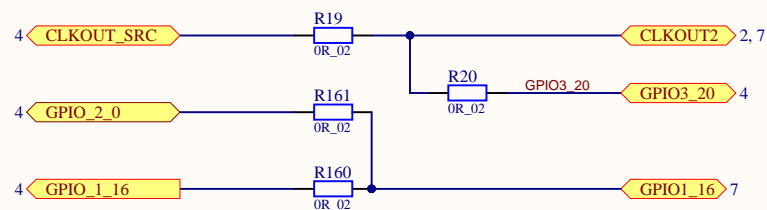
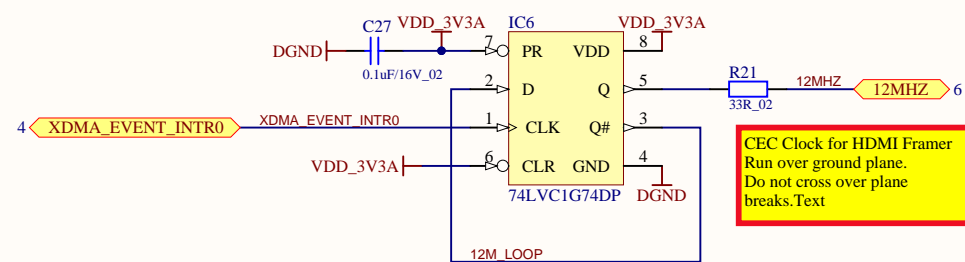
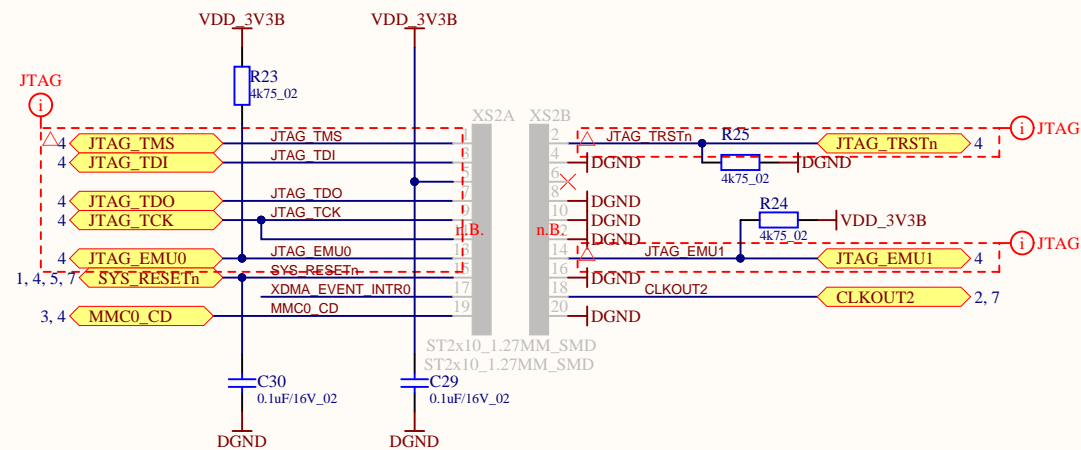
SYSBOOT[15:14]	SYSBOOT[13:12]	SYSBOOT[11:10]	SYSBOOT[9]	SYSBOOT[8]	SYSBOOT[7:6]	SYSBOOT[5]	SYSBOOT[4:0]	Boot Sequence			
00b = 19.2MHz 01b = 24MHz 10b = 25MHz 11b = 26MHz	00b (all other values reserved)	Don't care for ROM code	Don't care for ROM code	Don't care for ROM code	Don't care for ROM code	0 = CLKOUT1 disabled 1 = CLKOUT1 enabled	11100b	MMC1	MMC0	UART0	USB0[5]
00b = 19.2MHz 01b = 24MHz 10b = 25MHz 11b = 26MHz	00b (all other values reserved)	Don't care for ROM code	Don't care for ROM code	Don't care for ROM code	Don't care for ROM code	0 = CLKOUT1 disabled 1 = CLKOUT1 enabled	11000b	SPI0	MMC0	USB0[5] 1	UART0

	Date	Name
editor:	24.11.2011	Mierau
CAD:	24.11.2011	Mierau
checked:	24.11.2011	Finke

BEAGLECORE™

BCS1\_BB116a  
88645 - SL (3)

REV.:	2.3
LK :	6190202
sheet:	1
of:	8

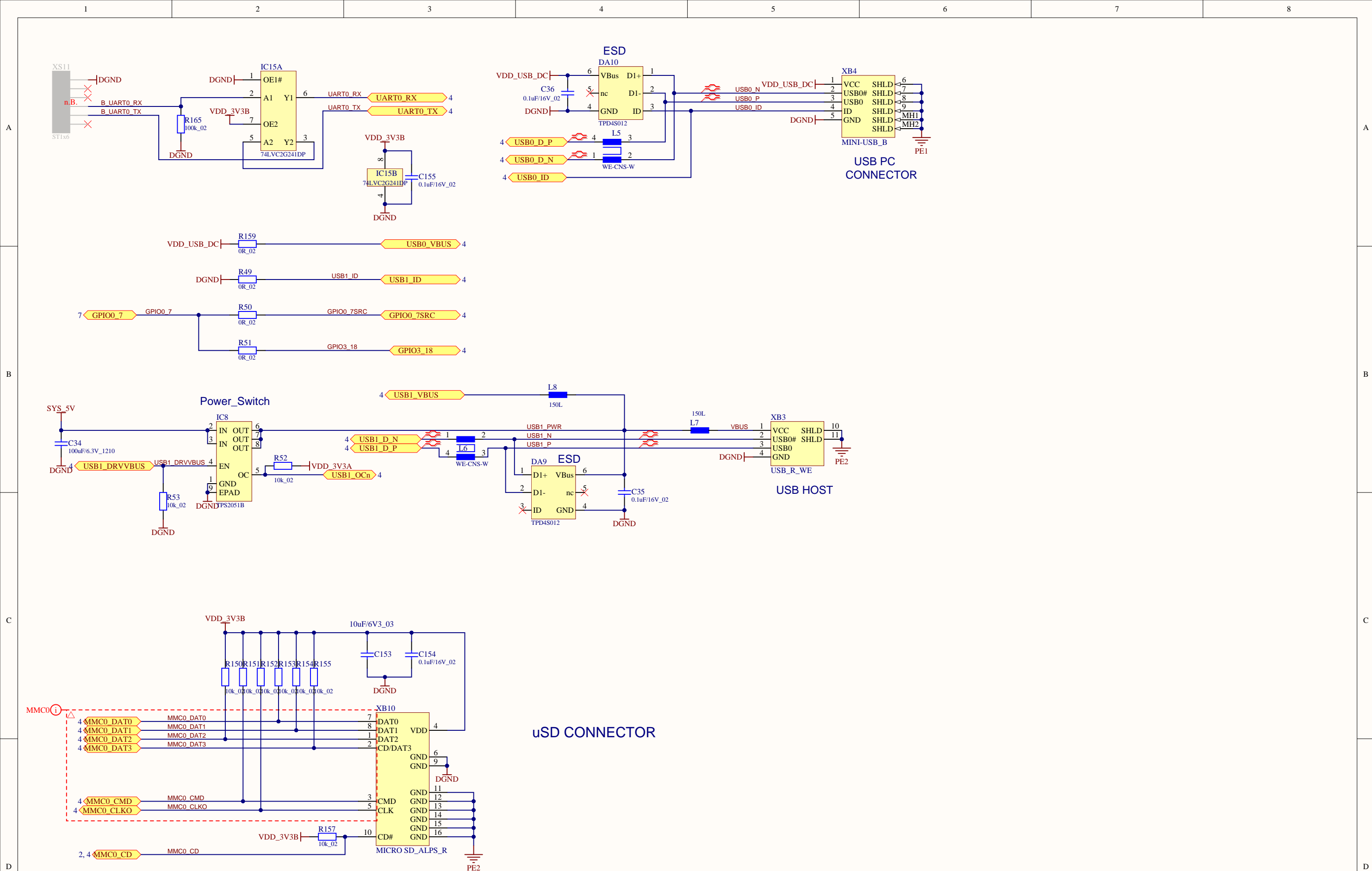


	Date	Name
editor:	24.11.2011	Mierau
CAD:	24.11.2011	Mierau
checked:	24.11.2011	Finke

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*BCS1\_BB116a*  
88645 - SL (3)

REV.:	2.3
LK :	6190202
sheet:	2
of:	8



	Date	Name
editor:	24.11.2011	Mierau
CAD:	24.11.2011	Mierau
checked:	24.11.2011	Finke

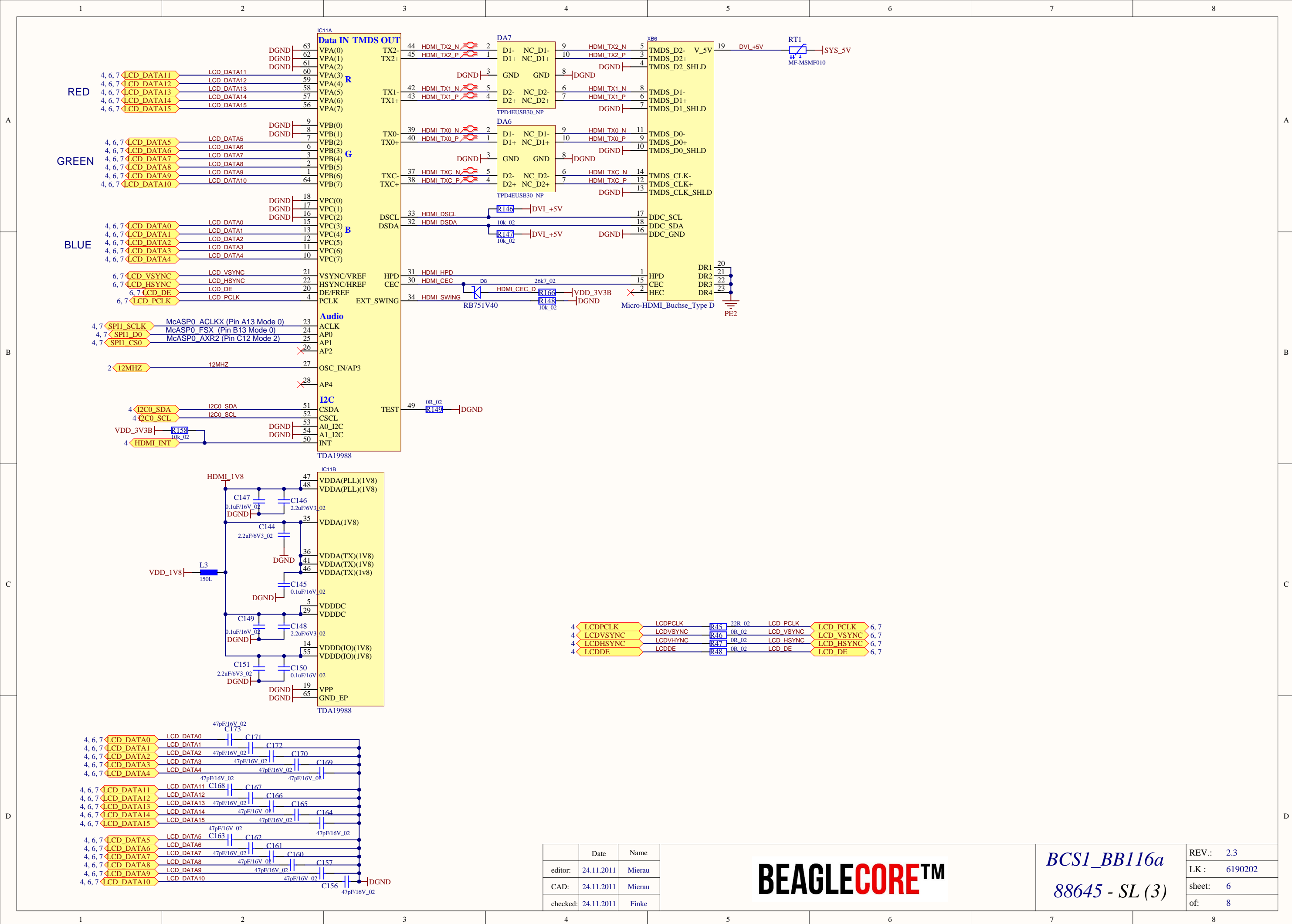
**BEAGLECORE™**

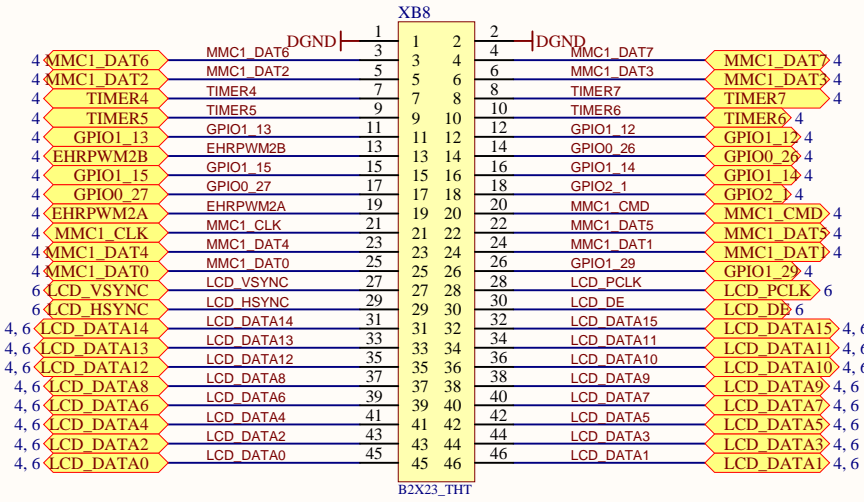
*BCS1\_BB116a*  
88645 - SL (3)

REV.:	2.3
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of:	8

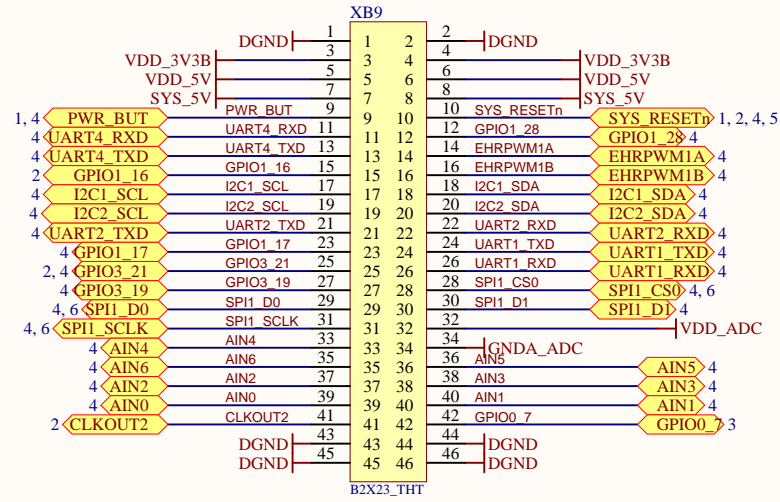








EXPANSION HEADER



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BCS1\_BB116a  
88645 - SL (3)

REV.:	2.3
LK :	6190202
sheet:	7
of:	8



