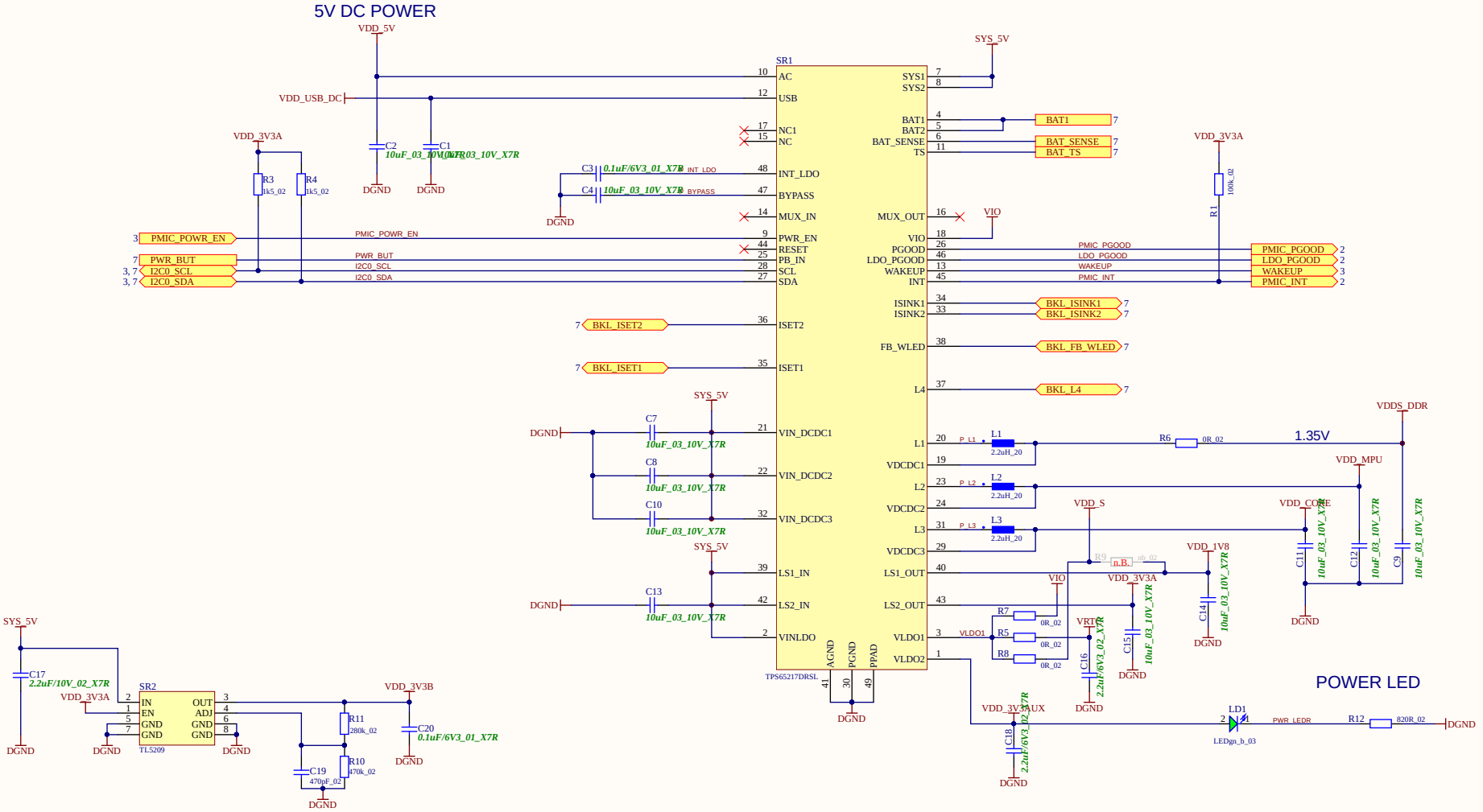


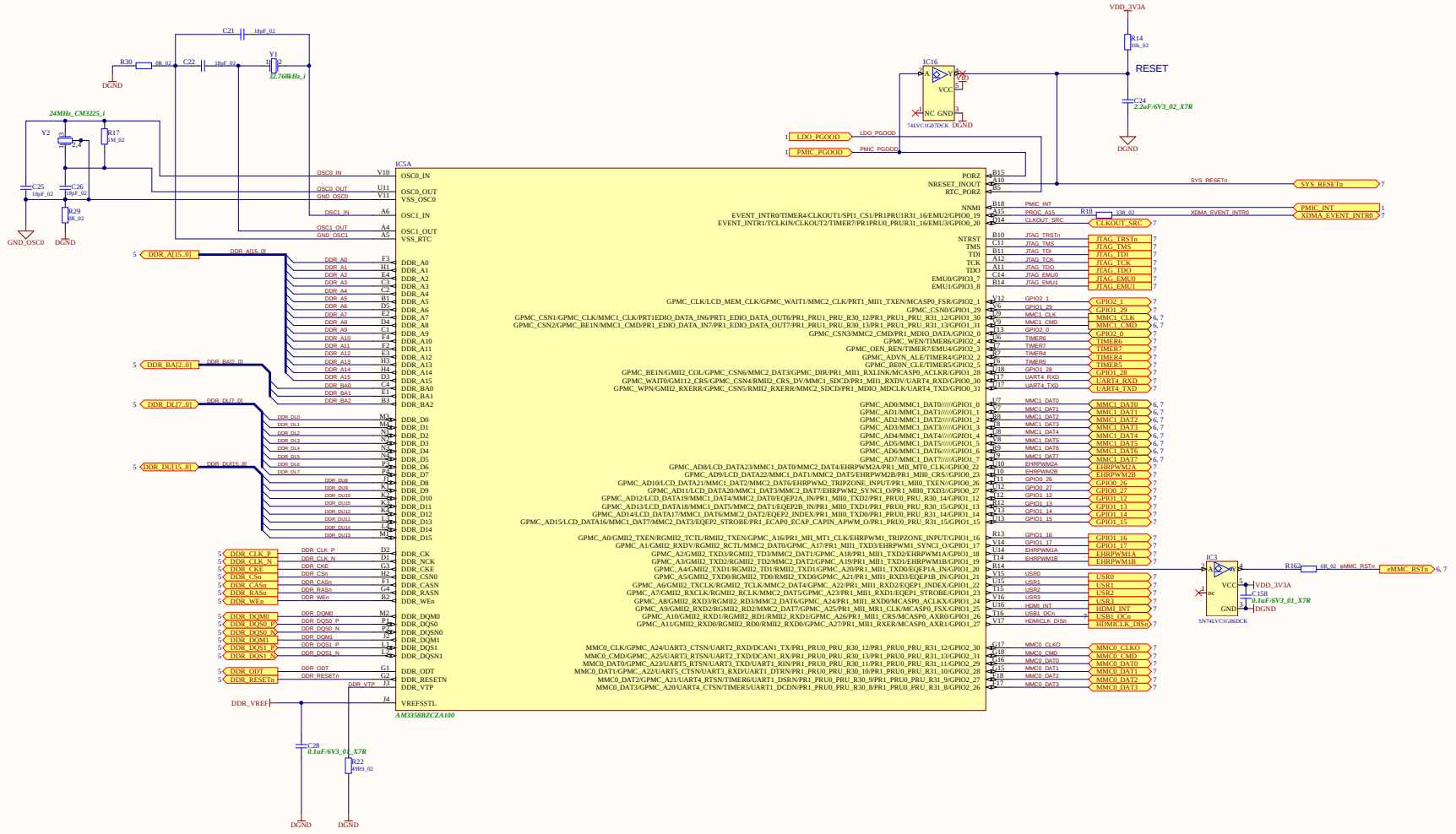
	Date	Name
editor:	24.11.2011	Mierau
CAD:	24.11.2011	Mierau
checked:	24.11.2011	Finke

BCMMODULE

BCM1 (CM001c)

88713 - SL (3)

REV.:	4.0
LK :	8083104
sheet:	1
of:	8



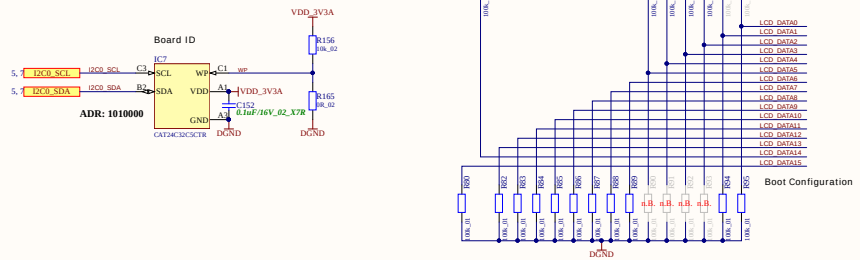
	Date	Name
editor:	17.02.2015	Mierau
CAD:	17.02.2015	Mierau
checked:	17.02.2015	Finke

BCMMODULE

BCM1 (CM001c)

88713 - SL (2)

REV.:	4.0
LK :	8083104
sheet:	2
of:	8



SYSBOOT[15:14]	SYSBOOT[13:12]	SYSBOOT[11:10]	SYSBOOT[9]	SYSBOOT[8]	SYSBOOT[7:6]	SYSBOOT[5]	SYSBOOT[4:0]	Boot Sequence			
00b = 19.2MHz 01b = 24MHz 10b = 25MHz 11b = 26MHz	00b (all other values reserved)	Don't care for ROM code	Don't care for ROM code	Don't care for ROM code	Don't care for ROM code	0 = CLKOUT1 disabled 1 = CLKOUT1 enabled	11100b	MMC1	MMC0	UART0	USB0[5]
00b = 19.2MHz 01b = 24MHz 10b = 25MHz 11b = 26MHz	00b (all other values reserved)	Don't care for ROM code	Don't care for ROM code	Don't care for ROM code	Don't care for ROM code	0 = CLKOUT1 disabled 1 = CLKOUT1 enabled	11000b	SPI0	MMC0	USB0[5] = 1	UART0

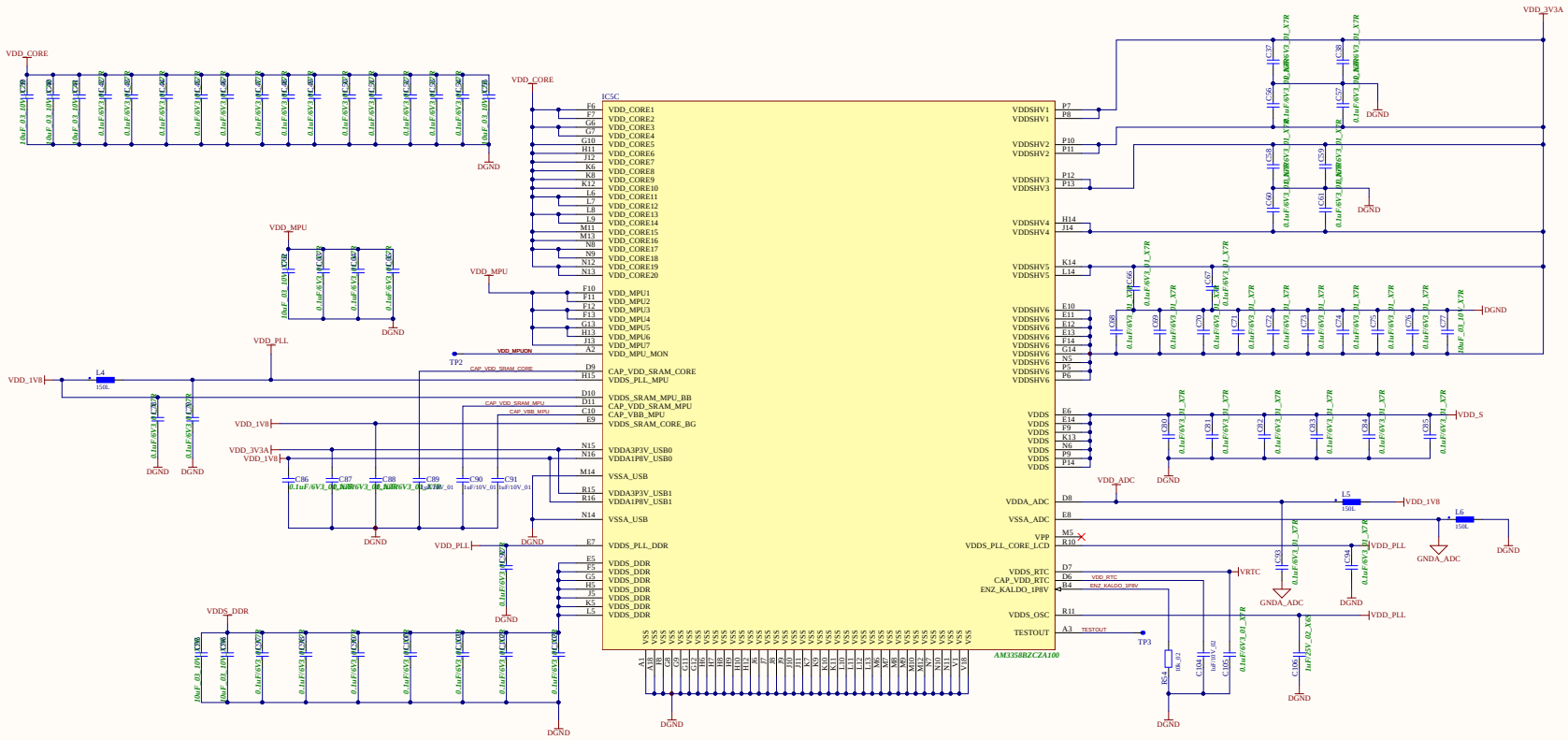
	Date	Name
editor:	17.02.2015	Mierau
CAD:	17.02.2015	Mierau
checked:	17.02.2015	Finke

BCMMODULE

BCM1 (CM001c)

88713 - SL (2)

REV.:	4.0
LK :	8083104
sheet:	3
of:	8



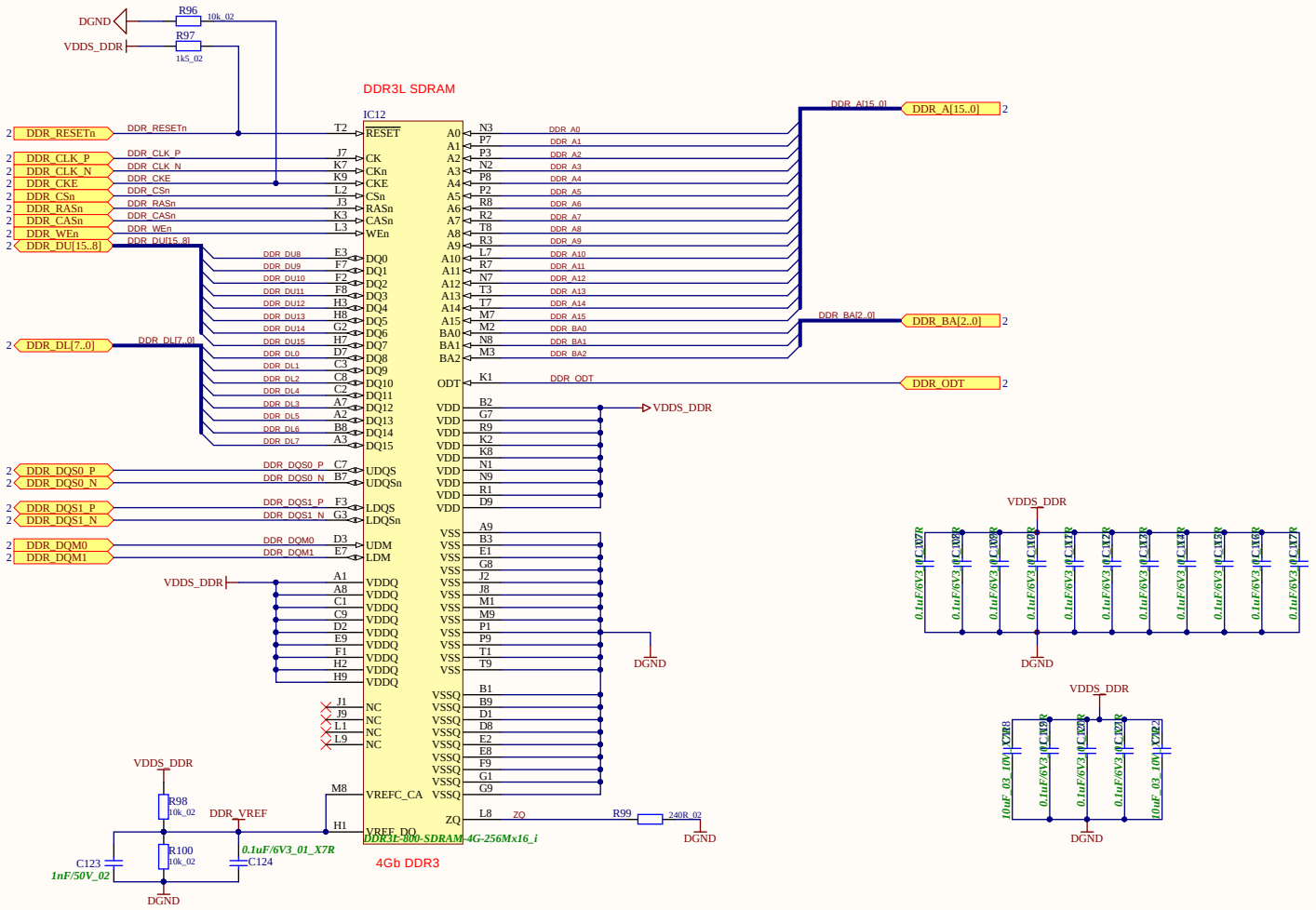
	Date	Name
editor:	17.02.2015	Mierau
CAD:	17.02.2015	Mierau
checked:	17.02.2015	Finke

BCMMODULE

BCM1 (CM001c)

88713 - SL (2)

REV.:	4.0
LK :	8083104
sheet:	4
of:	8



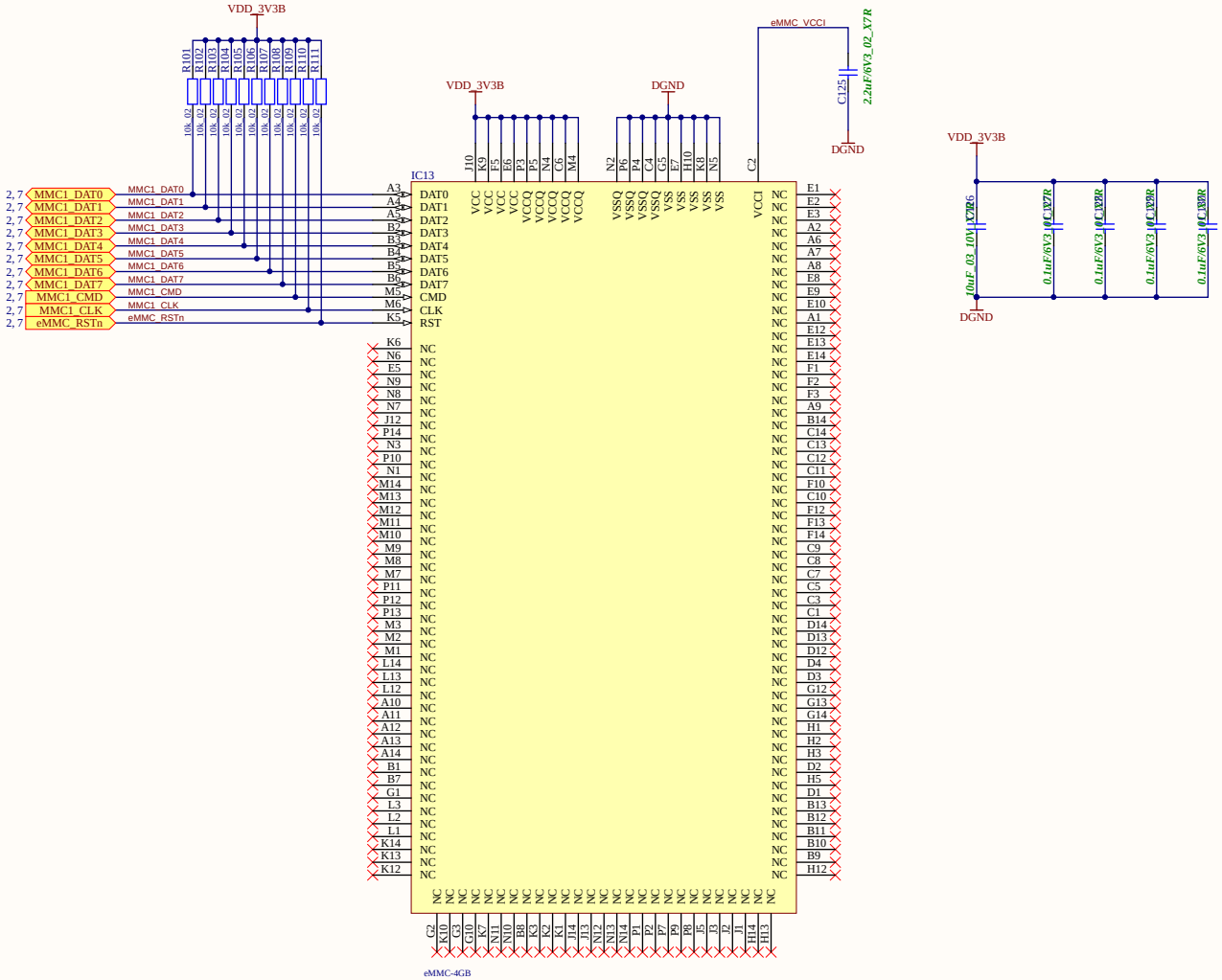
	Date	Name
editor:	24.11.2011	Mierau
CAD:	24.11.2011	Mierau
checked:	24.11.2011	Finke

BCMMODULE

BCM1 (CM001c)

88713 - SL (3)

REV.:	4.0
LK :	8083104
sheet:	5
of:	8



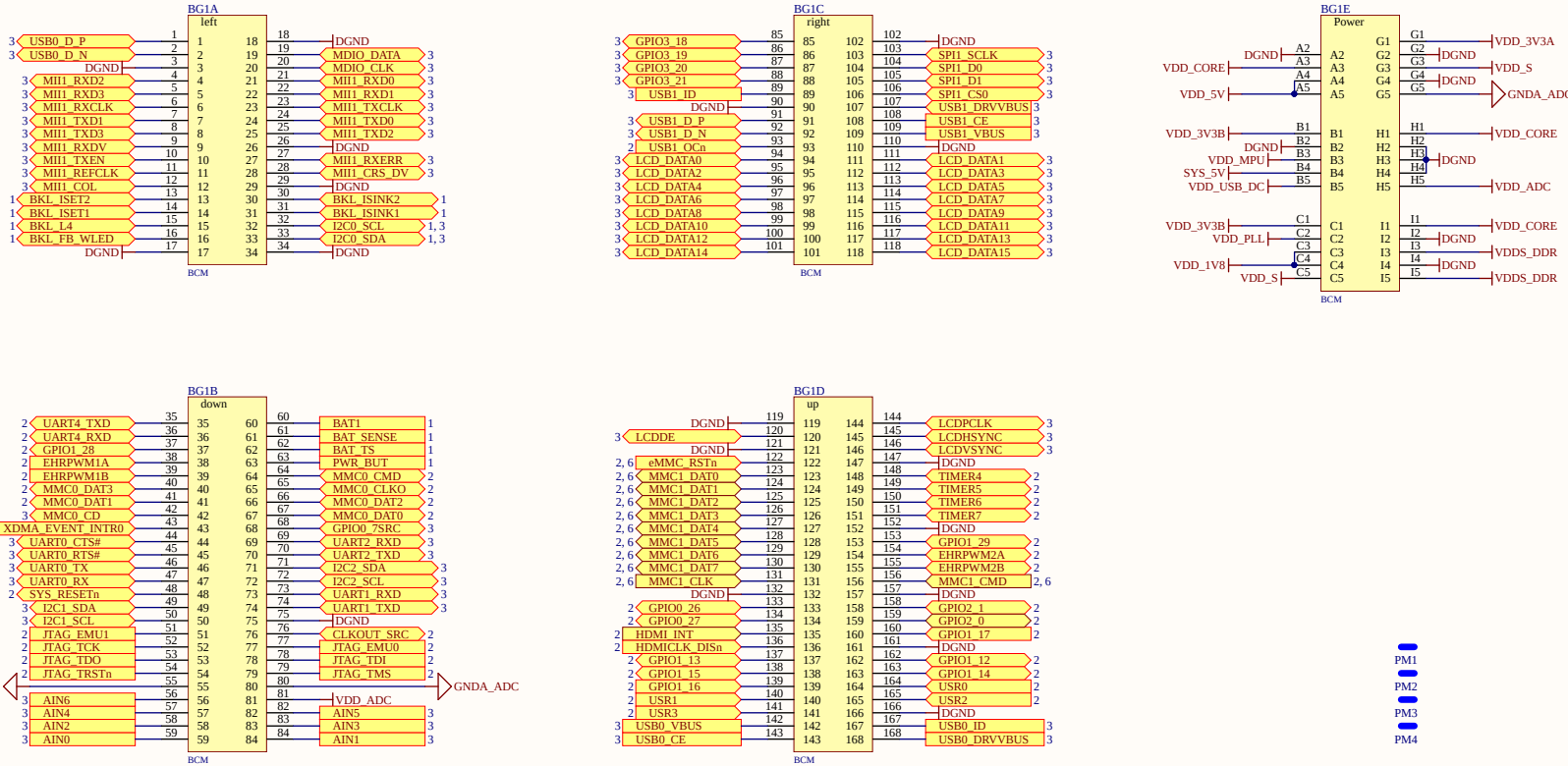
	Date	Name
editor:	24.11.2011	Mierau
CAD:	24.11.2011	Mierau
checked:	24.11.2011	Finke

BCMMODULE

BCM1 (CM001c)

88713 - SL (3)

REV.:	4.0
LK :	8083104
sheet:	6
of:	8



	Date	Name
editor:	24.11.2011	Mierau
CAD:	24.11.2011	Mierau
checked:	24.11.2011	Finke

BCMMODULE

BCM1 (CM001c)

88713 - SL (3)

REV.:	4.0
LK :	8083104
sheet:	7
of:	8

Revision History

REV. 1.0

Page	Component	Description	Date	By
All	All	New fom Design		

REV. 1.1

Page	Component	Description	Date	By
2	IC5	change to AM3358BZCZ100	07.10.2015	Fin
6	IC13	alternatively added: KE4CN2H5A-A58	07.10.2015	Fin

REV. 1.2

Page	Component	Description	Date	By
1	SR1	change to TPS65217DRSL	28.01.2016	Fin
5	IC12	alternatively added: IM4G16D3FABG-125 or NT5CC256M16DP-DI	28.01.2016	Fin
6	IC13	alternatively added: H26M31001HPR or H26M41204HPR	28.01.2016	Fin

REV. 2.0

Page	Component	Description	Date	By
1	SR1	change to TPS65217DRSL	18.02.2016	Fin
	C1, C2, C7, C8, C10, C13	change to 0603 / 10V	19.01.2016	Mie
3	R57, R65 ... R70, R80, R82 ... R95	added	19.01.2016	Mie
	IC7, R156, R165, C152	added	19.01.2016	Mie

REV. 2.1

Page	Component	Description	Date	By
all	text	change german to english	08.03.2016	Fin
1	SR1	change TPS65217D back to TPS65217C on CM001a	09.03.2016	Fin
	SR1	TPS65217D rest on CM001b (is not yet supported)	09.03.2016	Fin
5	IC12	IM4G16D3EABG-125 or IM4G16D3FABG-125 or NT5CB256M16DP-DI on CM001a	09.03.2016	Fin
	IC12	IM4G16D3FABG-125 or NT5CC256M16DP-DI on CM001b	09.03.2016	Fin

REV. 2.2

Page	Component	Description	Date	By
3	IC7	change to CAT24C32C5CTR	06.06.2016	Mie
	R90 ... R93	do not implement	19.07.2017	Fin

REV. 3.0

Page	Component	Description	Date	By
-	Layout	pcb panel break off tabs added to corners	14.05.2018	Mie

REV. 3.1

Page	Component	Description	Date	By
-	components	all components up to date	23.05.2018	Fin

REV. 4.0

Page	Component	Description	Date	By
5	IC12	connected PIN H1 to DDR_VREF again	31.08.2018	Mie

	Date	Name
editor:	24.11.2011	Mierau
CAD:	24.11.2011	Mierau
checked:	24.11.2011	Finke

BCMMODULE

BCM1 (CM001c)

88713 - SL (3)

REV.:	4.0
LK :	8083104
sheet:	8
of:	8