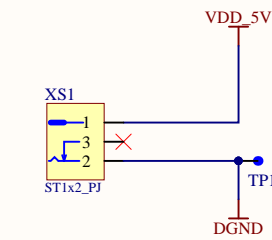
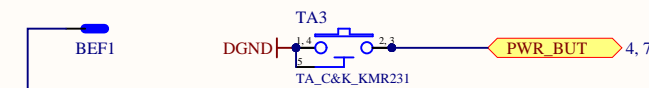


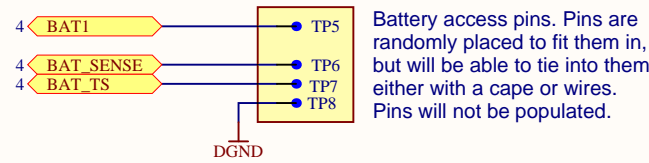
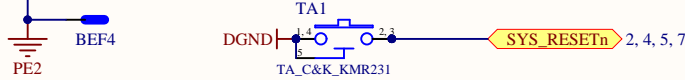
5V DC POWER



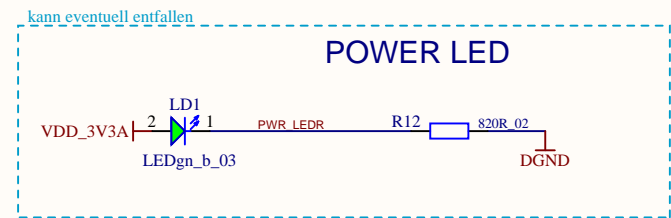
Power



RESET



Battery access pins. Pins are randomly placed to fit them in, but will be able to tie into them either with a cape or wires. Pins will not be populated.



ESD  
ESD1

ESD  
ESD2



uSD BOOT

BOOT ORDER....MMC1....MMC0...UART0....USB0.....24MHZ

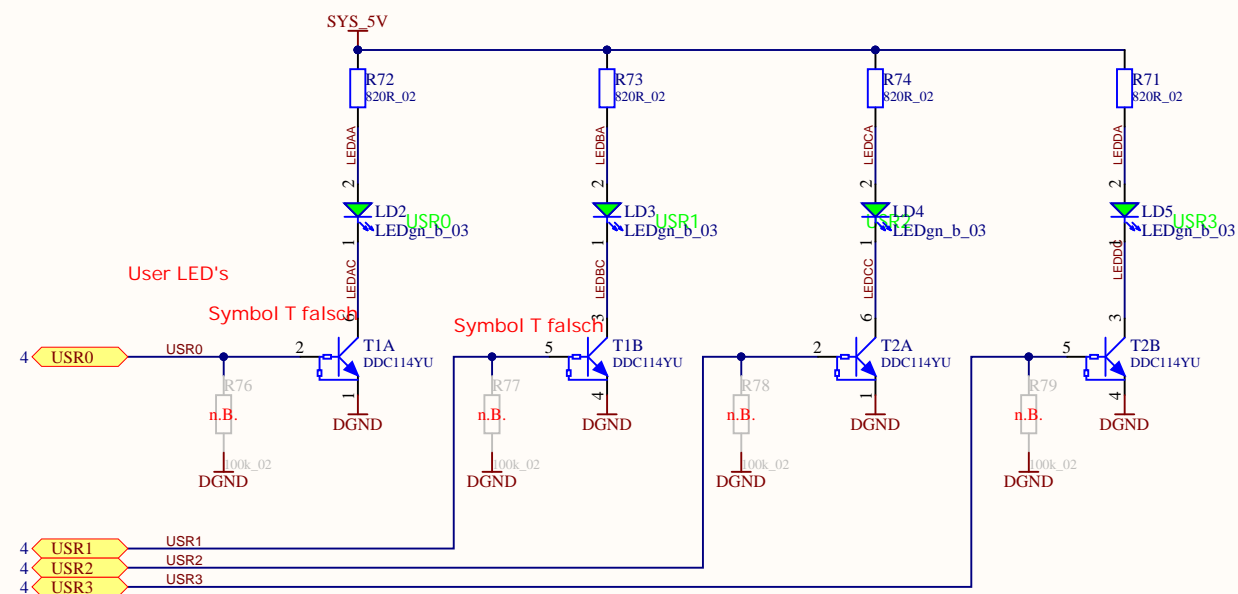
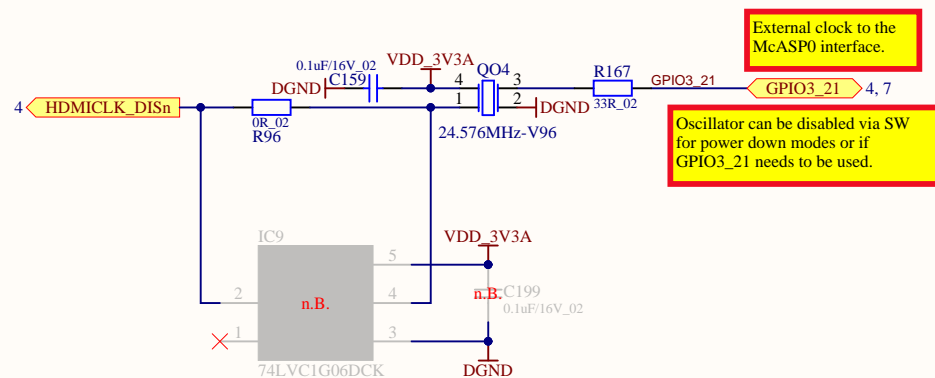
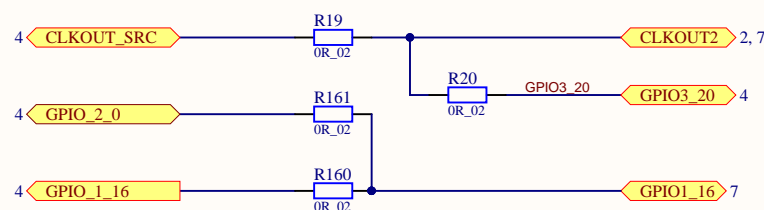
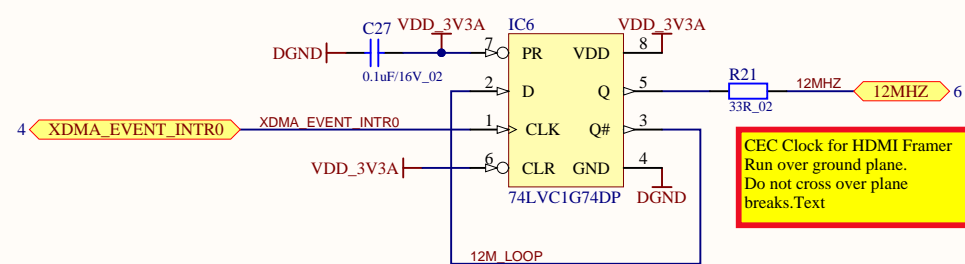
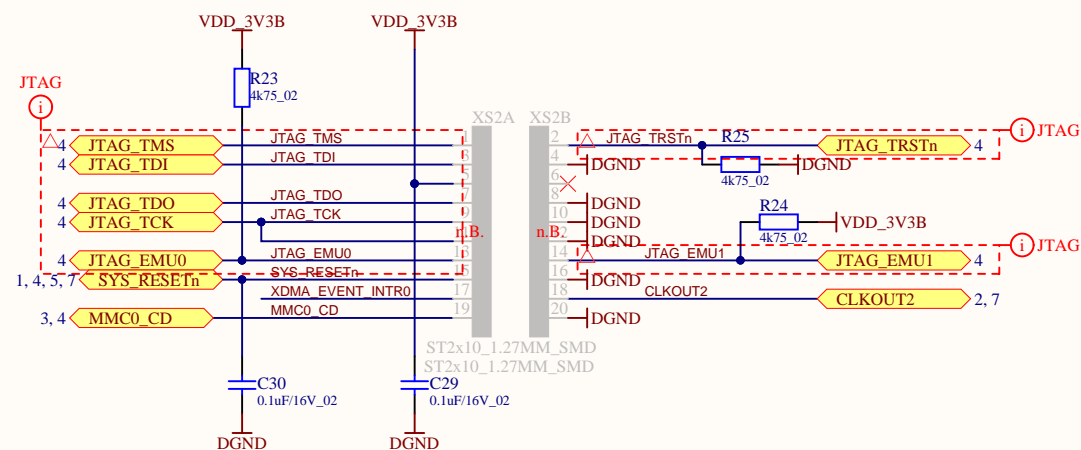
SYSBOOT[15:14]	SYSBOOT[13:12]	SYSBOOT[11:10]	SYSBOOT[9]	SYSBOOT[8]	SYSBOOT[7:6]	SYSBOOT[5]	SYSBOOT[4:0]	Boot Sequence			
00b = 19.2MHz 01b = 24MHz 10b = 25MHz 11b = 26MHz	00b (all other values reserved)	Don't care for ROM code	Don't care for ROM code	Don't care for ROM code	Don't care for ROM code	0 = CLKOUT1 disabled 1 = CLKOUT1 enabled	11100b	MMC1	MMC0	UART0	USB0[5]
00b = 19.2MHz 01b = 24MHz 10b = 25MHz 11b = 26MHz	00b (all other values reserved)	Don't care for ROM code	Don't care for ROM code	Don't care for ROM code	Don't care for ROM code	0 = CLKOUT1 disabled 1 = CLKOUT1 enabled	11000b	SPI0	MMC0	USB0[5] 1	UART0

	Datum	Name
Bearb.	10.12.2015	Salmen
CAD:	10.12.2015	Salmen
Prüfer:	10.12.2015	Finke

BEAGLECORE™

BB116a  
88645 - SL (3)

REV.: 2.0  
LK : 6190202  
Blatt-Nr: \*  
von: 8

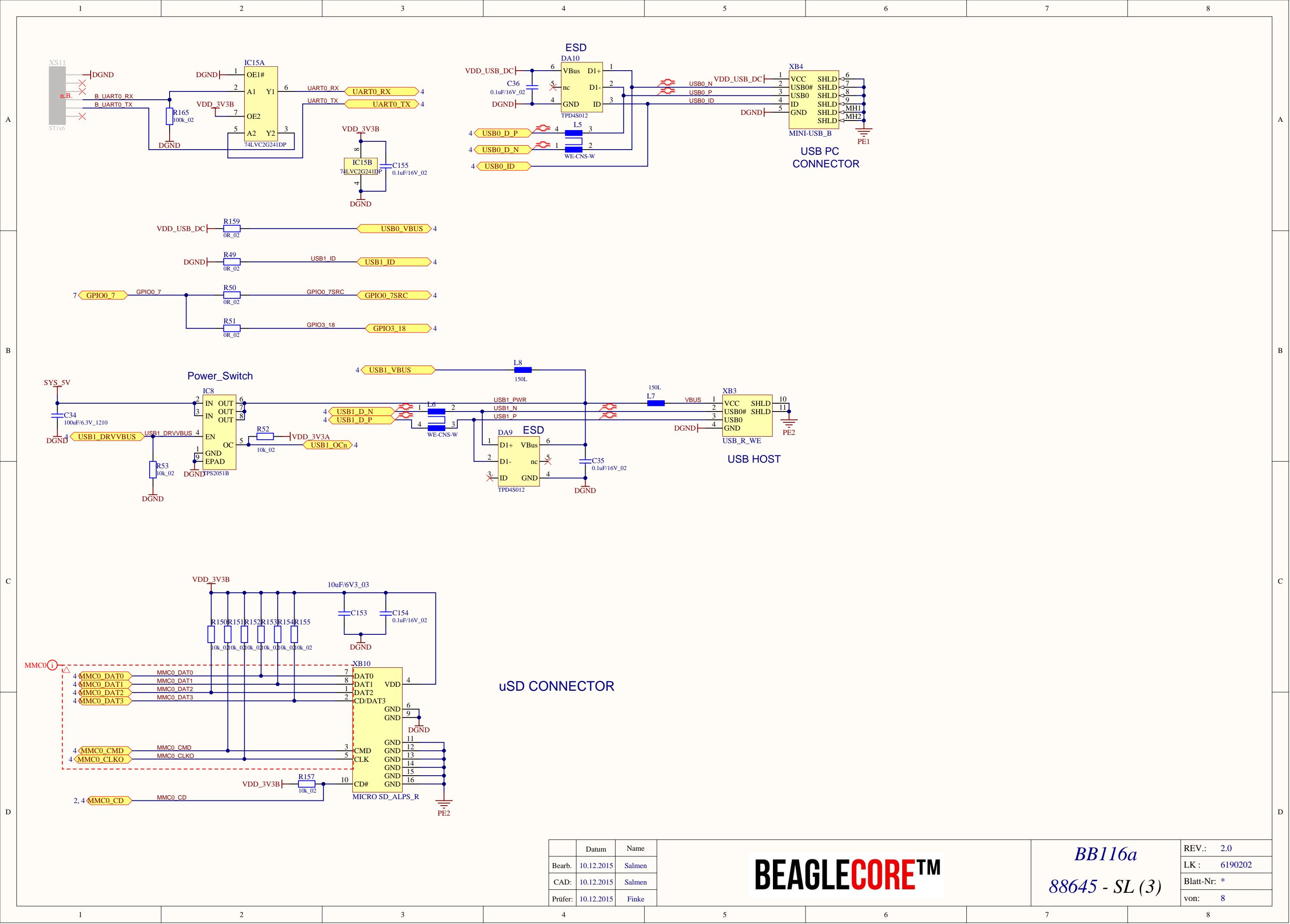


	Datum	Name
Bearb.	10.12.2015	Salmen
CAD:	10.12.2015	Salmen
Prüfer:	10.12.2015	Finke

**BEAGLECORE™**

*BB116a*  
*88645 - SL (3)*

REV.:	2.0
LK :	6190202
Blatt-Nr:	*
von:	8

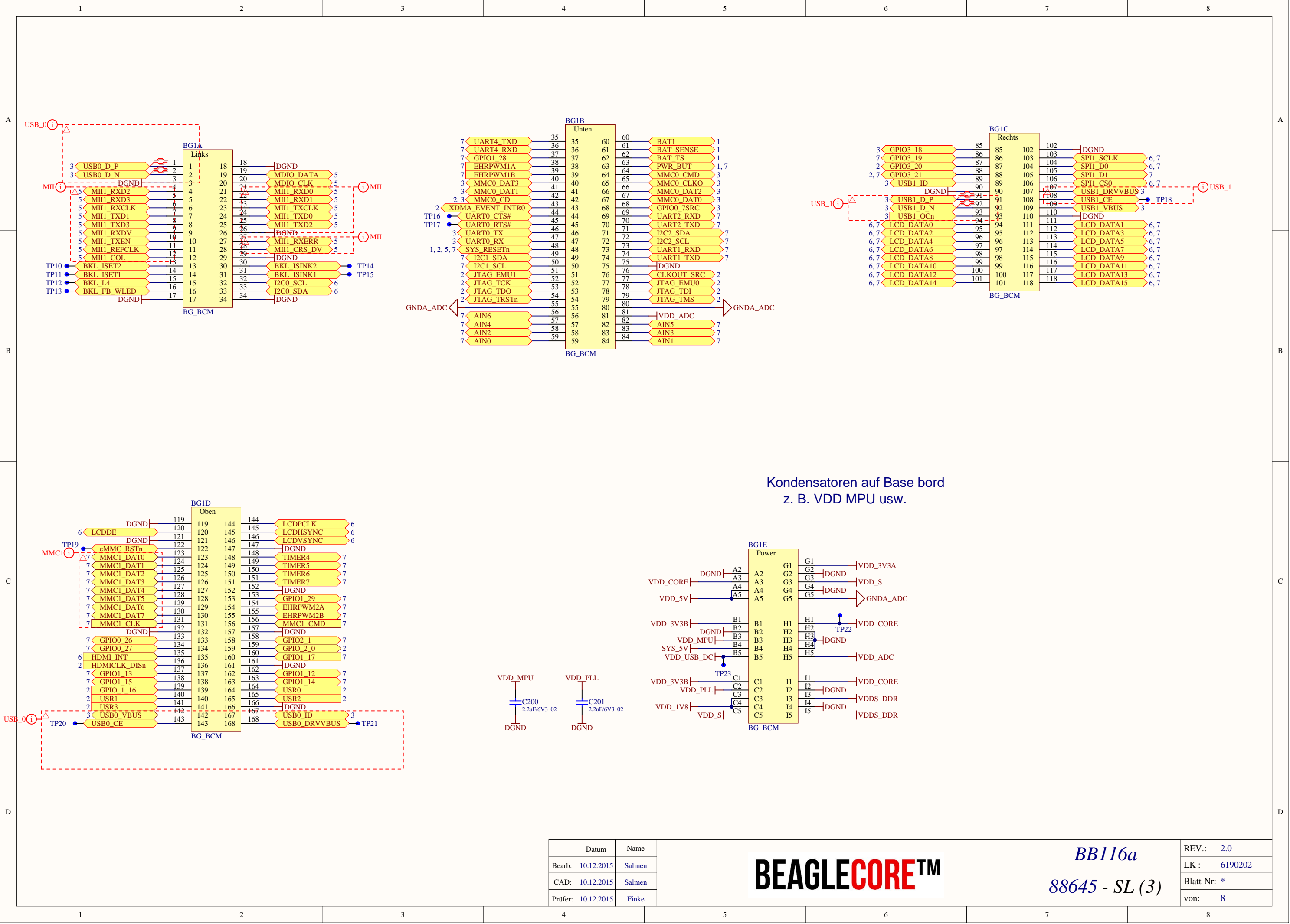


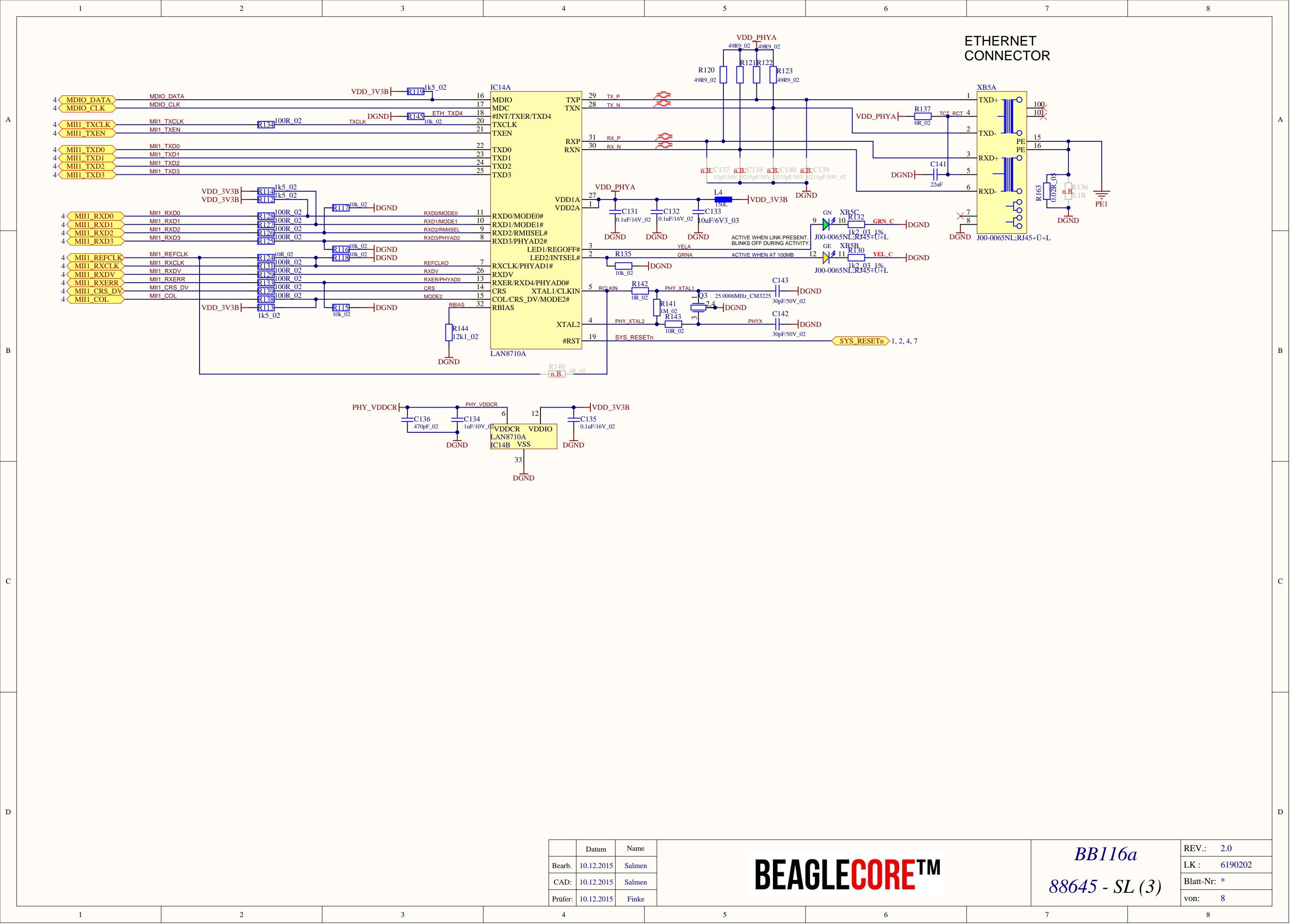
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Bearb.	10.12.2015	Salmen
CAD:	10.12.2015	Salmen
Prüfer:	10.12.2015	Finke

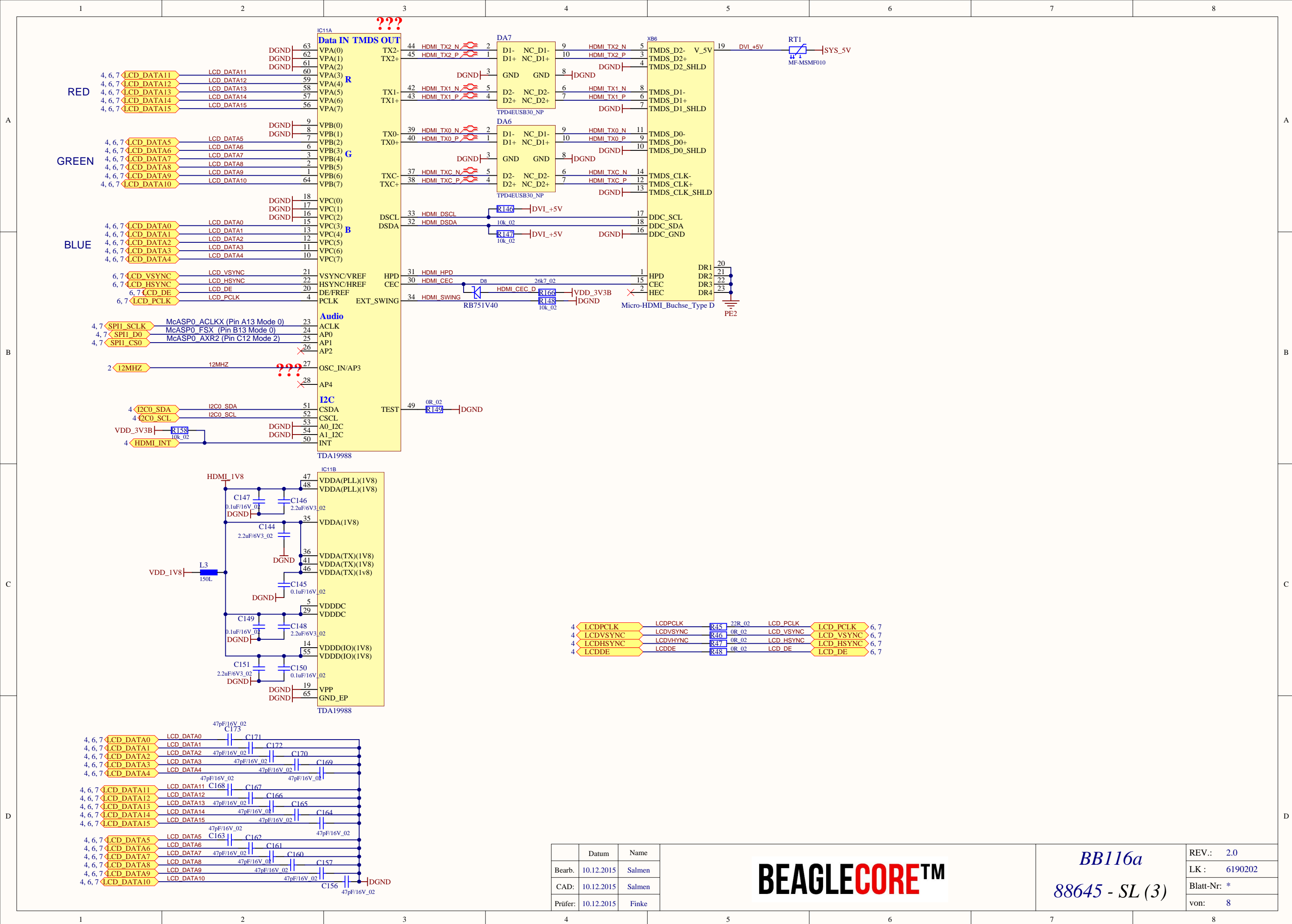
BEAGLECORE™

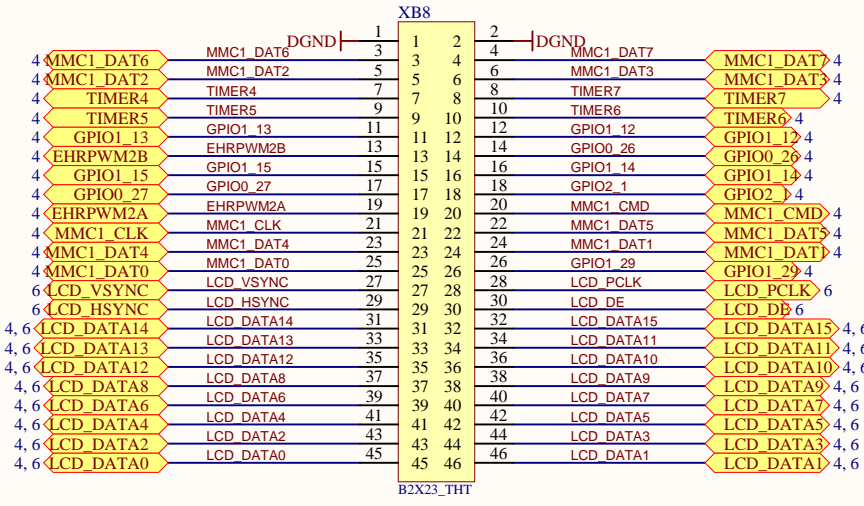
BB116a  
88645 - SL (3)

REV.: 2.0  
LK : 6190202  
Blatt-Nr: \*  
von: 8

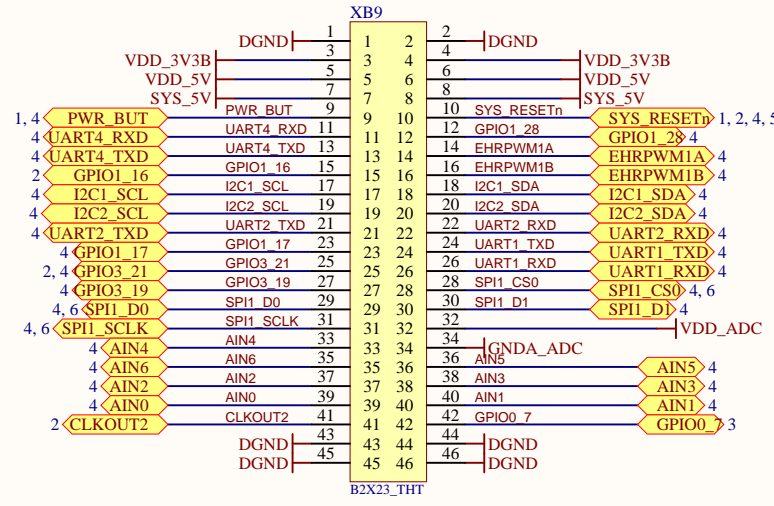








EXPANSION HEADER



EXPANSION HEADER

	Datum	Name
Bearb.	10.12.2015	Salmen
CAD:	10.12.2015	Salmen
Prüfer:	10.12.2015	Finke

BEAGLECORE™

BB116a  
88645 - SL (3)

REV.: 2.0  
LK : 6190202  
Blatt-Nr: \*  
von: 8



