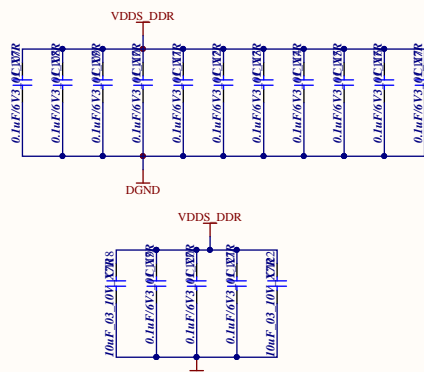


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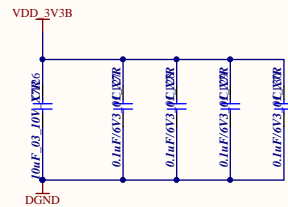
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BCM1 (CM001c)
88713 - SL (2)

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A	<div>Revision History</div> <div>REV. 1.0</div> <table><tr><th>Page</th><th>Component</th><th>Description</th><th>Date</th><th>By</th></tr><tr><td>All</td><td>All</td><td>New fom Design</td><td></td><td></td></tr></table> <div>REV. 1.1</div> <table><tr><th>Page</th><th>Component</th><th>Description</th><th>Date</th><th>By</th></tr><tr><td>2</td><td>IC5</td><td>change to AM3358BZCZ100</td><td>07.10.2015</td><td>Fin</td></tr><tr><td>6</td><td>IC13</td><td>alternatively added: KE4CN2H5A-A58</td><td>07.10.2015</td><td>Fin</td></tr></table> <div>REV. 1.2</div> <table><tr><th>Page</th><th>Component</th><th>Description</th><th>Date</th><th>By</th></tr><tr><td>1</td><td>SR1</td><td>change to TPS65217DRSL</td><td>28.01.2016</td><td>Fin</td></tr><tr><td>5</td><td>IC12</td><td>alternatively added: IM4G16D3FABG-125 or NT5CC256M16DP-DI</td><td>28.01.2016</td><td>Fin</td></tr><tr><td>6</td><td>IC13</td><td>alternatively added: H26M31001HPR or H26M41204HPR</td><td>28.01.2016</td><td>Fin</td></tr></table> <div>REV. 2.0</div> <table><tr><th>Page</th><th>Component</th><th>Description</th><th>Date</th><th>By</th></tr><tr><td>1</td><td>SR1 C1, C2, C7, C8, C10, C13</td><td>change to TPS65217DRSL change to 0603 / 10V</td><td>18.02.2016 19.01.2016</td><td>Fin Mie</td></tr><tr><td>3</td><td>R57, R65 ... R70, R80, R82 ... R95 IC7, R156, R165, C152</td><td>added added</td><td>19.01.2016 19.01.2016</td><td>Mie Mie</td></tr></table> <div>REV. 2.1</div> <table><tr><th>Page</th><th>Component</th><th>Description</th><th>Date</th><th>By</th></tr><tr><td>all</td><td>text</td><td>change german to english</td><td>08.03.2016</td><td>Fin</td></tr><tr><td>1</td><td>SR1</td><td>change TPS65217D back to TPS65217C on CM001a</td><td>09.03.2016</td><td>Fin</td></tr><tr><td></td><td>SR1</td><td>TPS65217D rest on CM001b (is not yet supported)</td><td>09.03.2016</td><td>Fin</td></tr><tr><td>5</td><td>IC12</td><td>IM4G16D3EABG-125 or IM4G16D3FABG-125 or NT5CB256M16DP-DI on CM001a</td><td>09.03.2016</td><td>Fin</td></tr><tr><td></td><td>IC12</td><td>IM4G16D3FABG-125 or NT5CC256M16DP-DI on CM001b</td><td>09.03.2016</td><td>Fin</td></tr></table> <div>REV. 2.2</div> <table><tr><th>Page</th><th>Component</th><th>Description</th><th>Date</th><th>By</th></tr><tr><td>3</td><td>IC7 R90 ... R93</td><td>change to CAT24C32C5CTR do not implement</td><td>06.06.2016 19.07.2017</td><td>Mie Fin</td></tr></table> <div>REV. 3.0</div> <table><tr><th>Page</th><th>Component</th><th>Description</th><th>Date</th><th>By</th></tr><tr><td>-</td><td>Layout</td><td>pcb panel break off tabs added to corners</td><td>14.05.2018</td><td>Mie</td></tr></table> <div>REV. 3.1</div> <table><tr><th>Page</th><th>Component</th><th>Description</th><th>Date</th><th>By</th></tr><tr><td>-</td><td>components</td><td>all components up to date</td><td>23.05.2018</td><td>Fin</td></tr></table> <div>REV. 4.0</div> <table><tr><th>Page</th><th>Component</th><th>Description</th><th>Date</th><th>By</th></tr><tr><td>5</td><td>IC13</td><td>connected PIN H1 to DDR_VREF again</td><td>31.08.2018</td><td>Mie</td></tr></table>								Page	Component	Description	Date	By	All	All	New fom Design			Page	Component	Description	Date	By	2	IC5	change to AM3358BZCZ100	07.10.2015	Fin	6	IC13	alternatively added: KE4CN2H5A-A58	07.10.2015	Fin	Page	Component	Description	Date	By	1	SR1	change to TPS65217DRSL	28.01.2016	Fin	5	IC12	alternatively added: IM4G16D3FABG-125 or NT5CC256M16DP-DI	28.01.2016	Fin	6	IC13	alternatively added: H26M31001HPR or H26M41204HPR	28.01.2016	Fin	Page	Component	Description	Date	By	1	SR1 C1, C2, C7, C8, C10, C13	change to TPS65217DRSL change to 0603 / 10V	18.02.2016 19.01.2016	Fin Mie	3	R57, R65 ... R70, R80, R82 ... R95 IC7, R156, R165, C152	added added	19.01.2016 19.01.2016	Mie Mie	Page	Component	Description	Date	By	all	text	change german to english	08.03.2016	Fin	1	SR1	change TPS65217D back to TPS65217C on CM001a	09.03.2016	Fin		SR1	TPS65217D rest on CM001b (is not yet supported)	09.03.2016	Fin	5	IC12	IM4G16D3EABG-125 or IM4G16D3FABG-125 or NT5CB256M16DP-DI on CM001a	09.03.2016	Fin		IC12	IM4G16D3FABG-125 or NT5CC256M16DP-DI on CM001b	09.03.2016	Fin	Page	Component	Description	Date	By	3	IC7 R90 ... R93	change to CAT24C32C5CTR do not implement	06.06.2016 19.07.2017	Mie Fin	Page	Component	Description	Date	By	-	Layout	pcb panel break off tabs added to corners	14.05.2018	Mie	Page	Component	Description	Date	By	-	components	all components up to date	23.05.2018	Fin	Page	Component	Description	Date	By	5	IC13	connected PIN H1 to DDR_VREF again	31.08.2018	Mie	
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