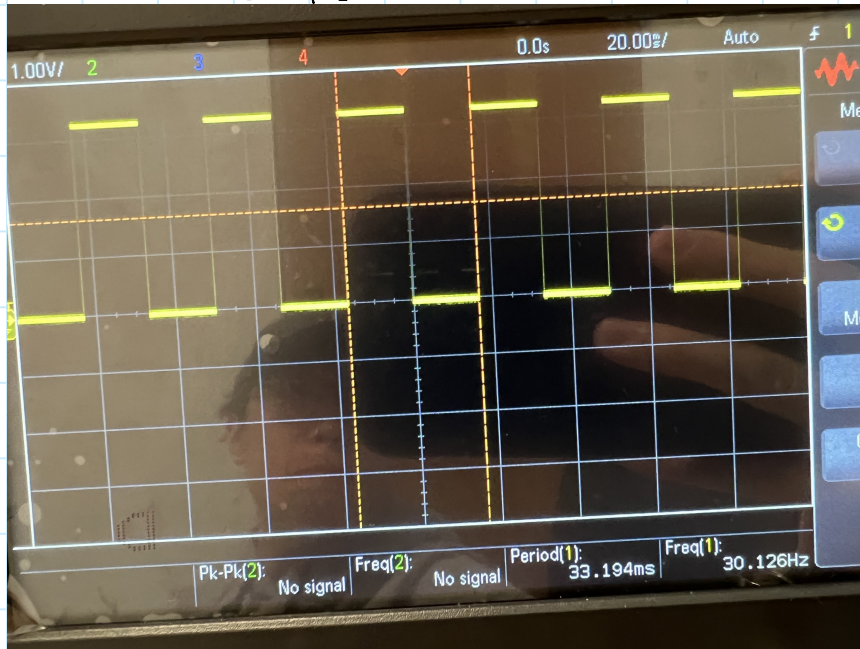


Timer Follow Up

Thursday, January 19, 2023 5:08 PM

30 Hz from scope



$$\begin{aligned}\text{Clock frequency} &= 100000 (30) \\ &= 3000000 \text{ Hz}\end{aligned}$$

clock frequency 3 MHz 2000000 cycles per second

1000000 micro in one second

∴

3 cycles in one microsecond

```
int increments[] = {3000,300,2700,24000,3000,300,300,26400,3000,300,2700,24000};  
int i = 0;
```

```
// Configure GPIO
```

```
P1->DIR |= BIT0;
```

```
P1->OUT |= BIT0;
```

```
P6->DIR |= BIT2;
```

```
P6->OUT |= BIT2;
```

```
P10->DIR |= BIT0 | BIT2;
```

```
P10->OUT = BIT0 | BIT2;
```

```

5 void TA0_0_IRQHandler(void) {
5     TIMER_A0->CCTL[0] &= ~TIMER_A_CCTLN_CCIFG;
7     P1->OUT ^= BIT0;
3     if(i > 11){
9         i=0;
9     }
1     switch(i){
2         case 0:
3             P6->OUT |= BIT2;
4             break;
5         case 1:
6             //adc
7             break;
8         case 2:
9             P6->OUT &= ~BIT2;
9             break;
1            case 3:
2                //adc
3                break;
4            case 4:
5                P10->OUT |= BIT0;
6                break;
7            case 5:
8                //adc
9                break;
10           case 6:
11               P10->OUT &= ~BIT0;
12               break;
13           case 7:
14               //adc
15               break;
16           case 8:
17               P10->OUT |= BIT2;
18               break;
19           case 9:
20               //adc
21               break;
22           case 10:
23               P10->OUT &= ~BIT2;
24               break;
25           case 11:
26               //adc
27               break;
28       }
29       TIMER_A0->CCR[0] += increments[i];
30       i++;
31   }

```

