

Memristor Presentation

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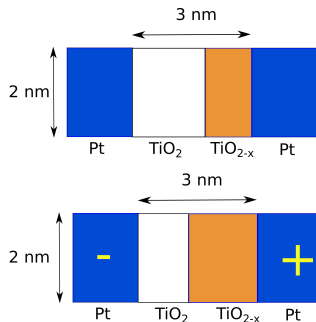
November 2 2015

Outline

- 1 Introduction
- 2 Background
 - Memristor
 - Crossbar Array
- 3 Computation in Memory
 - Basic Design and IMP Logic
 - Results on Large Data Sets
- 4 Read/Write Models for a Memristor Based 1T1R Cell
- 5 Conclusion

The Memristor

- Two-terminal, non-volatile device
- Made of resistant TiO_2 and conductive TiO_{2-x}
- Applying voltage alters the state



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Discussion

Questions?