RISC16 Simulator Debugging

January 14, 2016

1 Bugs

☐ In the sequential version, writting instructions in the program memory is not enough. It must first be saved in ROM, then imported back and only then can be executed.

→ See what happens when we try to run the simulation. Where does it take the information from ?

→ See what happens when we import the ROM. Is it loaded somewhere?

☑ If there are multiple comment symbols after an instruction, the whole line is ignored all together.

→ e.g. When importing `addi 1,1,0//double//comment`, the line does not appear in the program memory.

→ The problem probably comes from the comment parsing when importing the file in `MemProg::fileopen()`.

2 Performance improvements

☐ When loading a ROM, the file is opened twice in a row.

→ See `Memprog::fileopen(String path)`.