

## Calculation of Z-measurement:

assumption:

$$0 \leq ADC_1 \leq 4096 \quad \text{idle state: 4096}$$

$$0 \leq ADC_2 \leq 4096 \quad \text{idle state: 0}$$

$$\text{annotation: } 12 \text{ bit} = 2^{12} = 4096$$

$$Z_1 \propto ADC_1$$

$$Z_2 \propto ADC_2$$

$$Z_1 = 4096 - R_t + R_x$$

$$Z_2 = R_x$$

linear equations:

$$4096 - R_t + R_x = ADC_1$$

$$R_x = ADC_2$$

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subtract

$$4096 - R_t = ADC_1 + ADC_2$$

$$R_t = 4096 - ADC_1 - ADC_2$$