

ATK-ESP8266-V1.3 WIFI模块原

The schematic diagram illustrates the internal components and connections of the ATK-ESP8266-V1.3 module. The central component is the ESP8266 chip (U2), which is connected to a 6-pin header (P1) for power and data. The header pins are connected to VCC, GND, TXD_TTL, RXD_TTL, REST, and GPIO_0. The module also features a voltage regulator (U1, RT9193-33) that takes input from the header's VCC pin and provides a regulated 3.3V output to the ESP8266's VCC pin. The ESP8266's TXD pin is connected to a TXD_TTL pin, which is further connected to a TXD pin of the MBR0520 module. The RXD pin of the MBR0520 module is connected to an RXD_TTL pin, which is connected to the RXD pin of the ESP8266. The module includes several other components: a 1K resistor (R1) connected to GPIO_0, a 1K resistor (R4) connected to GPIO_15, a 5.6K resistor (R2) connected to VCC, a 5.6K resistor (R3) connected to VCC, a 1K resistor (R5) connected to VCC, a 1K resistor (R6) connected to VCC, a 510 resistor (R7) connected to VCC, a 1uF capacitor (C1) connected to GND, a 223 capacitor (C2) connected to GND, a 1uF capacitor (C3) connected to GND, and a 104 capacitor (C4) connected to GND. The module also includes a PWR LED connected to the VCC pin and a REST pin connected to the REST pin of the ESP8266. The module is powered by a 3.3V supply (VCC3.3) and has a GND connection.

Title		
Size A4	Number	Revision
Date: File:	2018/10/29 E:\开发文档\ESP8266.SchDoc	Sheet of Drawn By:

