

ECM-20/TRC-20 Status Display Definitions

An Application Note www.arium.com

Overview

The LED window on the front of an ECM-20 or TRC-20 gives the user basic information regarding system status. The decimals indicate target power status. The display codes provides failure information. Specifics are described below.

Decimals

As you face the LED screen, you see four decimals beneath a code. Both the far left and left center decimals are updated every 100 ms. The decimals indicate:

- Far left decimal if illuminated, target power is on
- Left center decimal –if illuminated, target has been reset
- Right center decimal blinks if no target power detected or target is being reset during target acquisition
- Far right decimal (not used)

Display Codes

Codes 00 through 0F are reserved for secondary failure information codes.

10	Start invalidate of cached assets
11	Start minimal save state
12	Start additional (full) save state; ECX,EDX,EAX,EIP
13	Submitting first instruction to processor
14	First instruction complete
15	Start save & modify segment registers
16	Start save of general and other assets
17	Start clear of Debug regs & Debug Mode control MSRs
18	End of acquire CPU
19	- Not Used -
1A	Start of minimal restore state
1B	- Not Used -
1C	- Not Used -
1D	- Not Used -

1E	End of release CPU
1F	
15	End of release target
20	Start of erase flash boot segment
21	Start of erase flash code 1 segment
22	Ţ ,
	Start of crase flash code 2 segment
23	Start of erase flash config data 1 segment
24	Start of erase flash config data 2 segment
25	Start of erase flash log 1 segment
26	Start of erase flash log 2 segment
27	Start of erase flash protected config segment
28	Start of loading default protected config data
29	Start of writing protected config data segment
2A	Start of loading config data
2B	Start of writing config data 2 segment
2C	Start of writing config data 1 segment
2D	End of flash initialization
2E	Flash memory component failure
2F	Attempt to reflash with invalid (pre 3.10.00) image
30	Target JTAG chain topology invalid (TGI w/CPUs + ITP w/CPUs)
31	JTAG shifter starting with a bong command
32	- Not Used -
33	Target JTAG chain failed tests
34	- Not Used -
35	- Not Used -
36	- Not Used -
37	- Not Used -
38	At least one CPU did not enter Debug Mode
39	- Not Used -
3A	- Not Used -
3B	- Not Used -
3C	- Not Used -
3D	- Not Used -
3E	- Not Used -
3F	- Not Used -
40	FPGA (10K50) already programmed, Boot code preset for display
41	FPGA (10K50) programming failed
42	Target state change caused interrupt, action routine not called
43	Trace board FPGA/CPLD programming started

44	Zlib uncompress returned an error - image defective
45	Cannot ID trace board - bad JTAG/unknown board
46	JTAG tests failed on AA trace board
47	FPGA (10Kxx/Orca) programming failed
48	7128 Image size incorrect
49	PLD (7128) erase failed
4A	PLD (7128) programming failed
4B	- Not Used -
4C	- Not Used -
4D	- Not Used -
4E	- Not Used -
4F	- Not Used -
50	- Not Used -
51	- Not Used -
52	- Not Used -
53	- Not Used -
54	- Not Used -
55	- Not Used -
56	- Not Used -
57	- Not Used -
58	- Not Used -
59	- Not Used -
5A	- Not Used -
5B	- Not Used -
5C	- Not Used -
5D	- Not Used -
5E	- Not Used -
5F	- Not Used -
60	- Not Used -
61	- Not Used -
62	- Not Used -
63	- Not Used -
64	- Not Used -
65	- Not Used -
66	- Not Used -
67	- Not Used -
68	- Not Used -
69	- Not Used -
6A	- Not Used -
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6B	- Not Used -
6C	- Not Used -
6D	- Not Used -
6E	- Not Used -
6F	- Not Used -
70	Invalid schema index detected in init
71	- Not Used -
72	Write of asset attempted when not dirty or not cached
73	Schema error - must have p3 = 0
74	- Not Used -
75	- Not Used -
76	Error writing dirty cached asset during release CPU
77	- Not Used -
78	- Not Used -
79	- Not Used -
7A	- Not Used -
7B	- Not Used -
7C	- Not Used -
7D	- Not Used -
7E	- Not Used -
7F	- Not Used -
80	No matching IR length for non-auto sniff JTAG
81	- Not Used -
82	All CPUs in target not matching type
83	At least one CPU not in Debug Mode after BreakMode inst
84	Method invalid for generic target type
85	- Not Used -
86	- Not Used -
87	Attempted trace collection while trace running
88	- Not Used -
89	- Not Used -
8A	- Not Used -
8B	- Not Used -
8C	- Not Used -
8D	- Not Used -
8E	- Not Used -
8F	JTAG shifter crash - log has crash code
90	- Not Used -

91	- Not Used -
92	- Not Used -
93	- Not Used -
94	- Not Used -
95	- Not Used -
96	- Not Used -
97	- Not Used -
98	- Not Used -
99	- Not Used -
9A	- Not Used -
9B	- Not Used -
9C	- Not Used -
9D	- Not Used -
9E	- Not Used -
9F	- Not Used -
AA	Waiting for RESET# from target
B0	MAC address not set - must set ECM s/n
B1	MAC address has default value - must set ECM s/n
B2	IP address or netmask or gateway has default - run confice
В3	ECM waiting for IP address assignment from DHCP server
B4	- Not Used -
B5	- Not Used -
B6	Waiting for step to complete
B7	Still waiting for step to complete
B8	The time to wait for the step to complete has expired
B9	Target state management exception
BA	- Not Used -
BB	- Not Used -
ВС	- Not Used -
BD	- Not Used -
BE	- Not Used -
BF	- Not Used -
C0	- Not Used -
C1	- Not Used -
C2	- Not Used -
C3	- Not Used -
C4	- Not Used -
C5	- Not Used -
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C6 - Not Used - C7 - Not Used - C8 - Not Used - C9 - Not Used - CA - Not Used - CB - Not Used - CC - Not Used - CC - Not Used - CC - Not Used - CE - Not Used - CF - Not Used - D0 - Not Used - D1 - Not Used - D2 - Not Used - D4 - Not Used - D5 - Not Used - D6 - Not Used - D7 - Not Used - D8 - Not Used - D9 - Not Used		
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E6 Cannot create Ethernet component E7 Cannot add Ethernet interface to pool E8 Cannot bind IP address to Ethernet controller E9 - Not Used -	E4	Cannot create ECM shifter log
E7 Cannot add Ethernet interface to pool E8 Cannot bind IP address to Ethernet controller E9 - Not Used -	E5	Cannot create RTCS component
E8 Cannot bind IP address to Ethernet controller E9 - Not Used -	E6	Cannot create Ethernet component
E9 - Not Used -	E7	Cannot add Ethernet interface to pool
	E8	Cannot bind IP address to Ethernet controller
EA Cannot create serial (config or debug) task	E9	- Not Used -
	EA	Cannot create serial (config or debug) task
EB Cannot create Ethernet (echo, telnet, WinDB server) task	EB	Cannot create Ethernet (echo, telnet, WinDB server) task
EC - Not Used -	EC	- Not Used -

ED	- Not Used -
EE	Generic ASSERT condition false - use SDS to debug
EF	- Not Used -
F0	- Not Used -
F1	- Not Used -
F2	- Not Used -
F3	- Not Used -
F4	- Not Used -
F5	- Not Used -
F6	- Not Used -
F7	- Not Used -
F8	- Not Used -
F9	- Not Used -
FA	- Not Used -
FB	- Not Used -
FC	- Not Used -
FD	- Not Used -
FE	- Not Used -
FF	- Not Used -

