

Overview

The purpose of the PBD-AJ module is to act as a buffer and provide voltage level shifting between the target debug/ITP port and the ECM-50. This allows the emulator to work with AMD Athlon™64 and Opteron™ processors.

Note: The PBD-AJ supports targets designed for 1-4 processors. The PBD does NOT support targets designed for more than 4 processors, even if only 4 or fewer processors are installed.

Note: PBD-AJ modules are specifically designed for use only with the ECM-50. It is extremely important that the jumper is in the correct position PRIOR to installation. Jumpers set in the wrong position may cause damage to the target system.

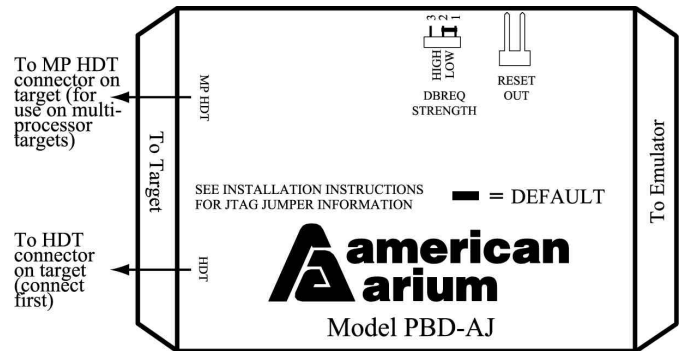
Configure Emulator TCK Current Level Setting

The JTAG current level is set in the SourcePoint™ debugger. Select **Options|Emulator Configuration** from the menu bar. After the dialog box opens, go to the **JTAG** tab.

Set the JTAG current at 7 on the PBD if the TCK termination resistor on the target is a matched end termination. Otherwise, set to 6 or less.

Since the HDT debug port cannot cause a system reset, a Reset Output is provided. Connect the reset adapter (yellow and black twisted pair cable) to Reset Out on the PBD. Connect the other end to the target wherever a reset switch normally would be connected.

Note: The reset adapter is not polarized and does not need to be connected in any particular orientation since it is effectively just a switch closure.



The TCK rate is set in the SourcePoint™ debugger. Select **Options|Emulator Configuration** from the menu bar. After the dialog box opens, go to the **JTAG Clock** tab. Set as appropriate for your target.

On the PBD, set DBREQ strength jumper to High if DBREQ_L has a matched end termination on the target. Otherwise, set it to Low.

If you have any problems or questions, contact Technical Support at 877-508-3970 toll free or 714-731-1661 outside the US or e-mail support@arium.com for assistance. For help outside North America, contact your local distributor.