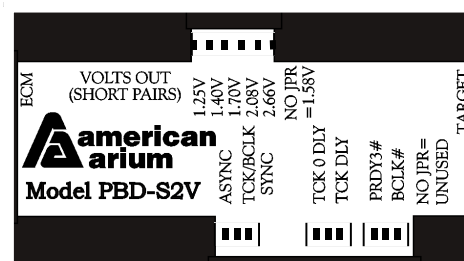


Overview

The purpose of the PBD-S2V module is to act as a buffer and provide voltage level shifting between the target debug/ITP port and the ECM-S2/ECM-20 or TRC-6/TRC-20. This allows the ECM or TRC to work with a number of different processors.

Note: PBD-S2V modules are specifically designed for use only with the ECM-S2/ECM-20 or TRC-6/TRC-20. It is extremely important that the jumpers are in the correct position PRIOR to installation. Jumpers set in the wrong position may cause damage to the target system and the ECM/TRC unit.

Jumper Position	I/O Voltage	
1.25V	1.25 Volts	
1.40V	1.40 Volts	
1.70V	1.70 Volts	
2.08V	2.08 Volts	
2.66V	2.66 Volts	For 2.5V systems
Removed	1.58 Volts	For 1.5V systems



SourcePoint - Configure Emulator TCK Current Level Setting

Set at 4 if TCK term resistor is 39 ohms. Otherwise, set to 3 or less. See below.

For Intel® Pentium® Pro processors:

Move the I/O VOLTS jumper to the 2.66 position and the TCK/BCLK jumper to the ASYNC position. TCK DLY can be set in either position. Select PRDY3# for multi-processor systems.

For Intel Pentium II and other processors:

Determine the I/O voltage of the target processor's debug port and use the appropriate setting.

- If BCLK is available on pin 29 (pin 30 of receptacle version) of the debug port, the TCK/BCLK jumper should be in the SYNC position, with the TCK rate set at 4MHz or less via SourcePoint's emulator configuration settings.
- If pin 30 (pin 29 of receptacle version) of the debug port is used as PRDY3#, set jumper towards PRDY3#. If pin 30 (pin 29 of receptacle version) is used as BCLK# (differential BCLK), set jumper towards BCLK#. Otherwise leave open.
- The TCK DLY setting is system dependent. If jumpering TCK 0 DLY results in intermittent JTAG communications failure although the SourcePoint JTAG Confidence Tests pass, move the jumper to TCK DLY.

If you have any problems or questions, contact Technical Support at 877-508-3970 toll free or 714-731-1661 outside the US or e-mail support@arium.com for assistance.