GETTING STARTED

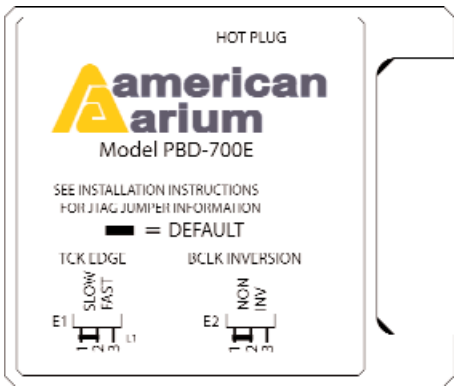
PBD-700E

Overview

The purpose of the PBD-700E is to act as a buffer and provide voltage level shifting between the target debug/ITP port and the ECM-700. The PBD-700E supports the Intellaptop, desktop, and server processors (excluding Intel® Itanium® processors), all of which use the ITP700 debug port specified by Intel.

TCK Edge Rate and BCLK Inversion Jumper Settings

Below are shown jumper settings on the PBD-700E. The default settings are satisfactory for most targets and should not be modified.



TCK Edge Rate Jumper: On most targets, TCK is not end-terminated. The default setting (1-2) of the TCK edge rate jumper provides a controlled (slowed) edge on TCK to prevent ringing and overshoot. This is the default setting.
The fast setting (2-3) should be used only if each branch of TCK on the target is end-terminated. (Such termination differs from Intel's recommendations.)

BCLK Inversion Jumper: For target processors that support bus triggers, the PBD-700E offers two choices of BCLK sampling edge. Bus trigger events can be sampled on the rising edge (NON on the PBD label). Alternatively, bus trigger events can be sampled on the falling edge (INV on the label). Use whichever edge works best. (The wrong edge will tend to cause known occurrences of bus triggers to fail to stop emulation.)

Hot Plug Button

The Hot Plug button is located along the edge of the PBD-700E near the Hot Plug text on the PBD label. The Hot Plug button pre-charges some of the debug interface signals to levels that should prevent unwanted glitches to the target when attaching the PBD-700E to a target when both devices are already powered.

Service and Support

If you have any problems or questions, contact Technical Support at 877-508-3970 toll free or 714-731-1661 outside the US or e-mail support@arium.com for assistance.

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