TAP-YM

Overview

The TAP-YM is the debug port adapter for target systems using the Intel processor code named "Yonah" in a 478-pin uFCPGA package. The adapter provides debug access to target systems that have been designed or built without an XDP debug port or systems where the built-in XDP port is not operating correctly.

Unpacking the Equipment

Carefully remove the equipment from the box and refer to the enclosed packing list to ensure that you have received all items.
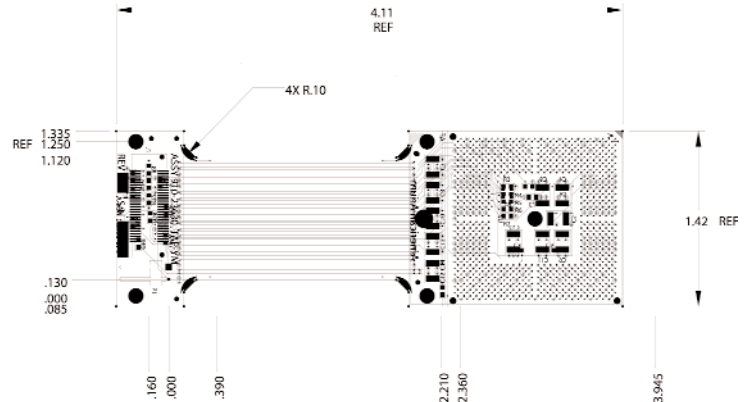
TAP-YM Installation

1. Remove the processor from the target and insert it into the TAP-YM. Ensure that the processor is correctly inserted into the socket on the TAP-YM.

   Note: This socket is not keyed; Pin 1 is identified by the RED inked corner. Disregard the socket pin indicators on the TAP-YM.

2. Carefully insert the TAP-YM (with processor) into the target processor socket; ensure that the TAP-YM is inserted into the target board socket with Pin 1 of the processor oriented to the motherboard socket. The TAP-YM pins have been keyed to assist placement.

3. Even if the system has a built-in debug port, it is possible that a debug signal has not been connected to the processor socket (DBR#).

   If DBR# is not connected to the processor socket, connect the reset adapter (yellow and black twisted pair cable provided with the emulator) between the target system's reset connector (if available) and the PBD-XDP connector labeled, "TARGET RESET OUT".
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   Note: The reset adapter is not polarized and does not need to be connected in any particular orientation since it is effectively just a switch closure. It should be connected to the target system wherever a reset switch normally would be connected.

4. The XDP debug port definition includes ITP-CLK0 and ITP_CLK1. These clocks are not available on the target socket, unlike previous 478-pin device pinouts. If the debugger in use requires these clocks, they may be supplied to the two-pin header (P1) next to the XDP connector. The pin of P1 nearest the XDP connection is ITP-CLK0, and the other pin is ITP_CLK1.

5. Connect the PBD-XDP to the J1 connector of the TAP-YM using the 60-pin connector on the end of the rigid flex cable.

6. Connect the entire PBD-XDP assembly to the ECM-50 via the 68-pin connector.

If you have any problems or questions, contact Technical Support at 877-508-3970 toll free in the US, 714-731-1661 in North America, or via e-mail at support@arium.com. International customers should contact their Arium distributors.