



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
HYDERABAD - 500 085, TELANGANA STATE, INDIA.

CONSOLIDATED MARKS MEMO / CREDIT SHEET



B.Tech ELECTRONICS & COMMUNICATION ENGINEERING
DM No. **C 0867238**

Serial No. 21277029312
Name **B H SATYA VANI**

Hall Ticket No. **148R1A0420**

Year of Admission 2014-2015

Name of the College : BR.CMREC,MEDCHAL
Month & Year of Final Exam : April, 2018



Class Awarded **FIRST CLASS**

No. Sl.	SUBJECT TITLE	2 Ex M	3 Ex M	4 Ex M	5 CREDITS	No. Sl.	SUBJECT TITLE	6 Ex M	7 Ex M	8 Ex M	9 CREDITS
	Maximum Marks in Theory						Maximum Marks in Lab				

I YEAR

1	ENGLISH	21	46	67	4	2	ENGLISH LANGUAGE COMMUNICATION SKILLS LAB	23	45	71	4
3	ENGINEERING WORKSHOP / IT WORKSHOP	21	40	61	4	4	MATHEMATICS - I	17	47	64	6
5	MATHEMATICAL METHODS	21	34	55	6	6	ENGINEERING PHYSICS	18	35	53	6
7	ENGINEERING CHEMISTRY	18	41	59	6	8	COMPUTER PROGRAMMING	15	36	51	6
9	ENGINEERING DRAWING	21	30	51	6	10	COMPUTER PROGRAMMING LAB	23	40	63	4
11	ENGINEERING PHYSICS & ENGINEERING CHEMISTRY LA	21	49	70	4						

I SEMESTER

II YEAR

III SEMESTER

1	MATHEMATICS - II	23	26	49	4	1	PRINCIPLES OF ELECTRICAL ENGINEERING	22	38	60	4
2	PROBABILITY THEORY AND STOCHASTIC PROCESSES	21	31	52	4	2	GENDER SENSITIZATION*	22	47	69	2
3	SWITCHING THEORY AND LOGIC DESIGN	24	44	68	4	3	ELECTRONIC CIRCUIT ANALYSIS	22	30	52	4
4	ELECTRICAL CIRCUITS	24	58	82	4	4	PULSE AND DIGITAL CIRCUITS	21	40	61	4
5	ELECTRONIC DEVICES & CIRCUITS	22	28	50	4	5	ENVIRONMENTAL STUDIES	24	48	72	4
6	SIGNALS AND SYSTEMS	21	44	65	4	6	ELECTROMAGNETIC THEORY AND TRANSMISSION LP	20	32	52	4
7	ELECTRONIC DEVICES AND CIRCUITS LAB	17	41	58	2	7	DIGITAL DESIGN USING VERILOG HDL	22	26	48	4
8	BASIC SIMULATION LAB	21	45	66	2	8	ELECTRICAL TECHNOLOGY LAB	23	41	64	2
						9	ELECTRONIC CIRCUITS AND PULSE CIRCUITS LAB	20	33	53	2

I SEMESTER

II YEAR

III SEMESTER

1	CONTROL SYSTEMS ENGINEERING	20	34	54	4	1	MANAGERIAL ECONOMICS AND FINANCIAL ANALYSIS	19	35	54	4
2	COMPUTER ORGANIZATION AND OPERATING SYSTEMS	19	33	52	4	2	DIGITAL SIGNAL PROCESSING LAB	20	45	65	2
3	ANTENNAS AND WAVE PROPAGATION	22	28	48	4	3	HUMAN VALUES AND PROFESSIONAL ETHICS	20	39	59	4
4	ELECTRONIC MEASUREMENTS AND INSTRUMENTATION	20	30	50	4	4	DIGITAL COMMUNICATIONS	22	29	51	4
5	ANALOG COMMUNICATIONS	18	42	60	4	5	VLS DESIGN	16	34	50	4
6	LINEAR AND DIGITAL IC APPLICATIONS	24	26	50	4	6	MICROPROCESSORS AND MICROCONTROLLERS	20	26	46*	4
7	ANALOG COMMUNICATIONS LAB	17	34	51	2	7	DIGITAL SIGNAL PROCESSING	18	40	58	4
8	IC APPLICATIONS AND HDL SIMULATION LAB	22	46	68	2	8	MICROPROCESSORS AND MICROCONTROLLERS LAB	11	22	33	2

I SEMESTER

IV YEAR

III SEMESTER

1	MANAGEMENT SCIENCE	21	39	60	4	1	SATELLITE COMMUNICATIONS	22	33	55	4
2	EMBEDDED SYSTEMS DESIGN	22	33	55	4	2	INDUSTRY ORIENTED MINI PROJECT	-	47	47	2
3	ADVANCED COMMUNICATION SKILLS LAB	24	45	72	2	3	SEMINAR	47	-	47	2
4	MICROWAVE ENGINEERING AND DIGITAL COMMUNICATIONS	20	46	66	2	4	MAJOR PROJECT	46	143	189	10
5	MICROWAVE ENGINEERING	21	33	54	4	5	COMPREHENSIVE VIVA	-	95	95	2
6	COMPUTER NETWORKS	18	34	50	4	6	TELECOMMUNICATION SWITCHING SYSTEMS AND NETWORKS	21	17	38*	0
7	CELLULAR AND MOBILE COMMUNICATIONS	19	49	68	4	7	WIRELESS COMMUNICATIONS AND NETWORKS	23	29	52	4
8	DIGITAL IMAGE PROCESSING	20	36	56	4						

(IP Project Internal=30, External=150)

Number of Credits registered for 228 Aggregate Marks Secured for best 218



Aggregate Marks Secured : 3388 OUT OF 5250 (64.50%)

Date of Issue : 6 June, 2018

Indicates Absent

(* Credits considered for award of degree)

(Courses registered but not counted for calculation of aggregate)

S. T. Telugu

CONTROLLER OF EXAMINATIONS



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
KUKATPALLY, HYDERABAD - 500 085, TELANGANA STATE, INDIA.

MEMORANDUM OF MARKS



Regd. No.: IM 1672665

Serial No.: 11277015648

Examination: IV B Tech. II Semester (R13) Regular

Branch: ELECTRONICS & COMMUNICATION ENGINEERING

Name: B H SATYA VANI



Hall Ticket No.:

148R1A0420

Month & Year of Exam:

April, 2016

Name of the College:

BR-CMREC, MEDCHAL

S.No.	SUBJECT CODE	S U B J E C T T I T L E	Internal Marks	End Exam	Total Marks	Result	Credits
1	118EK	SATELLITE COMMUNICATIONS	22	33	55	P	4
2	118EW	TELECOMMUNICATION SWITCHING SYSTEMS AND NETWORKS	21	17	38	F	0
3	118FG	WIRELESS COMMUNICATIONS AND NETWORKS	23	29	52	P	4
4	11866	INDUSTRY ORIENTED MINI PROJECT	-	47	47	P	2
5	11870	SEMINAR	47	-	47	P	2
6	11869	MAJOR PROJECT	46	143	189	P	10
7	11865	COMPREHENSIVE VIVA	-	95	95	P	2

SUBJECTS REGISTERED 7

APPEARED 7

PASSED 6

TOTAL:

159

364

523

24

Aggregate (in Words): **** Five Two Three ****



S.Tan kalyan P

Date of Issue: 21 May, 2016

CONTROLLER OF EXAMINATIONS

Instructions:

	Maximum Marks			Minimum Marks for pass	
	Internal	End Exam.	Total of Int. & End	End Exam.	Total of Int. & End
Theory Subjects	25	75	100	29	40
Practical Subjects					
Seminar	50	-	50	-	20
Mini Project	-	50	50	20	20
Project	50	150	200	53	80
Comprehensive Viva	-	100	100	40	40

Note: Any discrepancy must be represented within 15 days from the date mentioned above.

P-PASS F-FAIL AD-ABSENT

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
HYDERABAD - 500 085, TELANGANA STATE, INDIA.

College: BR - CMREC, MEDCHAL

31277015059



SL No. PC 01005858



PROVISIONAL CERTIFICATE

21277029312

HT No: 14BR1AO420

This is to certify that Ms.B H SATYA VANI

D/o B H RADHA KRISHNA

passed B. Tech. ELECTRONICS & COMMUNICATION ENGINEERING degree examination of this University, held in April, 2018 and that she was placed in FIRST CLASS.

She has satisfied all the requirements for the award of the degree.

S. Tan ~~Tan~~ Reddy Hollaiah

Hyderabad - T.S. Controller of Examinations

REGISTRAR

Date: 6 June, 2013

