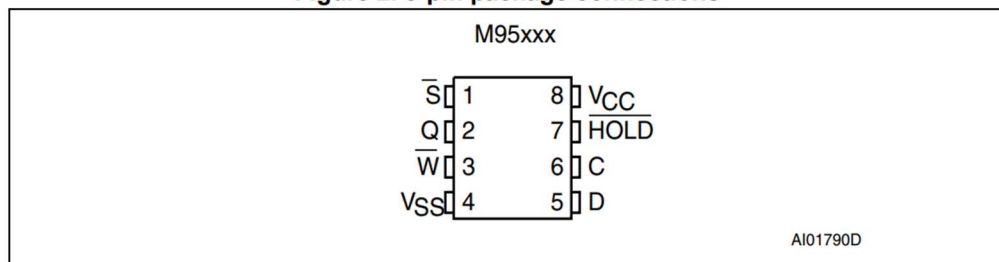


Figure 2. 8-pin package connections



1. See [Package information](#) section for package dimensions and how to identify pin-1.

Table 1. Signal names

Signal name	Description
C	Serial Clock
D	Serial data input
Q	Serial data output
\overline{S}	Chip Select
\overline{W}	Write Protect
\overline{HOLD}	Hold
V_{CC}	Supply voltage
V_{SS}	Ground