深圳市金逸晨电子有限公司

LCD MODULE

MODULE NO.:

$\underline{GM12864\text{-}01A} \text{ (ziku)}$

Customer:		
Approved By(核准):		

深圳市金逸晨电子有限公司								
Approved By(核准): Checked By(审核): Prepared By								

Version: V1.0 2 GM12864-01A (ziku)

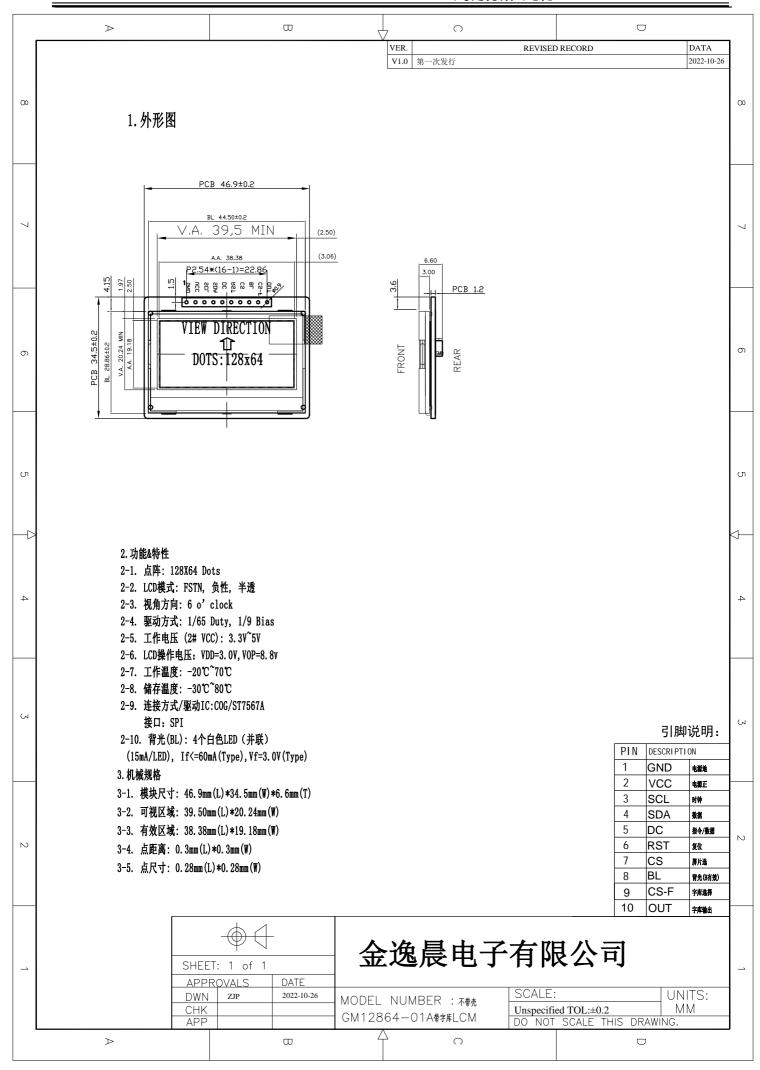
RECORDS OF REVISION (修订记录)

Dout N	Dovicion	Revision Content	Revised on
Part Number (产品型号)	Revision (版本)	(修订内容)	(修订日期)
GM12864-01A (ziku)	V1.0	第一次发行	2022-10-26
<u> </u>			-

<u>GM12864-01A</u> (ziku) <u>Version: V1.0</u> 3

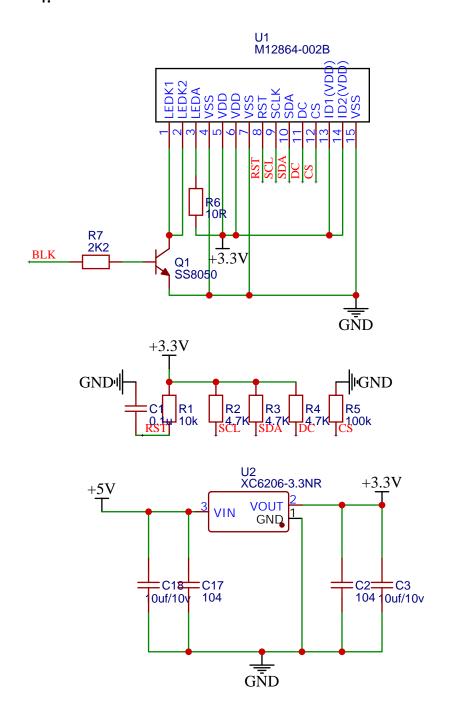
CONTENTS

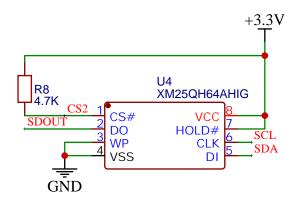
1. 外形图	4
2. 功能&特性	
3. 机械规格	4
4. 原理图	5
5. 引脚说明	5
6. 电气特性	6
6.1 DC电气特性	6
6.2 AC电气特性	7
7. 指令表	8

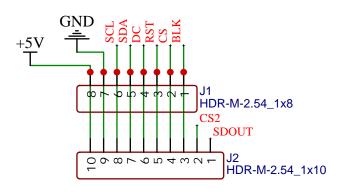


GM12864-01A (ziku) Version: V1.0 5

4. 原理图:







注意: 电路及元件值仅作参考

<u>GM12864-01A</u> (ziku) Version: V1.0

6. 电气特性

6-1 DC CHARACTERISTICS

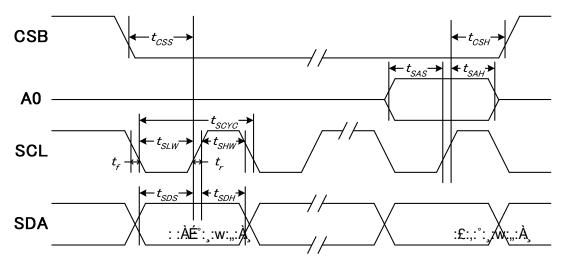
VSS=0V; Tamb = -30°C to +85°C; unless otherwise specified.

Item	Cumbal	Condition			Rating	Unit	Applicable	
item	Symbol	Co	naition	Min.	Тур.	Max.	Unit	Pin
Operating Voltage (1)	VDD1			1.7		3.465	V	VDD1
Operating Voltage (2)	VDD2			2.4		3.465	V	VDD2
Operating Voltage (3)	VDD3			2.4		3.465	V	VDD3
Input High-level Voltage	V _{IHC}			0.7 x VDD1		VDD1	V	MPU Interface
Input Low-level Voltage	V _{ILC}			VSS1		0.3 x VDD1	V	MPU Interface
Output High-level Voltage	V _{OHC}	I _{OUT} =1m	A, VDD1=1.8V	0.8 x VDD1		VDD1	V	D[7:0]
Output Low-level Voltage	V _{OLC}	I _{OUT} =-1m	nA, VDD1=1.8V	VSS1		0.2 x VDD1	V	D[7:0]
Input Leakage Current	lu			-1.0		1.0	μA	MPU Interface
Output Leakage Current	I _{LO}			-3.0		3.0	μA	MPU Interface
Liquid Crystal Driver ON	R _{ON}	Ta=25°C	Vop=8.5V, △ V=0.85V		0.6	0.8	ΚΩ	COMx
Resistance	NON	1a=20 C	VG=1.9V, ∆V=0.19V		1.3	1.5	ΚΩ	SEGx
Frame Frequency	FR	Duty=1/65, Vop=8.5V Ta = 25°C		70	75	80	Hz	

GM12864-01A (ziku) Version: V1.0 7

6-2 AC 电气特性

System Bus Timing for 4-Line Serial Interface



 $(VDD1 = 3.3V, Ta = 25^{\circ}C)$

Item	Signal	Symbol	Condition	Min.	Max.	Unit
Serial clock period		tSCYC		50	_	
SCLK "H" pulse width	SCLK	tSHW		25	_	
SCLK "L" pulse width		tSLW		25	_	
Address setup time	A0	tSAS		20	_	
Address hold time	AU	tSAH		10	_	ns
Data setup time	SDA	tSDS		20	_	
Data hold time	SDA	tSDH		10	_	
CSB-SCLK time	CSB	tCSS		20	_	
CSB-SCLK time	CSB	tCSH		40	_	

$(VDD1 = 2.8V , Ta = 25^{\circ}C)$

Item	Signal	Symbol	Condition	Min.	Max.	Unit
Serial clock period		tSCYC		100	_	
SCLK "H" pulse width	SCLK	tSHW		50	_	
SCLK "L" pulse width		tSLW		50	_	
Address setup time	A0	tSAS		30	_	
Address hold time	AU	tSAH		20	_	ns
Data setup time	SDA	tSDS		30	_	
Data hold time	SDA	tSDH		20	_	
CSB-SCLK time	CSB	tCSS		30	_	
CSB-SCLK time	CSB	tCSH		60	_	

<u>GM12864-01A</u> (ziku) <u>Version: V1.0</u> 8

7. 指令表 INSTRUCTION TABLE

INOTRICTION	4.0	R/W	COMMAND BYTE								DECORIDEION
INSTRUCTION	A0	(RWR)	D7	D6	D5	D4	D3	D2	D1	D0	DESCRIPTION
(1) Display ON/OFF	0	0	1	0	1	0	1	1	1	D	D=1, display ON D=0, display OFF
(2) Set Start Line	0	0	0	1	S5	S4	S3	S2	S1	S0	Set display start line
(3) Set Page Address	0	0	1	0	1	1	Y3	Y2	Y1	Y0	Set page address
(4)	0	0	0	0	0	1	X7	X6	X5	X4	Set column address (MSB)
Set Column Address	0	0	0	0	0	0	Х3	X2	X1	X0	Set column address (LSB)
(5) Read Status	0	1	0	MX	D	RST	0	0	0	0	Read IC Status
(6) Write Data	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write display data to RAM
(7) Read Data	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read display data from RAM
(8) SEG Direction	0	0	1	0	1	0	0	0	0	MX	Set scan direction of SEG MX=1, reverse direction MX=0, normal direction
(9) Inverse Display	0	0	1	0	1	0	0	1	1	INV	INV =1, inverse display INV =0, normal display
(10) All Pixel ON	0	0	1	0	1	0	0	1	0	AP	AP=1, set all pixel ON AP=0, normal display
(11) Bias Select	0	0	1	0	1	0	0	0	1	BS	Select bias setting 0=1/9; 1=1/7 (at 1/65 duty)
(12) Read-modify-Write	0	0	1	1	1	0	0	0	0	0	Column address increment: Read:+0 , Write:+1
(13) END	0	0	1	1	1	0	1	1	1	0	Exit Read-modify-Write mode
(14) RESET	0	0	1	1	1	0	0	0	1	0	Software reset
(15) COM Direction	0	0	1	1	0	0	MY	-	ı	-	Set output direction of COM MY=1, reverse direction MY=0, normal direction
(16) Power Control	0	0	0	0	1	0	1	VB	VR	VF	Control built-in power circuit ON/OFF
(17) Regulation Ratio	0	0	0	0	1	0	0	RR2	RR1	RR0	Select regulation resistor ratio
(18) Set EV	0	0	1	0	0	0	0	0	0	1	Double command!! Set
(10) Set LV	0	0	0	0	EV5	EV4	EV3	EV2	EV1	EV0	electronic volume (EV) level
	0	0	1	1	1	1	1	0	0	0	Double command!!
(19) Set Booster	0	0	0	0	0	0	0	0	0	BL	Set booster level: BL=0: 4X BL=1: 5X
(20) Power Save	0	0	Compound Command				Display OFF + All Pixel ON				
(21) NOP	0	0	1	1	1	0	0	0	1	1	No operation
(22) Test	0	0	1	1	1	1	1	1	1	-	Do NOT use. Reserved for testing.

Note: Symbol "-" means this bit can be "H" or "L".