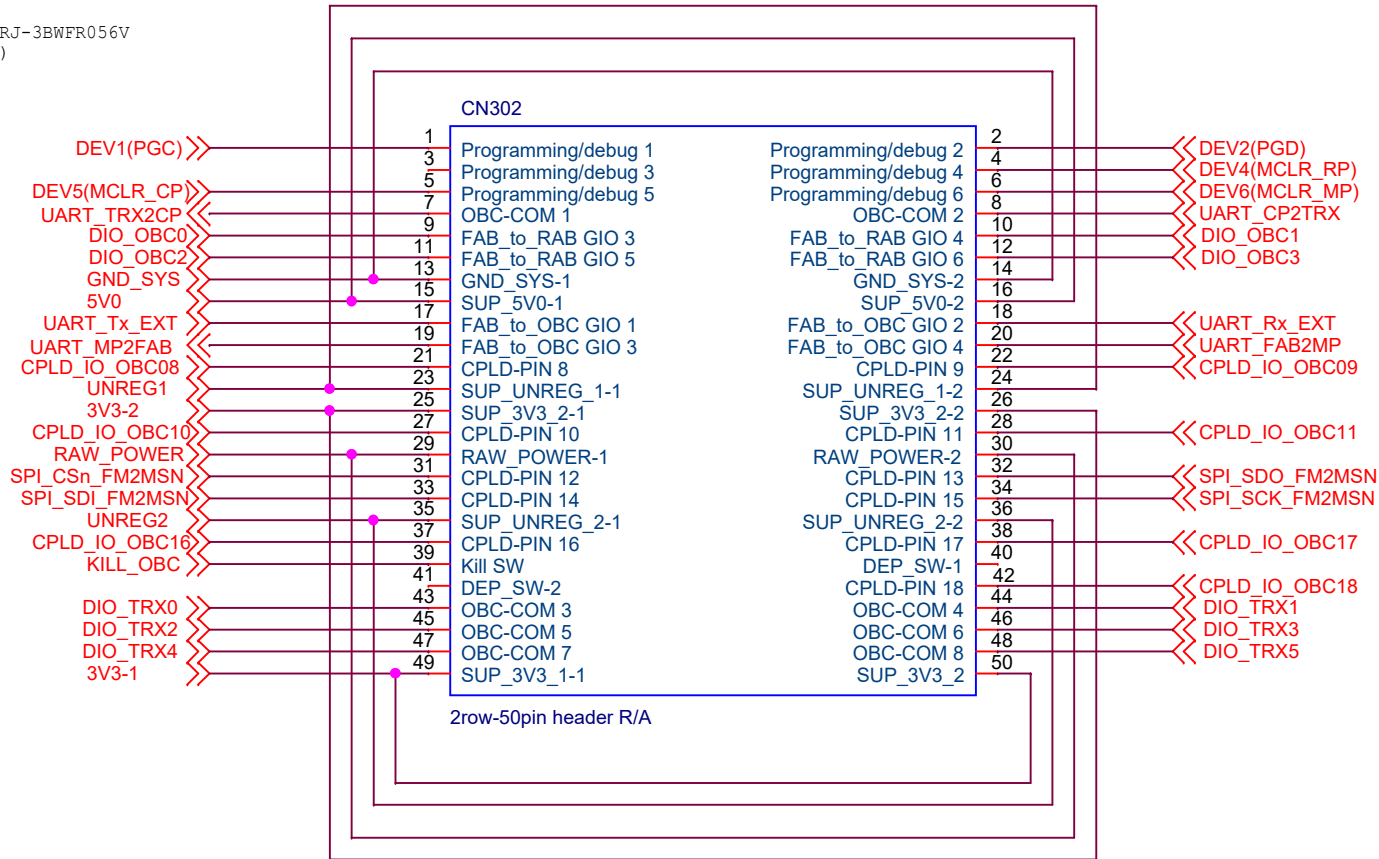


4

v0.30 2020/10/14
Insert R126(390[Ohm]) resistor
Insert R127(100[Ohm]) resistor
R36,R39 0.033-> 0.02 (OCP3A-> 5A)
Q1,Q3,Q5,Q7,Q8,Q10,Q11,Q13,Q14,Q16 (SSM6K411TU) -> SSM6K514NU
Change the input pin of secondary clock from pin29 to pin30
C96,C97,C98 (3300pF) Add
R114 F0.15 -> F0.33

5

v0.31 2020/10/19
Title: Birds3 -> Birds5
R28,R42 (ERJ-6BWFR025V) -> ERJ-3BWFR056V
R128,R129 add (ERJ-3BWFR056V)



1

v0.17 2018/02/09 DIO_TRX4 U26.21- >U26.23

2

v0.20 2018/05/24
U14,U26 PIC16F1787->PIC16F1789, X1,X2,X3 8MHz -> 16MHz
U23 UART pin assinnment swap MP and TRX (HW to TRX)
R123-R125 Add
U21 Flash control half -> full duplex w/ RE5 MP pin assign
R25,26 0.033 -> 0.025, DIO_OBC0 assignment RD4-> RC2(Analog)
R107 1->0.5, R114 1-> 0.15

3

v0.21 2018/10/02
R36,R39 0.033-> 0.025 (OCP3A-> 4A)

Title			
Birds5_OBC(BP connect)			
Size A	Document Number		Rev 0.5
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Date:	Tuesday, October 20, 2020		Sheet 1 of 4

